

**\$3.25**  
MM70924

# Electronics Today

June 1985

Canada's Magazine for Electronics & Computing Enthusiasts

## Satellite Reception Buy or build

**Anti Phaser Project**  
Music effects

**Electronics From  
The Start**  
Beginner's guide

**Distortion Meter  
Project**  
Measuring quality

**Computer Review**  
Commodore 16



**Z80 Inside:  
Design Guide**



0

57438 70924

# The **BEST MK II** Super PC and XT Compatibility

New Low Prices



## The BEST Mark II

As described above using 41256 RAM chips. Two 360K DS, DD disk drives, RS232 and parallel port, Real Time Clock, 7 slots, Phoenix BIOS, Colour Video and Disk Controller Cards, Keyboard and much, much more

**With 256K \$1695**

**With 512K \$1795**

## The BEST 10 Meg Hard Drive

As the BEST Mark II but with 10 Meg Hard drive (supplied with one Floppy Drive. For second drive add \$200).

**With 256K \$2695**

**With 512K \$2795**

## The BEST 20 Meg Hard Drive

As the BEST Mark II but with 20 Meg Hard drive (supplied with one Floppy Drive. For second drive add \$200).

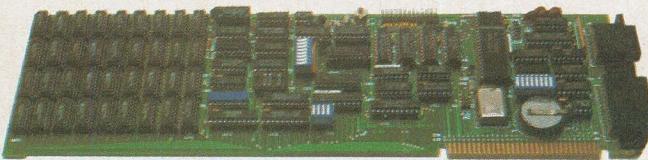
**With 256K \$2995**

**With 512K \$3095**

## Also the Basic BEST . . . . . \$1595.00

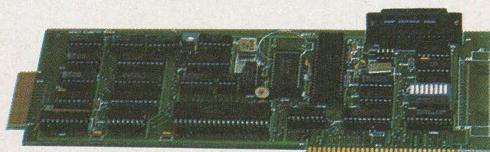
As the BEST Mark II described above but without Parallel Port or Real Time Clock

### The Best 256K PENTARAM BEST SELLER



**\$359.00** with 256K RAM, Real Time Clock Parallel, serial and Game Port (512K now available)

### FLOPPY CONTROLLER BOARD AND OPTIONS



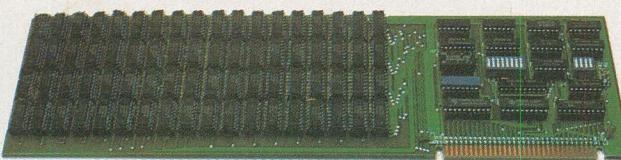
**\$129.00** With Real Time Clock and Serial Port only **\$189.00**

EPROM Programmer **\$99.00**

(with ZIF Socket Adapter **\$159.00**)

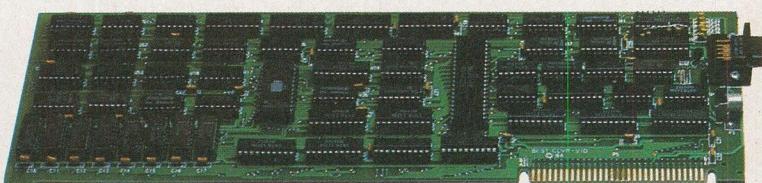
Parallel/Game Port **\$79.00** (cables Extra)

### BEST 512K RAM BOARD



**SPECIAL: \$365.00!** with 512K. With 64K **\$179.00**

### Colour Graphics Video Board



**\$179.00** (Composite and RGB Output)

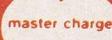
Quantity, Students and Teacher discount's available.

### Look what you get as standard!

- Uses 8088 microprocessor.
- New Feature, 256K RAM as standard.
- New Feature. Comes with the latest 41256 RAM chips.
- Expandable up to 512K and more on main board using 41256 RAM chips.
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- New Feature. Fitted with 150W power supply so system can be upgraded to a hard disk without changing power supply.
- New Feature. Flip-Top case.
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- Complete with the Phoenix BIOS, identical to that used by many of the large US companies manufacturing IBM compatible computers.
- Comes with two Slimline DS, DD 5 1/4" 360K Disk Drives.
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Tape Drive option backup suitable for all systems ..... **\$Call**

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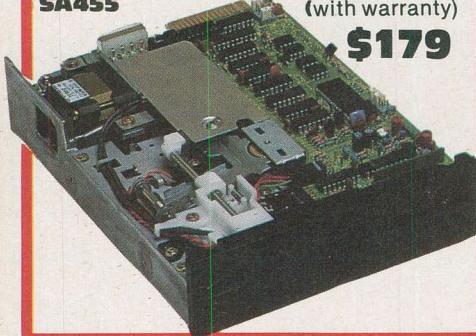
### IBM COMPATIBLE KEYBOARDS

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### BEST IBM Compatible Modem

- Plugs into motherboard
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### 300 BAUD MODEM for your APPLE

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Auto Answer, Dial, Direct, Con. Plugs right into your Apple, comes with software.

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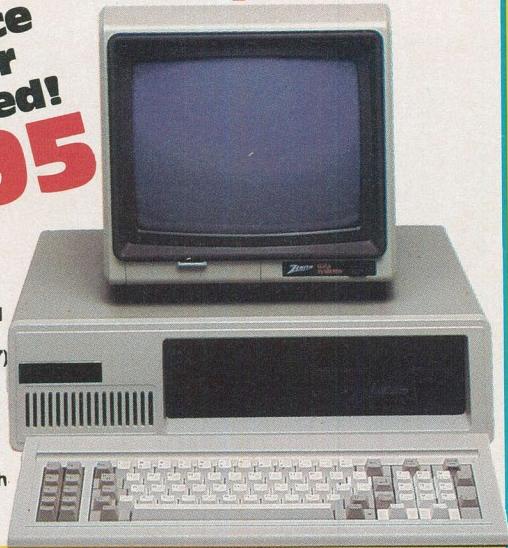
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## 8088 Computer

**The Price Barrier Smashed!**  
**\$995**

**Includes:**

- 8088 motherboard with 256K and all parts (except 8087)
- Phoenix Bios
- One 360K DS, DD disk drive and controller
- Power Supply with fan
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All you need extra is a keyboard, video board and monitor.  
8088 Motherboard only with 64K, fully assembled and tested, no Bios (one blank 2764). Expandable to 1Meg on-board memory. Flow soldered. Provision for 8087 . . . . . **\$299.00**

## 6502 Systems

### The Famous 6502 Board



- Assembled and fully tested (flow soldered)
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- System comes with blank EPROM'S on board, does not come with any proprietary software or Basic . . . . . **\$289.00**

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**Peripheral Interface Card** . . . . . **\$139.00**

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**Clock/Calendar Option** . . . . . **\$29.00**

With battery backup.

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**128K RAM Card with 64K** . . . . . **\$99.00**

**128K RAM Card with 128K** . . . . . **\$139.00**

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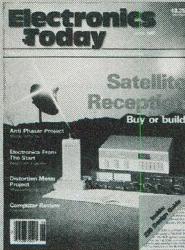
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## Our Cover

Satellite-to-home TV reception has started a growing industry; a guide appears on page 32. Equipment courtesy of Vexus Telecommunications, Mississauga; photo by Bill Markwick.



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**Director of Production:** Erik Blomkwist  
**Creative Manager:** Ann Rodrigues  
**Production:** Douglas Goddard  
Naznin Sunderji  
Sandra Hemburrow  
Lisa Salvatori  
Kenneth Marskell  
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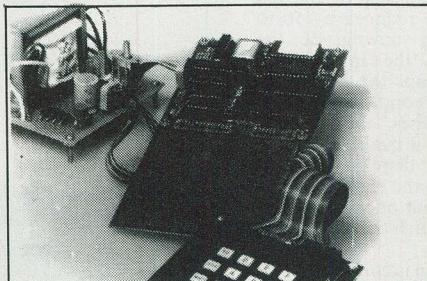
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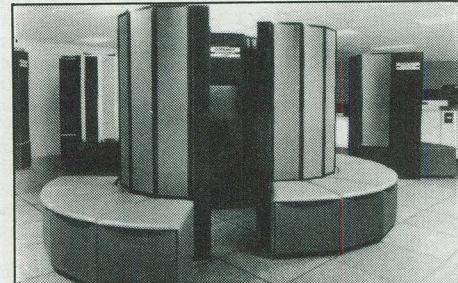
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## Editorial Queries

Written queries can only be answered when accompanied by a self-addressed, stamped envelope. These must relate to recent articles and not involve the staff in any research. Mark such letter Electronics Today Query. *We cannot answer telephone queries.*

## Binders

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## Back Issues and Photocopies

Previous issues of Electronics Today Canada are available direct from our office for \$4.00 each; please specify by month, not by feature you require. See order card for issue available.

We can supply photocopies of any article published in Electronics Today Canada; the charge is \$2.00 per article, regardless of length. Please specify both issue and article.

## Component Notation and Units

We normally specify components using an international standard. Many readers will be unfamiliar with this but it's simple, less likely to lead to error and will be widely used everywhere sooner or later. Electronics Today has opted for sooner!

Firstly decimal points are dropped and substituted with the multiplier: thus 4.7uF is written 4u7. Capacitors also use the multiplier nano (one nanofarad is 1000pF). Thus 0.1 uF is 100nF, 5600pF is 5n6. Other examples are 5.6pF = 5p6 and 0.5pF = 0p5.

Resistors are treated similarly: 1.8Mohms is 1M8, 56kohms is the same, 4.7kohms is 4k7, 100ohms is 100R and 5.6ohms is 5R6.

## PCB Suppliers

Electronics Today magazine does NOT supply PCBs or kits but we do issue manufacturing permits for companies to manufacture boards and kits to our designs. Contact the following companies when ordering boards.

Please note we do not keep track of what is available from who so please don't contact us for information on PCBs and kits. Similarly do not ask PCB suppliers for help with projects.

K.S.K. Associates, P.O. Box 266, Milton, Ont. L9T 4N9.

Wentworth Electronics, R.R. No. 1 Waterdown, Ont. L0R 2H0.

Danocinths Inc., P.O. Box 261, Westland MI 48185, USA.

Arkon Electronics Ltd., 409 Queen Street W., Toronto, Ont., M5V 2A5.

Beyer & Martin Electronic Ltd., 2 Jodi Ave., Unit C, Downsview, Ontario M3N 1H1.

Spectrum Electronics, 14 Knightswood Crescent, Brantford, Ontario N3R 7E6.

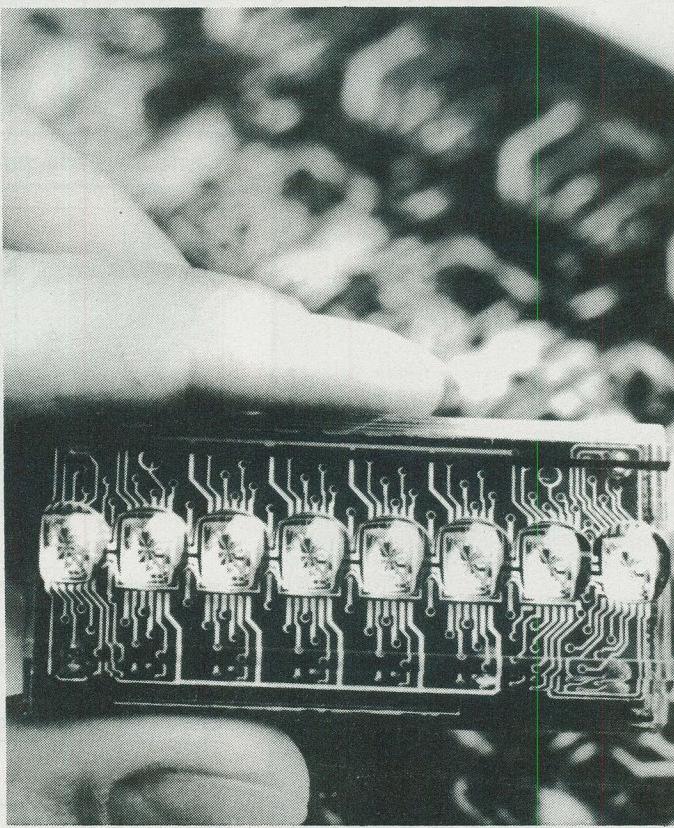
## Intelligent Programmable LEDs

Siemens Electric of Mississauga Ontario recently revealed a new intelligent programmable LED display capable of such functions as underlining and adjustable display intensity.

Now the intensity of the red emission can be reduced by 50 to 75% so as to downgrade the significance of the information displayed or adapt the brightness to the ambient conditions. When the display flashes, the memory contents remain intact. In contrast to conventional displays with an external flasher, the characters only need to be input once and not in the rhythm at which the LEDs light up.

The PD 2816 is comprised of a character generator (ROM), multiplexer, timer logic and driver logic, and also the CMOS circuit for the supplementary programmable functions. The LEDs are configured in eight 18-segment units only 4.1mm high. Any number of eight-element displays can be cascaded to form long lines of text.

For more information contact: Siemens Electric Ltd., 1180 Courtney Park Dr., Mississauga Ont., L5T 1P2.



## Cellular Service Areas Announced

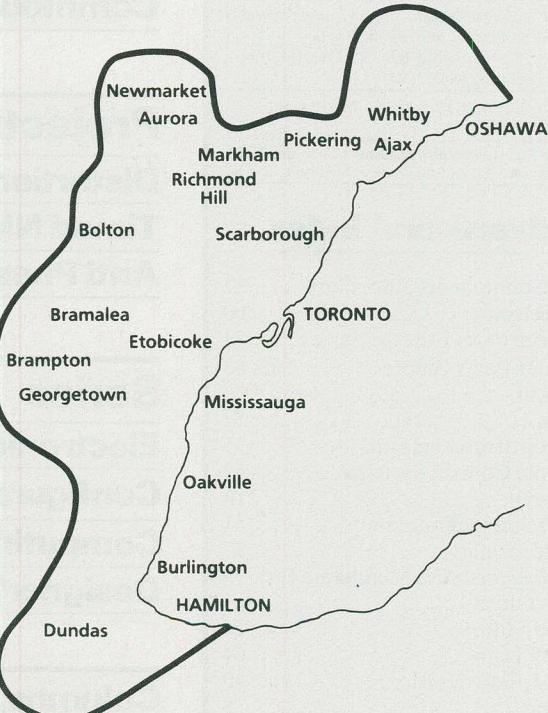
Cantel, the company selected by the D.O.T. to provide cellular phone service to 23 Canadian markets, recently announced two areas of initial service.

Commencing July 1st, continuous coverage will begin in and between Toronto, Hamilton, Dundas, Burlington, Oakville, Mississauga, Georgetown, Brampton, Etobicoke, Bramalea, Bolton, Scarborough, Richmond Hill, Markham, Newmarket, Aurora, Pickering, Whitby, Ajax, and Oshawa.

Also commencing on July 1st, the Montreal area service will include: Montreal, L'Assomption, Vercheres, St. Hyacinthe, Chambly, La Prairie, St. Jean, Beauharnois, Laval, Hudson, Ste. Therese, Terrebonne, St. Jerome and Ste. Adele.

If you would like more detailed information on cellular phone service contact: Cantel Inc., 20 Queen St. W., Suite 1204, P.O. Box 70, Toronto Ont., M5H 3R3.

**The popular ILP power amplifier modules are now available in standard 19 inch racks for professional use. They're available in bipolar or MOSFET versions, and feature fast slew rate, low noise and low distortion. Various power levels are available up to 180 watts per channel. They're from EDG Electronic Distributors, 3950 Chesswood Dr., Downsview, Ont. M3J 2W6 (416) 636-9404.**



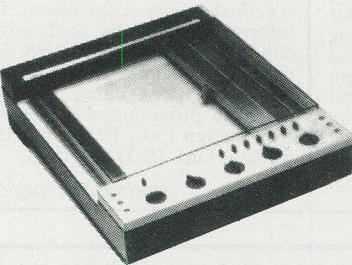
### Flat Bed Recorder

No, this won't check for saggy springs in your mattress, but it will plot XY or y/t charts quite nicely.

The new PL 4 from J.J. Lloyd is portable and is driven by a fully integrated quartz-controlled chart drive. Both inputs are fully floating and have a constant input impedance of greater than 1Mohm, as well each amplifier is fitted with an 18-range attenuator providing a maximum sensitivity of 25uV/mm.

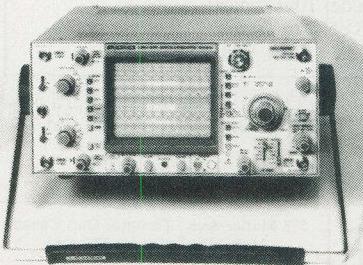
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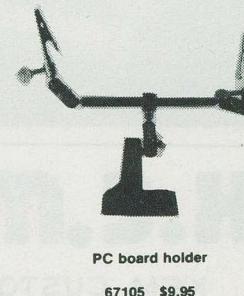
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*continued on page 12*

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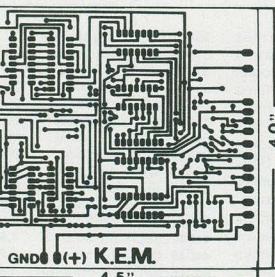


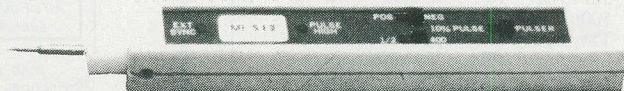
Fig. 1 Sample artwork (Solder side)

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# Electronics From The Start

**A new series for the complete beginner. This month we look at the basic theories and tools of the trade.**

**By Keith Brindley**

INTRODUCING THE START of our brand new series on electronics; month-by-month we take you through all the major principles and concepts you need to know, in a fascinating and practical way. You'll find out about electronic components: what they are; what they do; how they're made and why this affects their operation. You'll see the laws and principles which govern electronic circuits, right before your own eyes — not in a boring old text-book manner, but in a novel and interesting way, helping you to remember the things you need to know. You'll be able to build some of the circuits we show you, too. We'll give you instructions and details of how to do-it-yourself, so you'll have a permanent reminder of what you learn.

## Tools of the Trade

The first tool you need is a pair of **cutters**. There are many types but the most useful sorts are side cutters. Generally speaking, buy a small pair — the larger ones are OK for cutting thick wires but not for much else. In electronics most wires you want to

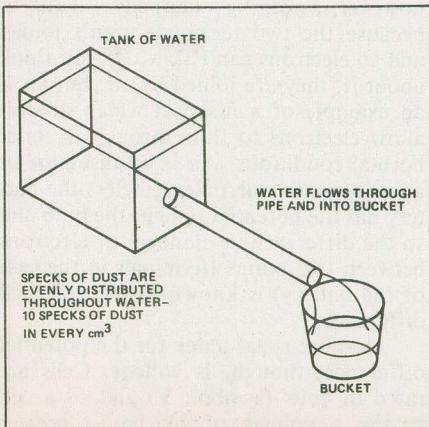


Fig. 1. Water flowing in a pipe is like electricity in a wire.

Electronics Today June 1985

cut are thin, so the smaller the cutters, the better.

You can expect to pay from \$5 to \$25 for a good quality pair, so look around and decide how much you want to spend. If you buy a small pair (as we recommend) don't use them for cutting thick wires, or you'll find they don't last very long, and you'll have wasted your money.

You can use side cutters for stripping the insulation from wires, too, if you're careful. But if you feel rich, a proper **wire stripping tool** makes the job much easier. There are many different types of wire

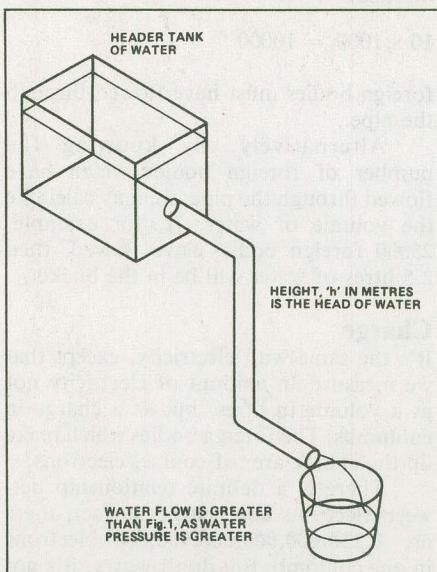


Fig. 2. A header tank's potential energy forces the water with a higher pressure.

strippers ranging in price from \$3 to \$40. Of course, if you don't mind paying large dentist's bills you can always use your teeth — but don't say we said so.

A small pair of **pliers** is useful for lightly gripping components etc. Flat-nosed or, better still snipe nosed varieties are preferable, costing between \$5 to \$25. Like side cutters, however, these are not meant for heavy-duty engineering work.

The last essential tool we're going to look at now, is a **soldering iron**. Soldering is the process used to connect electronic components together, in a good permanent joint. We'll look at how to solder in a future article, so don't worry about it now. Soldering irons range in price from \$5 to \$30, but the price doesn't necessarily reflect how useful they are in electronics. This is because irons used in electronics

generally should be of a low power rating. Power rating will be specified on the iron or its packaging and a useful iron will be around 25 watts.

It is possible to get soldering irons rated up to and over 100 watts, but these are of no use to you — stick with an iron with a power rating of no more than 25 watts.

## Ideas About Electricity

Electricity is a funny thing. Even though we know how to use it, how to make it work for us, to amplify, to switch, to control, to create light or heat (you'll find out about all of these aspects of electricity over the coming months of this series) we can still only guess at what it is. It is actually impossible to see electricity: we only see what it does!

The truth of the matter is that we may only hypothesize about electricity. Fortunately, the hypothesis can be seen to

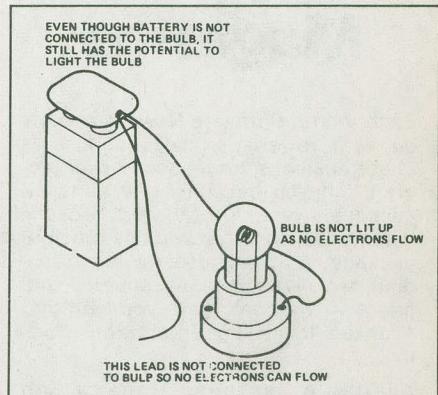


Fig. 3a. A battery forces electrons around a circuit.

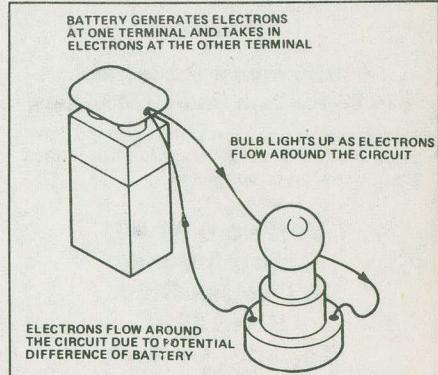


Fig. 3b. Even when the battery is disconnected and electrons don't flow, the battery still has a potential difference.

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stand in all of the aspects of electricity and electronics we are likely to look at, so to all intents and purposes the hypothesis we have is absolute. This means we can build up ideas about electricity and be fairly sure they are correct.

With that out of the way, let's move on to the first idea: that electricity is a flow of electrons. To put it another way, any flow of electrons is electricity. If we can measure the electricity, we must therefore be able to say how many electrons were in the flow. Think of an analogy, say, the flow of water through a pipe (Fig. 1). The water has an evenly distributed number of foreign bodies in it. Let's say there are ten foreign bodies in every cm<sup>3</sup> of water.

Now, if 1 litre of water pours out of the end of the pipe into the bucket shown in Fig. 1, we can calculate the number of foreign bodies which have flowed through the pipe. There are 1000 cm<sup>3</sup> of water in a litre, so:

$$10 \times 1000 = 10000$$

foreign bodies must have flowed through the pipe.

Alternatively, by knowing the number of foreign bodies which have flowed through the pipe, we may calculate the volume of water. If, for example, 25000 foreign bodies have flowed, then 2.5 litres of water will be in the bucket.

### **Charge**

It's the same with electricity, except that we measure an amount of electricity not as a volume in litres, but as a charge in **coulombs**. The foreign bodies which make up the charge are, of course, electrons.

There is a definite relationship between electrons and charge: in fact, there are **6,250,000,000,000,000,000** electrons in one coulomb. But don't worry, it's not a number you have to remember — you don't even have to think about electrons and coulombs because the concept of electricity, as far as we're concerned, is not about electron flow, or volumes of electrons, but about flow rate and flow pressure. And as you'll now see, electricity flow rate and pressure are given their own names which don't even refer to electrons or coulombs.

Going back to the water and pipe analogy, flow rate would be measured as a volume of water which flowed through the pipe during a defined period of time, say 10 litres in one minute, 1000 litres in one hour or one litre in one second.

With electricity, flow rate is measured in a similar way, as a volume which flows past a point, during a defined period of time, except that volume is, of course, in coulombs. So, we could say that a flow rate of electricity is 10 coulombs in one minute, 1000 coulombs

in one hour or one coulomb in one second.

We could say that, but we don't! Instead, in electricity, flow rate is called **current** (and given the symbol **I**, when drawn in a diagram). Electric current is measured in **amperes** (shortened to **amps**, or even further shortened to the unit **A**), where one amp is defined as a quantity of one coulomb passing a point in one second. Instead of saying 10 coulombs in one minute we would therefore say:

$$10/60 \text{ coulombs per second} = 0.166\bar{A}$$

Similarly, instead of a flow rate of 1000 coulombs in one hour, we say:

$$1000/3600 \text{ coulombs per second} = 0.3\bar{A}$$

The other important thing we need to know about electricity is flow pressure. Back to our analogy with the water and the pipe, Fig. 2 shows a header tank of water at a height, **h**, in metres, is the head. The effect of gravity pushes down the water in the header tank, forming a flow pressure, forcing the water out of the pipe. It is the energy contained in the water in the header tank due to its higher position — its **potential energy** — which defines the water pressure.

With electricity the flow pressure is defined by the difference in numbers of electrons between two points. We say that this is a **potential difference**, partly because the difference depends on the positions of the points and how many electrons potentially exist. Another reason for the name **potential difference** comes from the early days in the pioneering of electricity, when the scientists of the day were making the first batteries. Fig. 3a shows the basic operating principle of a battery, which simply generates electrons at one terminal and takes in electrons at the other terminal. Fig. 3a also shows how the electrons from the battery flow around the circuit, lighting the bulb.

Under the conditions of Fig. 3b, however, nothing happens. This is because the two terminals aren't joined and so electrons can't flow. (If you think about it, they are joined by air, but air is an example of a material which doesn't allow electrons to flow through it under normal conditions. Air is an **insulator** or an **non-conductor**.) Nevertheless the battery has the potential to light the bulb and so the difference in numbers of electrons between two points (terminals in the case of the battery) is known as the potential difference.

A more usual name for the potential difference, though, is **voltage**. Cells are rated in volts (symbol: **V**) and so a cell having a voltage of 3V has a greater potential difference than a cell having a voltage of 2V. The higher the voltage, the

harder the cell can force electrons around a circuit. Voltage is simply a way of expressing electric pushing power.

## Relationships

You'd be right in thinking that there must be some form of relationship between this pushing power in volts and the rate of electron flow in amps. After all, the higher the voltage, the more pushing power the electrons have behind them and so the faster they should flow. The relationship was first discovered by a scientist called Ohm, and so is commonly known as **Ohm's Law**. It may be summarized by the expression :

$$V/I = \text{constant}$$

where the constant depends on the substance through which the current flows and the voltage is applied across. Fig. 4 gives an example of a substance which is connected to a cell. The cell has a voltage of 2V, so the voltage applied across the substance is also 2V. The current through the substance is, in this case, 0.4A. This means, from Ohm's law, that the constant for the substance is:

$$2/0.4 = 5$$

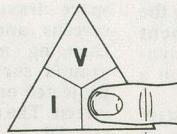
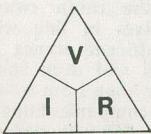
The constant is commonly called the substance's **resistance** (because it is a measure of the amount the substance resists the flow of current through it) and is given the unit:  $\Omega$  (pronounced ohm). In

### Ohm's Law

A simple method to help you remember Ohm's law: remember a triangle, divided into three parts. Voltage ( $V$ ) is at the top. Current ( $I$ ) and resistance ( $R$ ) are at the bottom. It doesn't matter which way around the  $R$  and  $I$  are — the important thing to remember is the  $V$  on top.

If you have any two of the constants, cover up the missing one with your finger and the formula for calculating the missing one will appear.

Say you know the voltage across a resistor and the current through it, but you need to know the resistance itself. Simply cover the letter  $R$  with your finger:



and the formula to calculate the resistance is  $V/I$ .

our example of Fig. 4, the resistance is  $5\Omega$ . Different substances may have different resistances and may therefore change the current flowing.

This is a vitally important concept, probably the most important one in the whole world of electronics, and yet is often misunderstood. Even if it is not misunderstood, it is often misinterpreted. Let's recap it and see what it all means:

*If a voltage ( $V$  in volts) is applied across a resistance ( $R$  in ohms), a current ( $I$  in amps) will flow. The voltage, current and resistance are related by expression (1) as  $V/I = R$ .*

The importance of this is that the current which flows depends entirely on the values of the resistance and the voltage. The value of the current may be determined simply by rearranging expression 1, so that it gives (2):

$$V/R = I$$

So, a voltage of say 10V, applied across a resistance of 20 ohms produces a current of:

$$10/20 = 0.5A$$

Similarly, if we have a resistance, and a current is made to flow through it, then a voltage is produced across it. The value of the voltage may be determined by again rearranging expression (1), so that it now gives (3):

$$V = IR$$

Thus, a current of 1A flowing through a resistance of 5 ohms produces a voltage of  $1 \times 5 = 5V$  across the resistance.

These three expressions are the most common ones you'll meet in electronics, so look at 'em, read 'em, use 'em, and learn 'em, inwardly digest 'em — just don't forget 'em. Right? Right.

And another thing. See the way we've said throughout, that a voltage is applied or produced *across* a resistance. Similarly a current flows *through* a resistance. Just remember that a voltage is *across*: a voltage does not flow through. Likewise, a current flows through: it is not across. There is no such thing as a flow of voltage through a resistance, or a current across a resistance.

## Electronic Components

The fact that different resistances produce different currents if a voltage is applied across them, or produce different voltages if a current is applied through them, is one of the most useful facts in electronics.

In electronics, an amp of current is a very large quantity — usually we only use much smaller currents, say, a thousandth

or so of an amp. Sometimes we even use currents smaller than this, as little as a millionth of an amp! Similarly, we sometimes need only small voltages, too.

Resistances are extremely useful in these cases, because they can be used to reduce the current flow or the voltage produce across them, due to the effects of Ohm's law. We'll look at ways and means of doing this later on in the series. All we need to know for now is that resistances are used in electronics to control current and voltage.

**Table 1** shows how amps are related to the smaller values of current. A thousandth of an amp, for example, is known as a **milliamp** (unit: mA). A millionth of an amp is a **microamp** (unit:

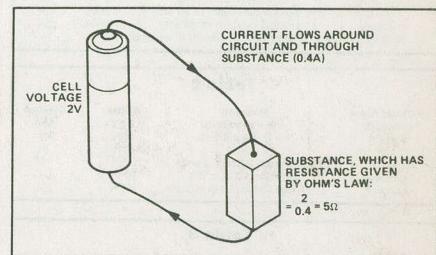


Fig. 4. The cell's voltage is 2V, and a current of 0.4A flows; the cube's resistance can be calculated by Ohm's law.

uA). Even smaller values of current are possible: a thousand millionth is a **nanoamp** (unit: nA); a million millionth is a **picoamp** (unit: pA). Chances are, you will never use or even specify a current value smaller than these, and you will rarely even use picoamps. Milliamps and microamps are quite commonly used.

It is easy to move from one current value range to another, simply by moving the decimal point one way or the other by the correct multiple of three decimal places. In this way, a current of 0.01mA is the same as a current of 10uA which is the same as a current of 10000nA and so on.

**Table 2** shows how volts are related to smaller values of voltage. Sometimes, however, large voltages exist (not so much in electronics, but in power electricity) and so these have been included in the table. The smaller values correspond to those of current, that is, a thousandth of a volt is a **millivolt** (unit: mV), a millionth of a volt is a **microvolt** (unit: uV) and so on — although anything smaller than a millivolt is only rarely used.

Larger values of voltage are the **kilovolt** (unit: kV), that is, one thousand volts, and the **megavolt** (unit: MV) which is one million volts.

## Resistors

The components which are used as resistances are called, naturally, resistors. So that we can control current and voltage in specified ways, resistors are available in

a number of values. Obviously, it would be impractical to have resistors of every possible value ( $1\Omega$ ,  $2\Omega$ ,  $3\Omega$ , etc.) because literally hundreds of thousands — if not millions would exist.

Instead, agreed ranges of values exist: and manufacturers make their resistors to have those values, within a certain tolerance. **Table 3** shows a typical range of resistor values. This range is the most common. You can see from it that large values of resistors are available, measured in kilohms (thousands of ohms) and even in megohms (millions of ohms).

**Table 1**

Current Name	Meaning	Value	Symbol
milliamp	one thousandth of an amp	$10^{-3}A$	$mA$
microamp	one millionth of an amp	$10^{-6}A$	$\mu A$
nanoamp	one thousand millionth of an amp	$10^{-9}A$	$nA$
picoamp	one million millionth of an amp	$10^{-12}A$	$pA$
femtoamp	one thousand million millionth of an amp	$10^{-15}A$	$fA$

**Table 2**

Voltage name	Meaning	Value	Symbol
megavolt	one million volts	$10^6V$	$MV$
kilovolt	one thousand volts	$10^3V$	$kV$
volt	—	$10^0V$	$V$
millivolt	one thousandth of a volt	$10^{-3}V$	$mV$
microvolt	one millionth of a volt	$10^{-6}V$	$\mu V$
nanovolt	one thousand millionth of a volt	$10^{-9}V$	$nV$

**Table 3**

1 $\Omega$	10 $\Omega$	100 $\Omega$	1k	10k	100k	1M	10M
1.2 $\Omega$	12 $\Omega$	120 $\Omega$	1k2	120k	1M2	—	—
1.5 $\Omega$	15 $\Omega$	150 $\Omega$	1k5	15k	150k	1M5	—
1.8 $\Omega$	18 $\Omega$	180 $\Omega$	1k8	18k	180k	1M8	—
2.2 $\Omega$	22 $\Omega$	220 $\Omega$	2k2	22k	220k	2M2	—
2.7 $\Omega$	27 $\Omega$	270 $\Omega$	2k7	27k	270k	2M7	—
3.3 $\Omega$	33 $\Omega$	330 $\Omega$	3k3	33k	330k	3M3	—
3.9 $\Omega$	39 $\Omega$	390 $\Omega$	3k9	39k	390k	3M9	—
4.7 $\Omega$	47 $\Omega$	470 $\Omega$	4k7	47k	470k	4M7	—
5.6 $\Omega$	56 $\Omega$	560 $\Omega$	5k6	56k	560k	5M6	—
6.8 $\Omega$	68 $\Omega$	680 $\Omega$	6k8	68k	680k	6M8	—
8.2 $\Omega$	82 $\Omega$	820 $\Omega$	8k2	82k	820k	8M2	—

Resistor tolerance is specified as a plus or minus percentage. A 10 ohm 10% resistor may have an actual resistance within the range between 9 and 11 ohms.

As well as being rated in value and tolerance, resistors are also rated by the power they can safely dissipate as heat, without being damaged. As you'll well remember from our earlier discussion on soldering irons, power rating is expressed in watts, and this is true of resistor power ratings, too.

As the currents and voltages we use in electronics are normally pretty small, the resistors we use also have small power ratings. Typical everyday resistors have ratings of  $1/4W$ ,  $1/3W$ ,  $1/2W$ ,  $1W$  etc. On the other end of the scale, for use in power electrical work, resistors are available with power ratings up to and over  $100W$  or so. *To be continued.*

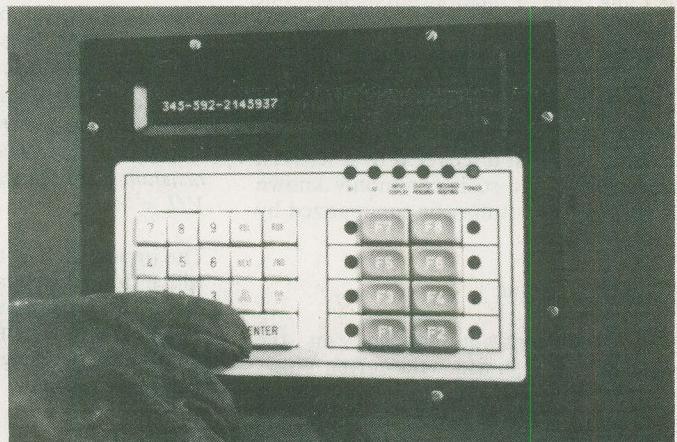
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## For Your Information

### Microterminal

The Burr-Brown TM2000 Microterminal can replace fragile CRTs in harsh industrial environments. It features an 8-bit microprocessor with RAM and

ROM, EPROM capability, a 40-character fluorescent display and a 27-key keypad. Contact Allan Crawford Associates, System Products Division, 5835 Coopers Ave., Mississauga, Ont. L4Z 1Y2 (416) 890-2010.



The Technical Service Council, One St. Clair Ave. E., Toronto, says that job vacancies for engineers and other professionals decreased 8 percent in the last three months but changed little in the last year. Of 99 specialists sought by employers, only systems analysts, computer programmers and mechanical sales engineers

recorded more than 40 vacancies in a 1700-firm survey. Electrical technologists rated only ten vacancies. Sigh. On the other hand, the Canadian Appliance Manufacturers Association reports a stable but moderate growth for microwave ovens and air conditioners. All is not lost.

### Digitizer

The HS-241-IE digitizer works with personal computers over a 24 by 36 inch area. It has 0.001 inch resolution, RS232 output, metric/imperial conversion, and

fixed or floating origin. It can run in five different modes and various options are available. Contact Gentian Electronics, PO Box 1240, Stittsville, Ont. K0A 3G0 (613) 836-3987.



Mitsubishi Electric Corporation, jointly with Korakuen Stadium Co., recently started marketing the "Flying Cabin" amusement machine that moves in synchronization with the image on a screen inside the cabin.

A six axis hydraulic system used for a flight simulator is employed in the Flying Cabin for computer-controlled synchronization of the moves of the 44-man cabin with the screen's crisp im-

ages and six channel sound.

The Flying Cabin simulates space travel: the cabin sways, careens and dives in line with changing magnificent scenes of ground surfaces and deep space projected on the huge 1.5m-by-4m screen. The two companies hope to sell 20 of the \$60,000 systems machine in Japan and 40 systems abroad in the coming five years, chiefly to amusement parks, shopping centers and science museums.

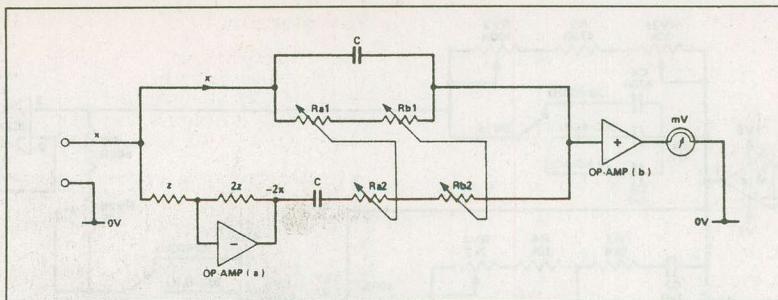


Fig. 1 The configuration of the Wien bridge used by the author; it has advantages over the more common differential version.

# Distortion Meter

**The distortion meter is a useful piece of test equipment that can quickly give a circuit a clean bill of health (or not). Here is the first of two parts on building your own.**

**By John Linsley Hood**

THE IDEAL power amplifier, along with other pieces of audio signal handling gear not intended to modify the frequency response of a system, is best described by the old adage "a piece of wire with gain". This implies that the equipment does not modify or impair the signal being handled except to amplify or add muscle power.

However, if this is the specification, how do we check to see how well or how badly this requirement is being met? This is, alas something on which there is very little agreement between audio engineers or circuit designers. So, before we consider the hardware, we need to examine the job we want it to do.

In simple terms, what we want is that the output from an amplifier should be identical to the input, except that it might be bigger or smaller or perhaps one part of the frequency spectrum has been enlarged or diminished. This is an awkward bit, so let's leave that to one side for the moment and just look at the simple flat-frequency response area.

When people first considered this

problem, their thoughts turned to the examination of a continuous, fixed-frequency sine wave somewhere in the middle of the audible band, say at 1000Hz. The logic of this was that any distortion of this waveform would lead to the generation of harmonics of the input signal, and these could be isolated and measured.

The problem with this approach is that it is highly artificial. We simply do not listen for enjoyment to steady single tones. Nevertheless, the technique is a useful one, especially if the output from the distortion meter can be examined on an oscilloscope. Quite a lot of information about its defects can then be gained, allowing the affects of changes to be assessed.

The most common of this kind of meter is the simple notch filter which will remove the incoming sine wave and leave only the waveform impurities which have been added by the hardware which we are testing.

The sort of result we would get at the output of the distortion meter is a small waveform which when added back to the distorted output would give us the pure signal with which we started. The waveform of the distortion products may be symmetrical about the zero axis, indicating distortion on both halves of the cycles, or it may be negative or positive spikes, indicating trouble on only one half of the cycle (such as an amplifier clipping on only the negative cycle).

The most conspicuous audible effect of the presence of large amounts of low-order distortion (mostly 2nd and 3rd) is that harmonic tones are added to the signal, making the system sound rather shrill. Those of us with long memories will recall the sound of output pentode power tubes, which caused generous quantities of 3rd order distortion. Triodes were

much preferred since they generated mostly 2nd harmonic distortion, and this was lower down in the spectrum and sounded less "squawky".

Also, as one might guess, these low-order harmonics do in fact harmonize with the input signal; once one gets beyond the 3rd in the odd-orders or the 6th in the evens, the tones become increasingly dissonant and objectionable to the listener.

This was one of the reasons why the first transistor amplifiers were so much worse than the tubes they replaced, even at 0.1 percent distortion; the distortion products were 7th, 9th, 11th and other dissonant odd harmonics.

A way of measuring distortion distinct from the notch method is to put in a high-purity sine wave and then display the output of the amplifier as a sweep of the frequency response on a spectrum analyzer. The various harmonic products will be displayed on a vertical log scale, either on a paper printout or a video screen. While the display is very effective, it's a bit hard to read if the distortion is down around 0.01 percent, not to mention the cost of the equipment required.

## Notching

The Total Harmonic Distortion meter in this project is the Wien bridge notch type; it produces a sharp notch in the frequency response which removes the fundamental of the test tone being used. Anything left over should consist of distortion products plus hum and noise. The noise can be subtracted by disconnecting the signal source; we'll come back to applications in the second part of the article.

The basic layout of the meter is shown in Fig. 1 and the circuit diagram in Fig. 2. The total circuit consists of the meter, a millivoltmeter, a built-in

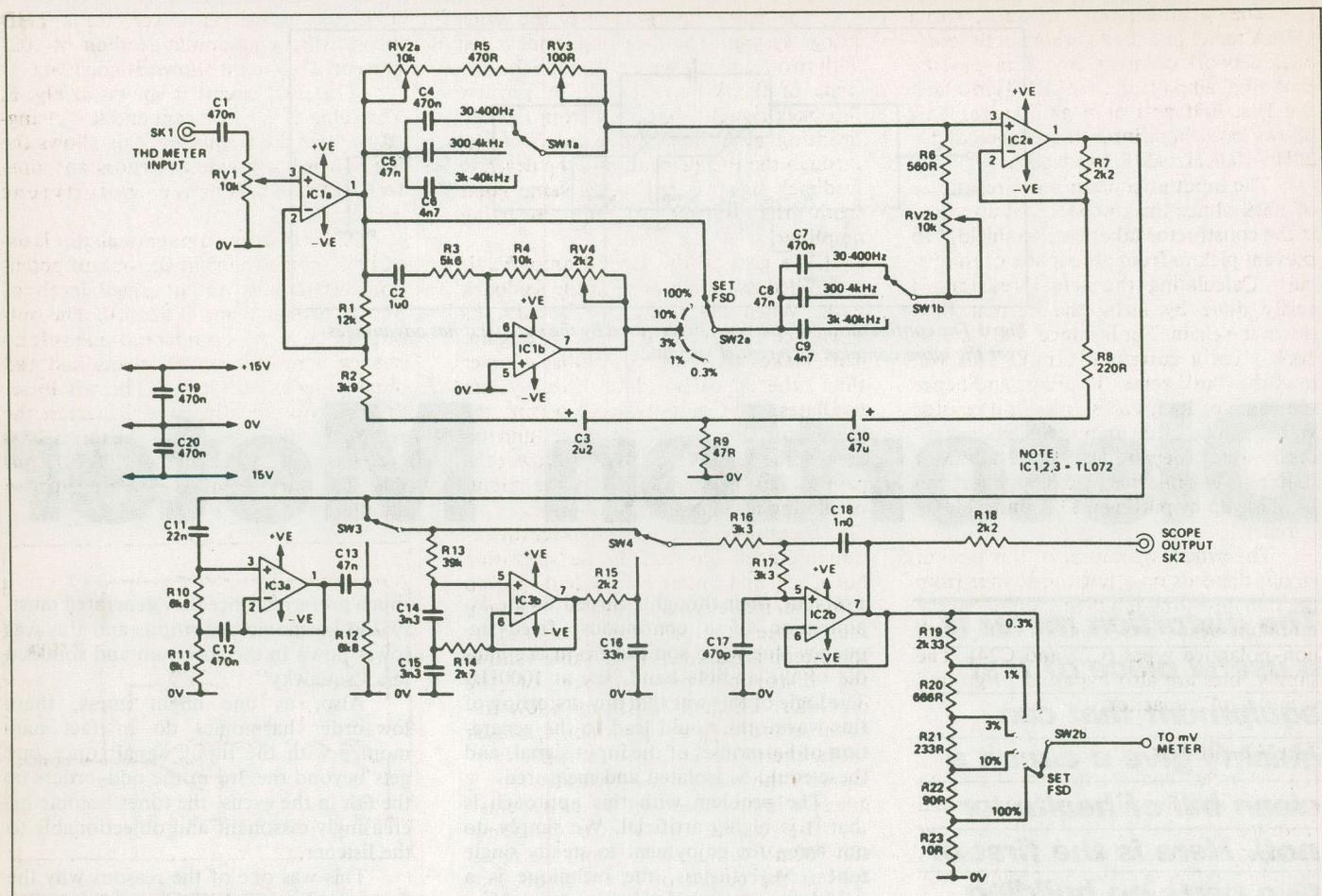


Fig. 2 The final circuit diagram of THD meter.

oscillator, and a regulated power supply. There is also a converter for running the unit from a single battery.

## The Meter Bridge

In Fig. 2, RV1 acts as a gain control in the input circuit of IC1a, a buffer stage that ensures that the Wien network is always driven from a low impedance. From the output of this the signal is divided into three paths, the upper RC parallel network, the inverter stage, and a feed to the mode switch SW2, which allows the network to be bypassed for setting the full scale meter reading.

In the other positions of SW2, the two halves of the network are connected to produce the notch characteristic required. For a perfect balance to be obtained, the input from the inverter to the lower section needs to be exactly twice as large as the input to the upper. To arrange this, a 2K2 10-turn pot, RV4, is connected in series with the op-amp feedback resistor so that its gain can be adjusted. This is the Trim control on the front panel.

Ideally, the tuning of this instrument would be done by dual gang pots, Ra and Rb. However, I wanted to keep circuit im-

pedances as low as possible for minimum hum pickup and circuit noise, and 1K dual pots are hard to come by. I have therefore, with regret, because it makes the instrument a little more difficult to use, opted for a single fine-tune pot, the 100 ohm RV3. This means that notching out requires the interaction of RV3 and RV4. If a decent quality low resistance dual pot is available, the other half should be inserted in series with R6, whose value can then be reduced to 470 ohms.

It is necessary to sharpen up the notch a bit to prevent unwanted attenuation of the lower harmonics, and this is done by negative feedback to IC1 from IC2 through R9, R2, and R8.

There are two signal infiltration stages. IC3a is a highpass hum filter with a turnover frequency of 250Hz and a slope of 18dB/octave; a similar lowpass built around IC3b has a similar slope and a turnover frequency of 4700Hz. These two options are selected by SW3 and SW4. The lowpass HF-noise filter allows an instrumental identification of the harmonics associated with crossover distortion, which with a 1KHz signal would be at 7, 9, 11, and 13KHz.

So, if the minimum signal is noted on

a test at 1KHz and the lowpass filter is then switched in and the new minimum noted, the amount of high-order harmonics present can be determined by an RMS subtraction of the two values. To distinguish between high-order harmonics and general noise, the extent to which the difference between the filtered and unfiltered signal levels changes when the signal input is removed can be noted.

The final stage of the distortion meter part of the circuit is the buffer amplifier, which precedes the meter attenuator, and from which an oscilloscope monitor signal can be obtained if needed.

An option included is the 50KHz, 12dB/oct, unity-gain rolloff buffer IC2b, which serves as a useful bandwidth limit. If this is not required, the output of SW4 can be sent to the non-inverting input of IC2b and C17, C18, R16, and R17 deleted.

## The Millivoltmeter

Since any distortion meter requires an AC millivoltmeter to display its result, and the millivoltmeter is a useful bench instrument, I have decided to make the input to the measuring circuit available separately by way of a switched attenuator, Fig. 3.

The circuit is straightforward, with a 100uA meter in a diode bridge in the feedback network of an op-amp. I have used a dual FET amp (TL072 or LF353) in which the first half acts as a gain stage. This allows both high input impedance and a 20Hz-100KHz -3dB bandwidth.

The input attenuator has a resistance of 100K ohms; this can be scaled up to 1M if the constructor takes care to shield it to prevent pickup from other parts of the circuit. Calculating the actual resistors is easily done by using the current flow down the chain. For instance, 100V across 100k gives a current of 1mA. This will develop 10mV across 10 ohms, and hence the value of R32, and so on. Odd resistor values can be made up by paralleling standard values, depending on the accuracy required. For instance, a 6k66 resistor can be made up by putting a 330k and a 6k8 in parallel.

The proper operation of this type of circuit depends on a low impedance from the non-inverting input to ground, so the tantalum beads are bypassed with small non-polarized types (C22 and C24). The supply lines are also bypassed to ground with 0.47uF capacitors.

### The Oscillator

It's a great convenience to have an oscillator actually on the instrument, and from my experience I find that one does not need a continuous spread of frequencies, but rather a few spot points. The reason for this is that if you know how a system behaves at, say, 1KHz or 3Khz, its behaviour at 1500Hz or 3500Hz is unlikely to be anything but intermediate between the known points.

The basic circuit used is the Wien bridge system. The inverting input is fed with two feedback signals through the sections of the Wien network. A positive feedback signal is obtained from the two inverting amplifiers connected in series through the RC element, and the negative feedback signal is fed to the same point from the output of the inverting amplifier.

The gain of the second amplifier is controlled by a thermistor in the feedback path. When the thermistor is cold, its resistance is high and ICb has a high gain. This makes the positive feedback larger than the negative and the system oscillates. The output signal warms the thermistor, lowers its resistance, and increases the negative feedback to lower the gain to just enough to keep the circuit oscillating at a constant amplitude.

Because op amps have a lower distortion in the non-inverting mode (surprising but true), and because the circuit has no common-mode signal which the op-amp must cancel, the distortion produced by this circuit is extremely low. Table 1 shows the distortion performance of the oscillator. The high distortion at low frequencies is mostly third harmonic, caused by the thermistor resistance actually varying with the waveform amplitude. This is inherent in amplitude-stabilized systems.

The output from ICb is about 600-700mV with an RA53, and the signal level at the output of ICa is about half this. I mention this because ICa is an integrator with a response which decreases with frequency, reducing the third harmonic distortion to about one-third at ICa, making a very low distortion

oscillator indeed. However, for a THD meter with a minimum reading of .005 percent, the circuit shown is adequate.

The final circuit is shown in Fig. 5. The value of C is constant and R is changed to alter the frequency; this allows the use of easily-obtained resistors and only two close-tolerance polystyrene capacitors.

A three-stage output attenuator is used in combination with the output potentiometer to give output signal levels of 0-6mV, 0-60mV and 0-600mV. The output can be increased to about one volt by putting a resistor of 500 ohms and 1k5 ohms in series with RA53. This will lower the distortion slightly, but will increase the settling time. On the prototype this is 2000 cycles, about 20 seconds at 100 Hz, but this will vary from one thermistor to another.

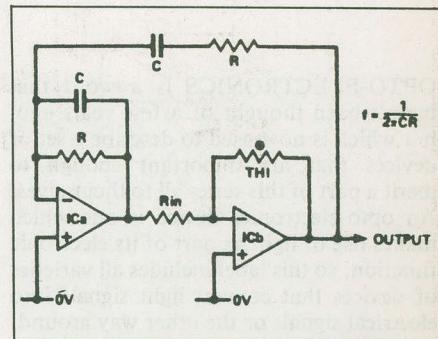


Fig. 4 The basic arrangement used in the spot frequency oscillator.

### Power Supply

The total current consumption of the instrument is 18mA at plus and minus 15 volts, which is obtained from a small stabilized supply.

It is possible to make the instrument operate from batteries. Two options exist. The first is use a pair of 6V or 9V batteries, such as the popular 9V radio battery, and switch both supply rails. The second is to use a single 9V battery and use the adapter shown to give plus and minus 4V5. In both cases it is worthwhile substituting TL062s for IC1, IC2, IC3, and IC5, and a TL061 for IC4. This reduces current consumption to 1.5 to 2mA with little performance penalty.

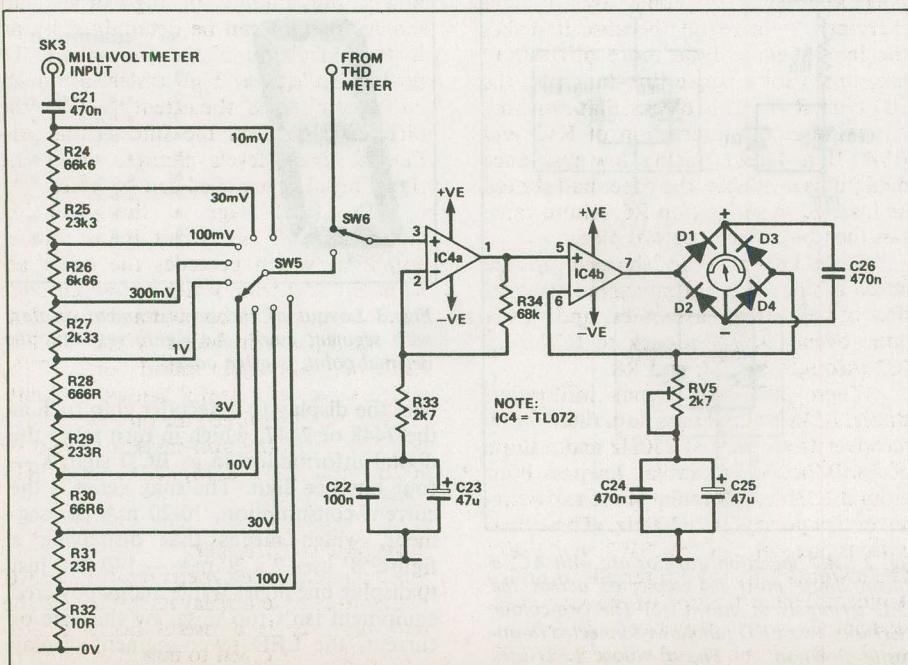


Fig. 3 The circuit diagram of the millivoltmeter.

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FREQUENCY (Hz)	THD (%)
100	0.02
300	0.005
1k	less than 0.003
3k	less than 0.003
10k	less than 0.003
20k	less than 0.003

Table 1 Measured performance of the spot frequency oscillator.

continued on page 48

# Configurations

**This month, a look at —  
opto-electronics.**

By Ian Sinclair

**OPTO-ELECTRONICS** is a word that hadn't been thought of a few years ago, but which is now used to describe a set of devices that are important enough to merit a part of this series all to themselves. An opto-electronics device is one which makes use of light as part of its electronic function, so this label includes all varieties of devices that convert light signals into electrical signals or the other way around.

The simplest opto-electronic devices of the electricity-to-light type are the familiar LEDs. Familiar they may be, but even experienced engineers are not always aware of their eccentricities. Like any other diode the LED has an anode and a cathode, and passes current in the forward bias direction; this is when the light is emitted. What is not nearly so well known is that the peak reverse voltage of these diodes is very low; if you get an LED the wrong way round in a circuit, it's usually curtains for the LED when the voltage is switched on. A typical value of peak reverse voltage is 3V, so practically any circuit that will operate the LED when it is connected the right way round (Fig. 1) will blow it up if it happens to be the wrong way round.

In addition, the forward voltage across the LED is very much higher than the 0V6 that we merrily assume for a silicon diode. For gallium arsenide, the material used for many types of LEDs, the forward voltage is more like 2V1 to 2V4, so that LEDs are of little use in very low-voltage circuit — they won't, for example, work from a 1V5 cell.

## Current Affairs

One of the major snags about LEDs is that they consume a surprising amount of current. Manufacturers quote 'adequate' light output for red LEDs with 5 to

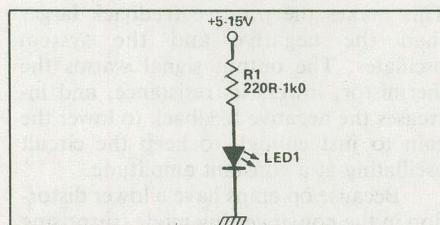


Fig. 1 The basic LED operating circuit. A current-limiting resistor must always be used unless the output resistance of the driving circuit is high.

25mA, and for the green/yellow varieties with 10 to 40 mA. This wouldn't be missed in a circuit operating at 5 V, 2.5 amps, but it can be quite a drain on battery equipment, often considerably more than all the CMOS ICs in a circuit intended for battery operation.

LEDs can be used with AC supplies providing there is a diode connected in reverse across each LED (to prevent excessive reverse voltage) as well as the usual

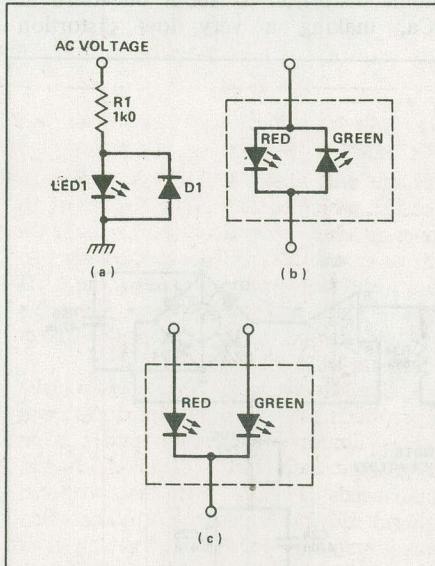


Fig. 2 LED operation. (a) For use with AC, a silicon diode must be connected across the LED terminals as shown. (b) The two-colour LED uses two LED junctions connected in opposite direction. (c) The tri-colour LED uses separate LEDs with a common cathode connection.

current limiting resistor (Fig. 2a). Bi-colour LEDs consist of a package of two LEDs in one casing, connected in inverse parallel so that current in one direction will give a light of one colour, while the other colour is achieved by reversing the current (Fig. 2b). In this circuit, one LED protects the other against reverse voltage. Tri-colour indicators (Fig. 2c) use two diodes with a common cathode connection and separate anode leads, so that three colours can be indicated, one in each lead, plus yellow when both LED sections are activated. Personally, for indicating when power is on, I much prefer the old-fashioned neon.

## On Display

When it comes to digit displays, LED types have quite a lot of competition. The traditional seven-segment display (Fig. 3) comes as a common anode or a common-cathode type (Fig. 4), and each type needs a separate limiting resistor in each driver lead. The normal method of use is to con-

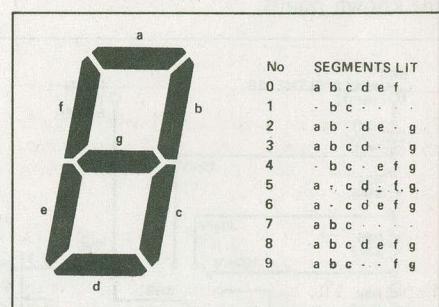


Fig. 3 Layout of the seven-segment display, with segment guide. An eighth segment, the decimal point, is often added.

nect the display to a decoder chip such as the 7448 or 7447, which in turn takes the digital information in as BCD signals — four bits per digit. The snag again is the current consumption, 10-20 mA per segment, which means that displaying a figure '8' uses  $7 \times 20$  mA — 140 mA just to display one digit! While mains-powered equipment isn't too upset by this size of current, the LED seven-segment display did not last very long in battery-powered devices, even when multiplexing was used.

Multiplexing means that only one digit at a time is activated, the digits being switched on in sequence fast enough to present the appearance of all the digits being illuminated at once.

Oddly enough, the forward voltage for the segments of an LED seven-segment display tends to be lower than for diodes, around 1V3 to 1V7. At temperatures above about 25°C, the maximum current has to be reduced by 0.3 mA per degree to avoid over-dissipation of the junction in each segment.

One competitive display that seems to be much less well-known is the filament seven-segment display. This can use as little as 5 mA per segment, and looks surprisingly bright — it can be driven by a

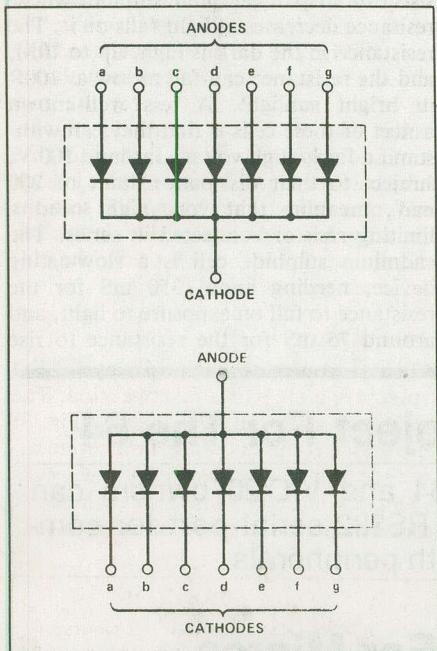


Fig. 4 Internal connections for common anode and common cathode displays. Whichever type is used, there must be a separate limiting resistor for each segment.

decoder directly with no limiting resistors, and for many purposes is superior to LED displays. The usual reason for preferring solid-state displays is long life, but the quoted life of more than 100,000 hours for the filament type of display is pretty competitive and some LED displays are notorious for short life.

### Oldies But Goodies

The two older types of displays which are also worth considering are the electron-beam type and the gas-discharge type. The electron beam display uses a miniature cathode wire to emit electrons, which will then be attracted to any positive anode. The anodes are coated with phosphors (similar to the phosphors used in cathode ray tubes), and any anode which is positive to the cathode by a sufficient

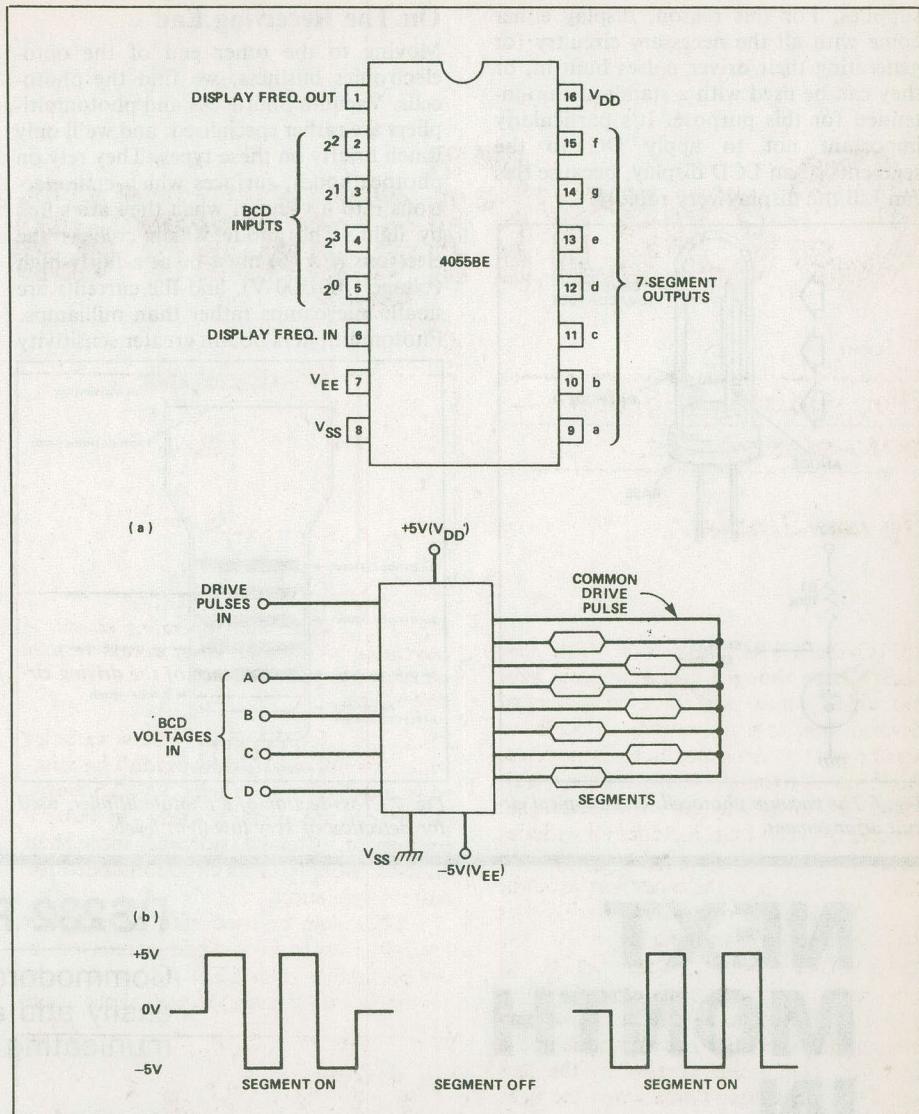


Fig. 5 Driving LCD displays. The common lead of the LCD display must not be earthed; it has to be returned to the driver IC. The waveform (b) applied is AC with no trace of DC.

voltage will glow. A 24-40 V supply is needed, which usually means the use of an inverter when low-voltage batteries are used, as in calculators. The display is easy to read, and uses less current than the LED type — I still prefer a calculator using this type of display to one using the more-common LCD display.

The gas-discharge display is an older type which uses the principle of the neon light — ionisation of a low-pressure gas in an electric field. Like all gas-discharge, this needs a high operating voltage, around 150-250 V, but the operating current is very low: only 0.7 mA per segment in a typical application. The display is very bright, and is worth considering for 120 operated equipment whose display has to be viewed under difficult illumination conditions, such as alternate brightness and darkness. A driver IC is available nowadays — in times past (dare we say the Dark Ages?), the major handi-

cap of using this type of display was the lack of suitable driver transistors.

### Liquid Light

Last among the displays, of course, there is the LCD. A good LCD can give a dense black indication against a light grey background, is clearly visible in bright light, and reasonably visible even in low illumination conditions. There's a lot of variation between displays, however, even from the same manufacturer and some are poor, with low contrast and very slow response to changing digits. Prices also vary considerably — one catalogue lists the price of a calculator-size display as being twice as much as I would have to pay for a complete calculator using a similar display!

Operating conditions for these displays are very different from those of other types of displays, because they have to be operated from high-frequency AC

supplies. For this reason, display either come with all the necessary circuitry for generating their driver pulses built in, or they can be used with a standard chip intended for this purpose. It's particularly important not to apply DC to the segments of an LCD display, because this can kill the display very rapidly.

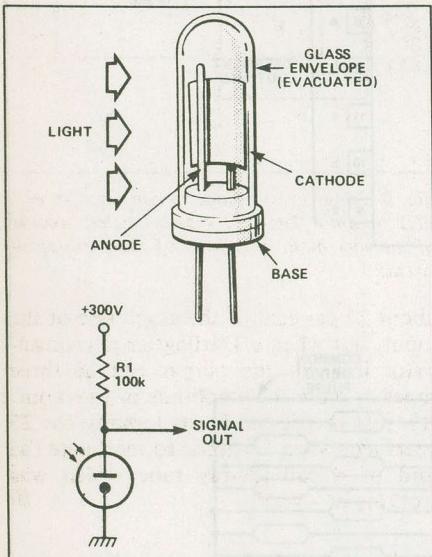


Fig. 6 The vacuum photocell, and a typical circuit arrangement.

### On The Receiving End

Moving to the other end of the optoelectronics business, we find the photo-cells. Vacuum photocells and photomultipliers are rather specialised, and we'll only touch briefly on these types. They rely on photocathodes, surfaces which emit electrons into a vacuum when they are struck by light. The anode which collects the electrons (Fig. 6) must be at a fairly high voltage (100-500 V), and the currents are small: microamps rather than millamps. Photomultipliers obtain greater sensitivity

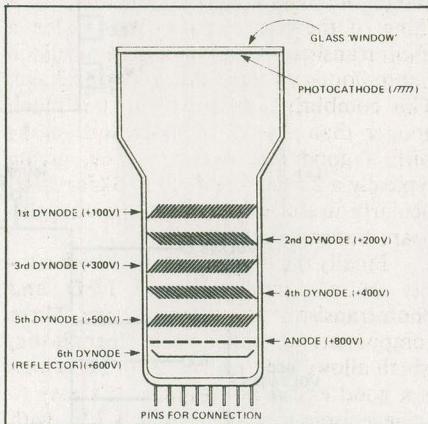


Fig. 7 Cross-section of a photomultiplier, used for detection of very low light levels.

and increased output by using secondary multiplication, meaning that the electrons from the cathode (Fig. 7) are accelerated to surfaces, called dynodes, which will release electrons each time an electron strikes the surface. If each of these multipliers releases two to five electrons for each striking electron, spectacular gain can be achieved which, unlike amplification of signals by conventional methods, is practically noise-free.

The more familiar solid-state light-to-electrical-signal devices that we use are the solid-state photodetectors, of which the most commonly used is the cadmium sulphide cell. The ORP 12 is the standard device of this type, often called an LDR (light dependent resistor). The cell consists of a strip of cadmium sulphide whose resistance decreases as light falls on it. The resistance in the dark is high, up to 10M, and the resistance can fall as low as 100R in bright sunlight. A less well-known aspect of these cells is that they can withstand a fairly high voltage, around 100 V; subject to their dissipation limit of 200 mW, meaning that you might need a limiting resistor connected in series. The cadmium sulphide cell is a slow-acting device, needing about 350 mS for the resistance to fall on exposure to light, and around 75 mS for the resistance to rise

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# Electronics Today

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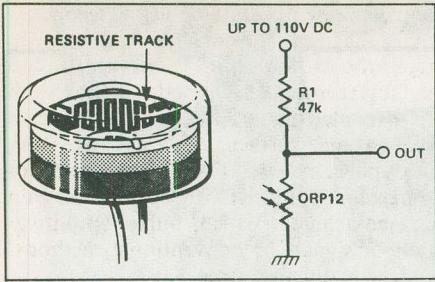


Fig. 8 The photoresistive cell or light-dependent resistor (LDR)

again when the light is shut off. The response to different colours is generally similar to that of the human eye, but the cadmium sulphide is much more sensitive to red and infrared, which is why its use in cameras is now less common than it was some 10 years ago.

### Fun With Photodiodes

Other light detectors need some degree of amplification. Photodiodes are diodes of fairly conventional construction, with a transparent window over the junction, which is used reverse-biased. For such a diode, the reverse leakage current increases as the intensity of light on the junction is increased. This current is small, ranging from around 1 nA in

darkness to almost 1 mA in very bright light, so that amplification is usually necessary, as in the circuit of Fig. 9. The response time is about 250 nS, so that the op-amps shown in Fig. 9 would have to be replaced by a transistor circuit, using high-speed switching transistors, if you wanted to use the photodiode for high-speed signals. Combined photodiode/op-amp packages can be bought for medium-speed applications.

The old-style phototransistor, which was a transistor formed with a window above the base-collector junction, is a thing of the past; what is now called a phototransistor is a combination of silicon photodiode and transistor in one package. This combines a sensitivity that is much greater than that of a photodiode alone with a good fast response time, giving typically a 2 MHz bandwidth. This is particularly useful for receiver use in light-beam transmission systems.

Finally the opto-isolators, which consist of a combination of LED and phototransistor in a single package. These components are embedded in clear plastic, which allows light transmission but which is a good electrical insulator. It's easy to achieve isolation to at least 4 kV, with reasonable signal transmission. For an ordinary isolator the output signal will be

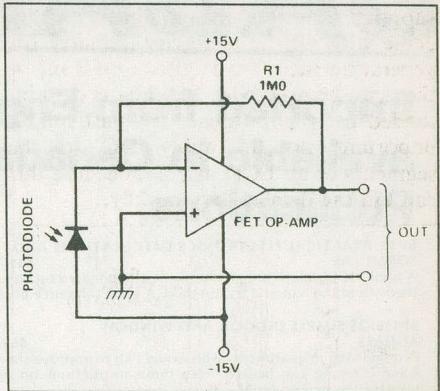


Fig. 9 Using a photodiode in conjunction with FET op-amp. The FET type is needed because of the very high impedance of the photodiode circuit.

about 20 per cent of the amplitude of the input, but when a Darlington phototransistor is used, the output can be three times or more the amplitude of the input. It's just the device I was looking for 25 years ago when I wanted to modulate the grid of a cathode-ray tube which was working at -4 kV!

## This Month in

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Build a small Z80-based computer.

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The powerful Sinclair QL microcomputer has some outstanding capabilities in terms of its internal structure. With a 32-bit architecture, the QL has a large address range, advanced instructions which include multiplication and division. These features give the budding machine code programmer a good start at advanced programming methods. This book assumes no previous knowledge of either the 68008 or machine code programming.

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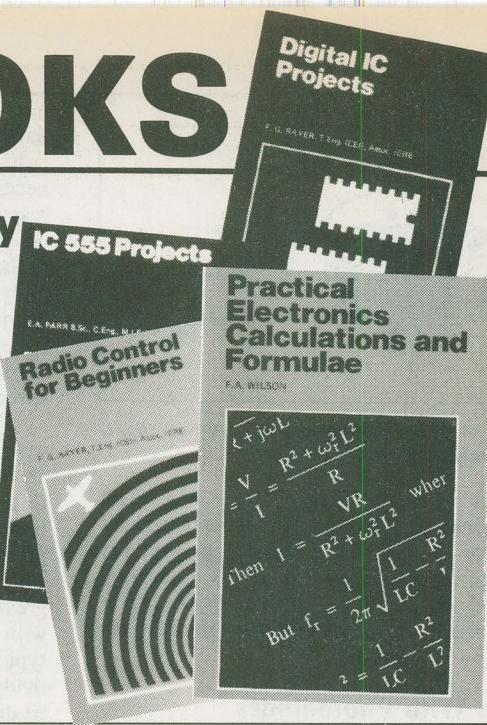
Divided into six parts, this book covers such areas of mobile "disco" as: Basic Electricity, Audio, Ancillary Equipment, Cables and Plugs, Loudspeakers, and Lighting. All the information has been considerably sub-divided for quick and easy reference.

**BP59: SECOND BOOK OF CMOS IC PROJECTS** \$7.75

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Although one of the more recent branches of amateur electronics, electronic music has now become extremely popular and there are many projects which fall into this category. The purpose of this book is to provide the constructor with a number of practical circuits for the less complex items of electronic music equipment, including such things as Fuzz Box, Waa-Waa, Pedal Sustain Unit, Reverberation and Phaser-Units, Tremolo Generator etc.

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Every so often a device appears that is so useful that one wonders how life went on before without it. The 555 timer is such a device. Included in this book are Basic and General Circuits, Motor Car and Model Railway Circuits, Alarms and Noise Makers as well as a section on the 556, 558 and 559 timers.

**BP82: ELECTRONIC PROJECTS USING SOLAR CELLS** \$7.75

A book of simple circuits which have applications in and around the home and that are designed to be powered by the energy of the sun. Although, if the reader wishes, they could be powered by ordinary button cells or batteries.

See order form in this issue.

# BABANI BOOKS

## BP49: POPULAR ELECTRONIC PROJECTS

R.A. PENFOLD

Includes a collection of the most popular types of circuits and projects which, we feel sure, will provide a number of designs to interest most electronics constructors. The projects selected cover a very wide range and are divided into four basic types: Radio Projects, Audio Projects, Household Projects and Test Equipment.

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R.A. PENFOLD

We have all built circuits from magazines and books only to find that they did not work correctly, or at all, when first switched on. The aim of this book is to help the reader overcome just these problems by indicating how and where to start looking for many of the common faults that can occur when building up projects.

## BP84: DIGITAL IC PROJECTS

F.G. RAYER, T.Eng.(CEI), Assoc.IERE

This book contains both simple and more advanced projects and it is hoped that these will be found of help to the reader developing a knowledge of the workings of digital circuits. To help the newcomer to the hobby the author has included a number of board layouts and wiring diagrams. Also the more ambitious projects can be built and tested section by section and this should help avoid or correct faults that could otherwise be troublesome. An ideal book for both beginner and more advanced enthusiast alike.

## BP67: COUNTER DRIVER AND NUMERAL DISPLAY PROJECTS

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\$7.05 Numerical indicating devices have come very much to the forefront in recent years and will, undoubtedly, find increasing applications in all sorts of equipment. With present day integrated circuits, it is easy to count, divide and display numerically the electrical pulses obtained from a great range of driver circuits.

In this book many applications and projects using various types of numeral displays, popular counter and driver IC's etc. are considered.

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## BP107: 30 SOLDERLESS BREADBOARD PROJECTS — BOOK 1

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\$8.85 A "Solderless Breadboard" is simply a special board on which electronic circuits can be built and tested. The components used are just plugged in and unplugged as desired. The 30 projects featured in this book have been specially designed to be built on a "Verobloc" breadboard. Wherever possible the components used are common to several projects, hence with only a modest number of reasonably inexpensive components it is possible to build, in turn, every project shown.

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### Audio Amplifier Construction

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A wide circuits is given, from low noise microphone and tape head preamps to a 100W MOSFET type. There is also the circuit for 12V bridge amp giving 18W. Circuit board or stripboard layout are included. Most of the circuits are well within the capabilities for even those with limited experience.

## BP80: POPULAR ELECTRONIC CIRCUITS — BOOK 1

R.A. PENFOLD

Another book by the very popular author, Mr. R.A. Penfold, who has designed and developed a large number of various circuits. These are grouped under the following general headings: Audio Circuits, Radio Circuits, Test Gear Circuits, Music Project Circuits, Household Project Circuits and Miscellaneous Circuits.

## BP98: POPULAR ELECTRONIC CIRCUITS, BOOK 2

R.A. PENFOLD

\$8.85 70 plus circuits based on modern components aimed at those with some experience.

## BP39: 50 (FET) FIELD EFFECT TRANSISTOR PROJECTS

F.G. RAYER, T.Eng.(CEI), Assoc.IERE

\$6.75 Field effect transistors (FETs), find application in a wide variety of circuits. The projects described here include radio frequency amplifiers and converters, test equipment and receiver aids, tuners, receivers, mixers and tone controls, as well as various miscellaneous devices which are useful in the home.

This book contains something of particular interest for every class of enthusiast — short wave listener, radio amateur, experimenter or audio devotee.

## BP87: SIMPLE L.E.D. CIRCUITS

R.N. SOAR

\$5.40 Since it first appeared in 1977, Mr. R.N. Soar's book has proved very popular. The author has developed a further range of circuits and these are included in Book 2. Projects include a Transistor Tester, Various Voltage Regulators, Testers and so on.

## BP24: 50 PROJECTS USING IC 741

\$6.75 A unique book containing 52 different projects that can be simply constructed using the 741 op amp and a few components. Originally published in Germany, this book will be an valuable asset to any hobbyist.

## BP88: HOW TO USE OP AMPS

E.A. PARR

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## BP23: 50 PROJECTS USING IC CA3130

R.A.PENFOLD

\$5.00 In this book, the author has designed and developed a number of interesting and useful projects which are divided into five general categories: I — Audio Projects II — R.F. Projects III — Test Equipment IV — Household Projects V — Miscellaneous Projects.

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BOOK 1

\$7.60 Virtually any electronic circuit will be found to consist of a number of distinct stages when analysed. Some circuits inevitably have unusual stages using specialised circuitry, but in most cases circuits are built up from building blocks of standard types.

This book is designed to aid electronics enthusiasts who like to experiment with circuits and produce their own projects rather than simply follow published project designs.

The circuits for a number of useful building blocks are included in this book. Where relevant, details of how to change the parameters of each circuit are given so that they can easily be modified to suit individual requirements.

## BP102: THE 6809 COMPANION

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Written for machine language programmers who want to expand their knowledge of microprocessors. Outlines history, architecture, addressing modes, and the instruction set of the 6809 microprocessor. The book also covers such topics as converting programs from the 6800, program style, and specifics of 6809 hardware and software availability.

## BP118: PRACTICAL ELECTRONIC BUILDING BLOCKS - Book 2

R.A. PENFOLD

\$7.60 This sequel to BP117 is written to help the reader create and experiment with his own circuits by combining standard type circuit building blocks. Circuits concerned with generating signals were covered in Book 1, this one deals with processing signals. Amplifiers and filters account for most of the book but comparators, Schmitt triggers and other circuits are covered.

## BP24: 50 PROJECTS USING IC741

\$6.75

RUDI & UWE REDMER

This book, originally published in Germany by TOPP, has achieved phenomenal sales on the Continent and Babani decided, in view of the fact that the integrated circuit used in this book is inexpensive to buy, to make this unique book available to the English speaking reader. Translated from the original German with copious notes, data and circuitry, a "must" for everyone whatever their interest in electronics.

## BP83: VMOS PROJECTS

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R.A. PENFOLD

Although modern bipolar power transistors give excellent results in a wide range of applications, they are not without their drawbacks or limitations. This book will primarily be concerned with VMOS power FETs although power MOSFETs will be dealt with in the chapter on audio circuits. A number of varied and interesting projects are covered under the main headings of: Audio Circuits, Sound Generator Circuits, DC Control Circuits and Signal Control Circuits.

## RADIO AND COMMUNICATIONS

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In this book, R.A. Penfold has designed and developed several modern solid state short wave receiver circuits that will give a fairly high level of performance, despite the fact that they use only relatively few and inexpensive components.

## BP91: AN INTRODUCTION TO RADIO DXing

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This book is divided into two main sections one to amateur band reception, the other to broadcast bands. Advice is given to suitable equipment and techniques. A number of related constructional projects are described.

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The subject of aerials is vast but in this book the author has considered practical designs including active, loop and ferrite aerials, which give good performances and are reasonably simple and inexpensive to build. The complex theory and math of aerial design are avoided.

# Timed NICAD Charger

**Extend the capacity and life of your nicad batteries by optimizing the charge.**

**By Robert Card**

NICAD batteries are an increasingly popular replacement for dry cell batteries. Their one disadvantage is that you have to have a special charging unit, and although relatively cheap units are available, they have the drawback that they don't have any facility for timing. Timing the length of charge is important; overcharging can actually reduce the capacity of the battery.

The unit described here avoids this problem by providing a timed charging interval after which it switches off the current and flashes a ready light.

The way the circuit works is shown in the block diagram. It is quite difficult to generate a low frequency accurately, so the first section generates a relatively high frequency, 5KHz or higher, and then uses a binary counter to divide this down by 16384 to give a frequency of about 0.3Hz. This section is the clock generator, and its frequency is set by the value of Rx.

The next section, divide and stop, is another binary counter but one which turns itself off when it reaches its maximum count. The output from this stage goes to the control logic, which turns off the constant current generator and flashes the indicator light.

The final section is the constant current generator; this provides a constant current to the battery regardless of the voltage across the battery's terminals. This is the usual way of charging nicads, and has the advantage that several batteries can be put in series and charged at the same time. The magnitude of the charging current is set by selecting one of the resistors using the switch shown. This is necessary because different battery sizes require different charging currents.

Other sections are: the power supply, which converts the 115VAC to 15VDC, the GO switch, which, when off, resets the divide and stop function and holds off the charging current and indicator light, and the indicator lights with drivers Q1 and Q2.

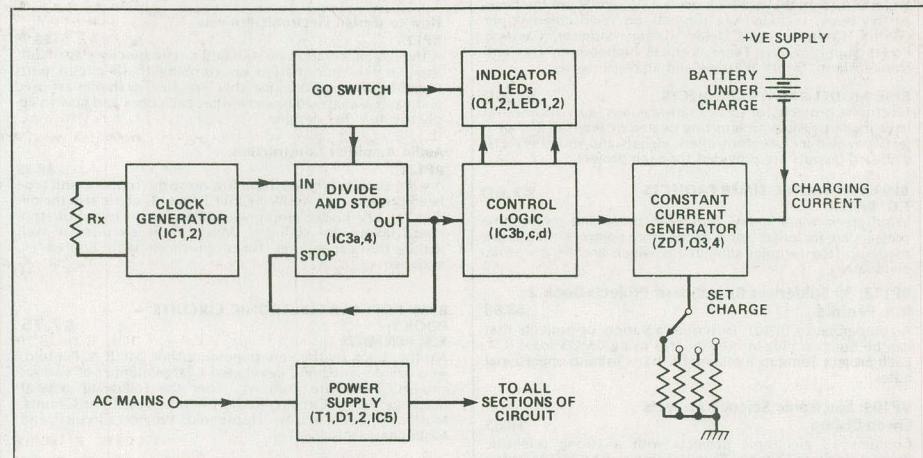


Fig. 1 The block diagram. Several batteries can be charged in series. There are limitations.

## Choosing and Setting

Nicads have capacities usually measured in milliamp-hours (mAH) or amp-hours (AH). In theory, a battery with a capacity of 1AH could deliver a current of one amp for one hour, but in practice it doesn't work this way; the battery's voltage tails off as it runs down. There is also some energy dissipated in the battery's internal resistance. Nonetheless, the capacity is a guide to how much charging is necessary.

Generally, nicads prefer reasonably long charging periods, say ten hours, and for this Rx should be 33K. Other periods are possible: Rx is equal to  $3456T - 1350$  where T is the required charge time in hours. Table 1 shows some charge times if you don't trust your math.

Actually, choosing ten hours makes the choice of charging current much easier, so we recommend sticking to this. Note that some batteries can be charged at a very high rate, requiring a much shorter period.

The next step is to work out the charging current: this is the battery's capacity divided by the charging time. A 1AH battery for ten hours would be a 100 mA charge.

The value of Ry sets the charging current, and unless you only use one type of battery you'll find it useful to use a switch, SW3, to select different values of Ry according to Table 2. If none of these values suit you, you can work out the value of Ry from the equation  $Ry = 2.5/I$ , where I is the required current in amps, and Ry is in ohms.

To prevent overcharging, be sure that the batteries are discharged before charging them. A fully charged nicad has a lower voltage than standard cells, about 1.2V compared to about 1.5V. This may make them unsuitable for certain applications.

One final point on this section is that a switch, S1, can be used to select the full divide chain, or miss off the final divider stage by taking the output from pin 2 rather than pin 3. This latter option halves the timing period.

The output from this section is passed to IC3d, and this gates the output from the divide and stop section and gates it with the GO switch. As long as both these signals are low, the output of IC3d is high, which keeps the constant current

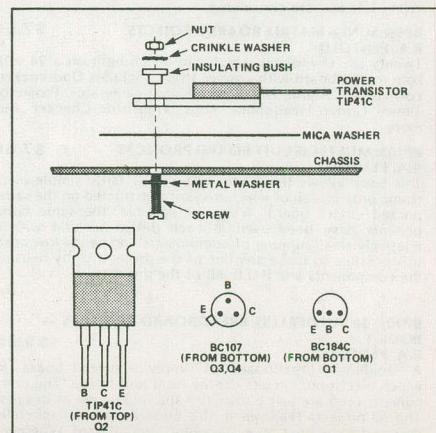


Fig. 2 The heatsinking arrangement for the power transistor, plus connection details for the other transistor types.



You can't go outside any more without being inundated with advertisements for computers. In fact, even staying inside won't help very much, unless you unplug the TV and the radio and threaten the postman with violence so he'll stay away. Computers are becoming as much a part of our culture as are TV, cars and central heating. In five years, computer illiteracy may be as much of a handicap as print illiteracy was five years ago.

Despite the colourful advertisements that have turned up in the media for computer hardware, micros are still understandably forbidding for many people. In fact, though, most new computer owners are amazed at just how small the leap from unpacking the thing, to getting it to do what they want it to do really is.

**A computer is an everything tool.** It can do anything you can conceive of having it do, limited only by your own ingenuity in telling it how. This is the art of programming, an experience not unlike that of an artist with every colour of paint imaginable and a blank canvas the size of Australia. However, the canvas of a computer never gets full... the more you learn how to do, the more you'll realize it is possible to tackle.

**Computing Now!** magazine can be the most useful accessory you can buy for your system. Every issue is stuffed full of news about products which will enhance your system, programs to run on it, ideas and applications to give you new things to do with your machine, articles about programming techniques, and tricks to make your computer a better tool for realizing your ideas, and piles of other features to help you get the most out of your system.

Whether you are looking at a computer as a hobby or to run your business, **Computing Now!** can provide you with information and applications to make your system the most useful thing you've ever bought next to the can opener. The articles in **CN!** are written by people who are using computers daily, and are among the most experienced small computer users in the country.

**Computing Now!** is published and wholly owned in Canada. As such, the articles and advertisements in it relate specifically to products you can get North of the border. There is nothing more frustrating than finding something interesting in an American magazine, only to discover that it will take four months to drag it through customs.

We know microcomputers and we practise what we preach — in fact, we are probably one of the most computerized companies in Canada. Here are just some of the systems we currently own: Apple II and compatibles (15 systems); Apple Macintosh; IBM PC and compatibles (BEST; Corona, Columbia, JLS); Nelma Persona (2); TRS-80 Model II (2); Commodore 64; Vic-20; Commodore PET; ZX81; if800; Acorn Atom; Multiflex. These are just some of the systems we own and use ourselves; our contributing editor's systems are additional to this list.

With powerful computers becoming widely available, you can't afford not to be aware of the expanding universe of micros. **Computing Now!** can provide you with an understandable, incisive and varied insight into this powerful new technology. It is written to be

both understandable by the beginner and informative for the more experienced user with a depth and variety unequalled by any other contemporary computer publication.

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**Table 1 Charging Times And Resistor Values**

Time (hours)	Rx
1	2k1 (or 2k2)
2	5k6
5	16k (or 15k)
10	33k
14	47k
20	68k

generator on. If either the counter output or the output from the circuit around SW2a goes high, the output from IC3d goes high. SW2a also controls the input to the rest of the divide and stop section, pin 11 of IC4; opening SW2a takes this input high and resets all the stages of the divider IC.

### The CCG

The output of IC3d goes to the constant current generator (CCG). This uses the fact that transistors have a relatively constant voltage between base and emitter. If we hold the base voltage constant by means of a Zener regulator diode, the emitter will also be held at this voltage (less the B-E drop). Now any resistor from the emitter to ground will have a constant current through it (because of the constant voltage), and this current flows through the collector circuit. If we place the load (in this case, the battery) from the collector to the power supply, it has a constant current through it. This current can be changed by varying the emitter resistor.

In this circuit, Q3 and Q4 form a Darlington amplifier; Q3 is used to reduce the control current into the base of Q4 and prevent loading of IC3d, a CMOS gate.

A limitation of the circuit is that since there has to be a volt or two between Q3's collector and its base, plus 3V9 for the Zener diode, the maximum output voltage is limited to about 10 volts. This means that a maximum of about six cells can be charged in series, less for some types of cells.

The indicator drivers are formed from IC3. IC3c keeps IC3b off while the input to IC3c from the divide and stop is low. This means that the output for IC3c will be high, holding IC3b off, until the divide and stop output goes high. From then on, when the other input to IC3b from the clock generator goes low, IC3b can turn on and illuminate LED 2 via Q1. When the clock generator goes high, IC3b is turned off, switching off the LED. As the line oscillates between high and low, LED 2 will flash.

The overall effect of this circuit is as follows: while charging is taking place,

**Table 2 Battery Capacities And Charging Currents**

Battery capacity (mAH)	Charging Current for 10hr charge (mA)	Ry value (ohms)	Ry power (watts)
110	11	230 (or 220)	1/4
200	20	125 (or 120)	1/4
500	50	50 (or 47)	1/4
1AH	200	25 (or 24)	1/2
4AH	400	6.25 (or 4.7 + 1.8)	2

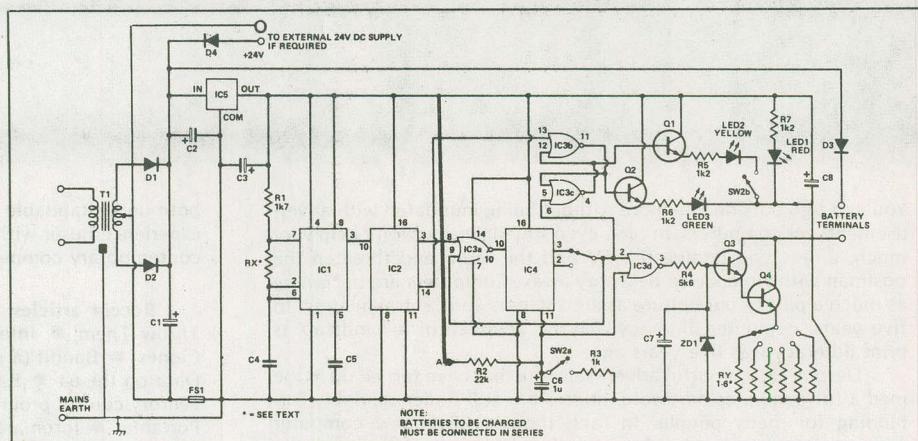
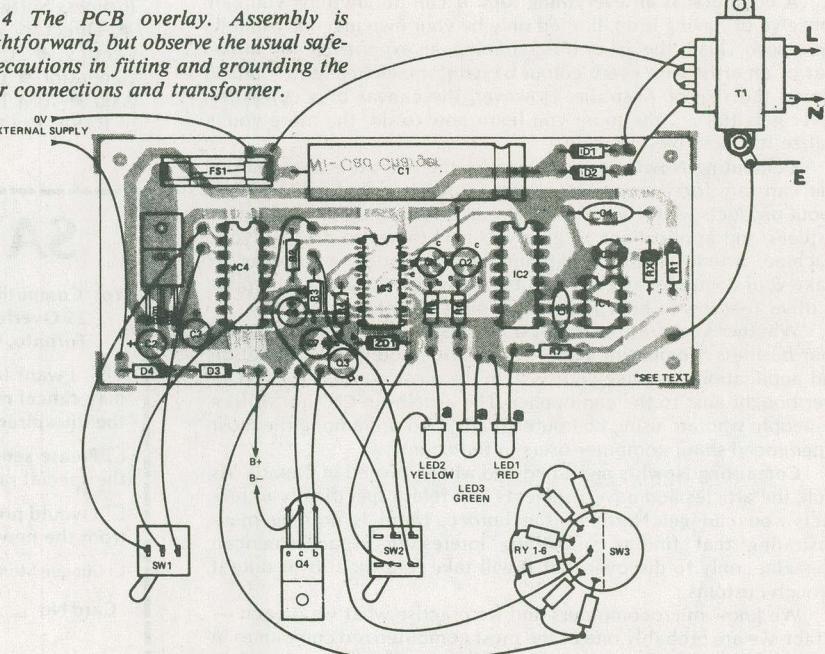


Fig. 3 The Circuit.

Fig. 4 The PCB overlay. Assembly is straightforward, but observe the usual safety precautions in fitting and grounding the power connections and transformer.



LED 3 will be on, driven by Q2 from the output of IC3c. When the charging period is finished, this turns off and LED 2 will flash. Moving the GO switch to OFF turns off LED 2 via SW2b. LED 1 is on all the time as a pilot light.

### More Workings

IC1, a 555 timer, is configured as a continuous oscillator with the frequency set

by C4 and Rx. The output at pin 3 is sent to IC2, a 14-stage binary counter which divides by 16384. This divided output at pin 3 comprises the output of the clock generator section. The output from IC2 pin 14 is taken from part way along the divider chain; it's the 555 signal divided by 1024, and this is used to pulse the "charge finished" LED.

The divide and stop section is based

continued on page 28

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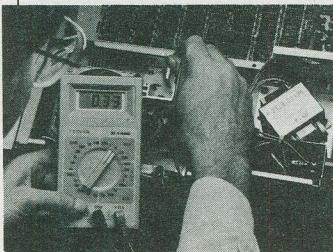
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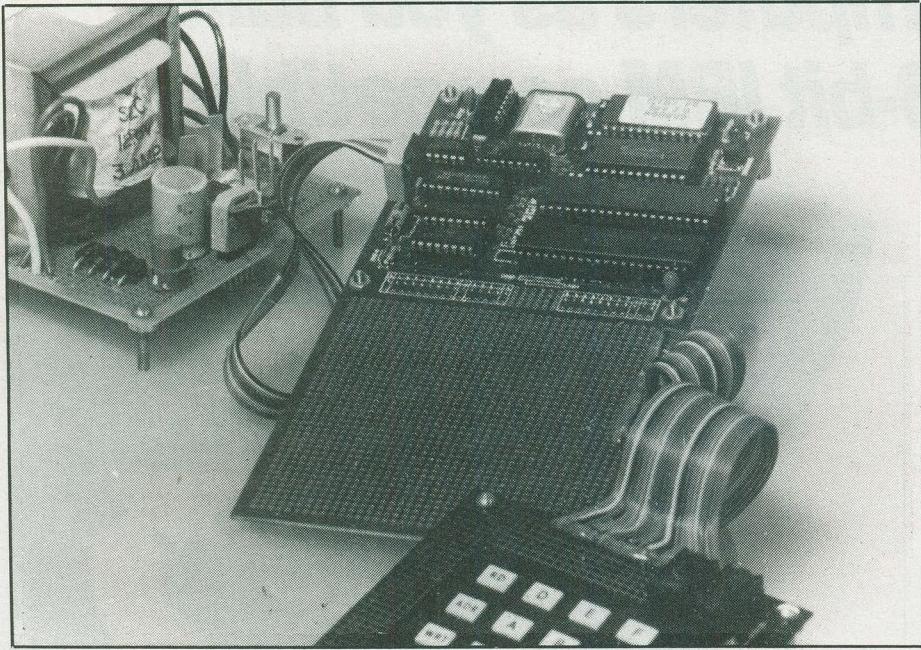
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# MTHS-80



**A Z80 controller that makes an intelligent interface; use it as a stand-alone or as part of a system.**

**By E. Penn**

LISTEN, I bought a printer at a surplus store for \$29.95 and the guy said "Sure it'll print with your computer, man, no sweat, we sell a million of 'em, here's your change." Good deal.

Of course it didn't work and it just sat like a bump on a log, didn't it, except for the letter "A" which made it spool the paper out onto the floor. Hosed again. I'm told that the code used by the printer doesn't correspond with the computer's ASCII and a translator is needed.

Now I've been handed this PCB and a chip and a manual and all, I'm told, is not lost. The package comes from Nicologix, of Streetsville, Ontario, and an interesting package it is, too. What you get, actually, is a printed circuit about 8 by 4 inches with lots of room on it for a CPU and support chips, plus an area of uncommitted pads where you can stick the circuit of your dreams. This circuit can be wire-wrapped or soldered, what with space being provided for your choice of connectors.

The entire point of this, the very essence, is that you now have a Z80A CPU that's crystal-controlled at 3.57MHz and has a buffered data bus, two I/O ports via a Z80-PIO, and software in ROM. There's also 2k of static RAM which can be expanded to 8k. This gives the hardware hacker the ability to noodle out all sorts of designs without opening up the old Apple clone and disturbing its cantankerous innards.

This little gadget is known as the HS-80 system, and will solve problems for you such as building a translator for a cheap printer that doesn't speak your computer's language. All you need is a power supply that can let it have five volts and at least 300mA. Plus, of course, the manual, which, although not a designer's coursebook, covers the layout, as well as the operation, of the HS-80, superbly. Love them commas.

The bare PCB sets you back \$20, not a bad bite for quality double-sided stuff, and the nifty manual another \$5.00. The

Inteface

*The HS-80 circuit board with the MTHS-80 ROM. The 5V supply is at right, and a keypad (foreground) has been wired in for entering codes.*

parts you'll need are the garden variety stuff like a CPU and gates; the only weird part is the Z80-PIO port chip, and Z80 CPU dealers should carry it.

If you're really into this by now, and desperate to make that printer work, for another \$40 you can get the MTHS multi-tasking software on a ROM chip plus another manual. This manual is even thicker and better than the first, and explains in detail the simple assembler-style language for turning the board into an intelligent interface. You'll note in the photo that the board has a small keyboard and 7-segment readout attached; a few simple codes and we had the board beeping a beeper while leaving the CPU free for other tasks. Some of those other tasks would be making a printer buffer or smart EPROM programmer or smart keyboard encoder or a terminal controller or a parallel-serial converter or even, bless 'em, a printer driver.

The uses for this little intelligent breadboard are - dare I say it? - limited

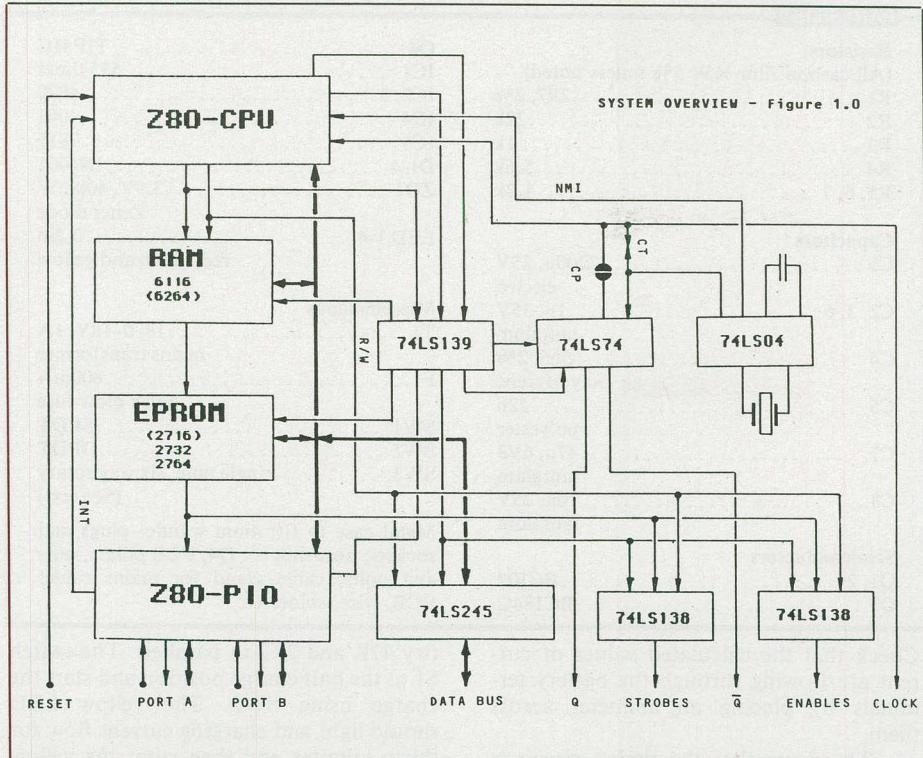


Fig. 1 The block diagram of the HS-80 hardware; the MTHS multi-tasking system is added via the ROM chip.

only by your imagination. After you've decided to get some use out of your home computer by making it run the household appliances and the robot, this little board can do it all for you and prevent tying up your home computer and now you're back where you started trying to find a use for the old Atari. Maybe you could program it to write song lyrics - they'll probably be better than most of the stuff around now. Developing one-time circuits no longer means a custom PCB or a tangle breadboard. Software for the board can be developed on an Apple or IBM.

Least I lead anyone astray with raving on here, this board is not an instant solution to your interface problems; Nicologix assumes that you have a working knowledge of the CPU and its support chips, and that you understand the wants and needs of the circuits you're trying to interface.

But assuming you're good at logic circuits, this little card should save you no end of time and trouble when you're inventing your concoctions. I should have that printer interfaced to my wristwatch in no time. It's available at Exceltronix, 319 College St., Toronto, (416) 921-8941, or contact:

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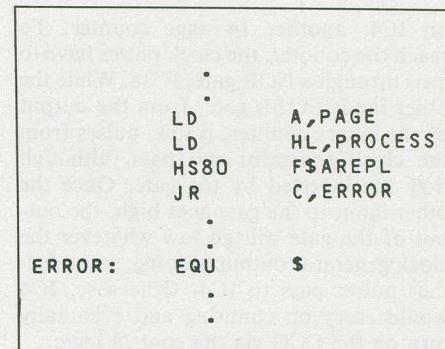
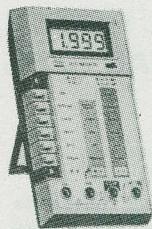


Fig. 2 An example from the manual showing the simple code used in the MTHS system.

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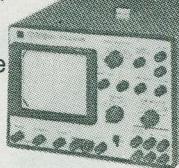


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on IC4, another 14-stage counter. To reach the counter, the clock pulses have to pass through a NOR gate, IC3a. While the other input to this gate, from the output of the binary counter, is low, pulses from the clock generator can pass, although they are inverted by the gate. Once the other input to the gate goes high, the output of the gate will go low whatever the clock generator output is doing, so no further pulses pass to IC4. Otherwise, IC4 would carry on counting and eventually turn on the CCG via the control logic.

**Construction**

The prototype was built in a metal box with the PCB mounted on plastic pillars. The power transistor, Q4, becomes quite hot when fully loaded. This has to dissipate up to 20 watts, so it will require a heatsink. This can be achieved by bolting Q4 to the bottom of the box, using a suitable insulator and heatsink compound on both sides of the insulator.

**Testing**

The voltage across C1 should be 18 to 25 volts and the red LED should be illuminated. The voltage across ZD1 should be zero when SW2 is OFF, and about 3.9V in the ON position. The yellow LED should illuminate in the ON position.

**PARTS LIST**

Resistors	
(All carbon film $\frac{1}{4}$ W 5% unless noted)	
R1	2k7, 2%
R2	22k
R3	1k
R4	5.6k
R5, 6, 7	1.2k
Capacitors	
C1	2200u, 25V electro
C2, 3, 6	1u, 35V tantalum
C4	.5n6, 2% polystyrene
C5	.22n polyester
C7	.47u, 6V3 tantalum
C8	10u, 25V tantalum
Semiconductors	
Q1, 2	BC107
Q3	BC184C

Q4	TIP41C
IC1	555 timer
IC2, 3	4020
IC4	4001
IC5	7815
D1-4	IN4001
ZD1	3.9V, 400mW Zener diode
LED 1-4	.02in red, green and yellow
Miscellaneous	
T1	18-0-18V, 1A mains transformer
F1	800mA quick blow fuse
SW1	SPDT
SW2	DPDT
SW3	single pole, six way rotary (See text)

Metal case to fit; 4mm wander plugs and sockets; heat sink for Q4; PCB pillars, nuts and bolts; cable gland for mains cable; PCB, wire, solder etc.

Check that the calculated values of current are flowing through the battery terminals by placing an ammeter across them.

To ensure that the timing circuit is working without having to wait for the full charge time, calculate a value for Rx so that the time is one hour, for example

(try 47K and 2K2 in parallel). The switch S1 to the half charge position and start the charge using SW2. The yellow LED should light and charging current flow for thirty minutes and then stop; the yellow LED goes off and the green one flashes to indicate a charge. Now replace Rx with the proper value.

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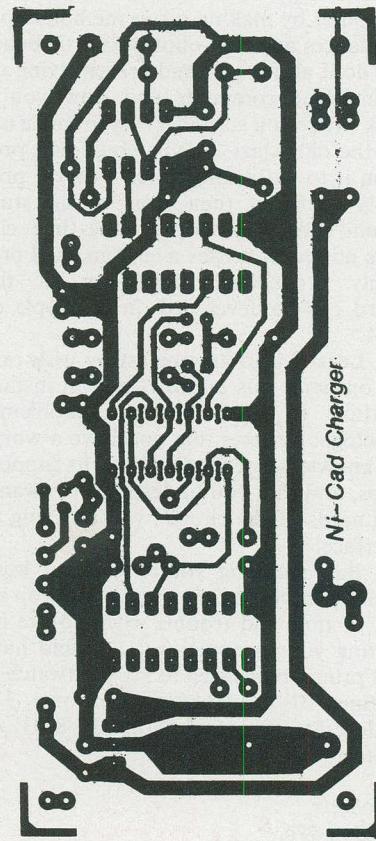
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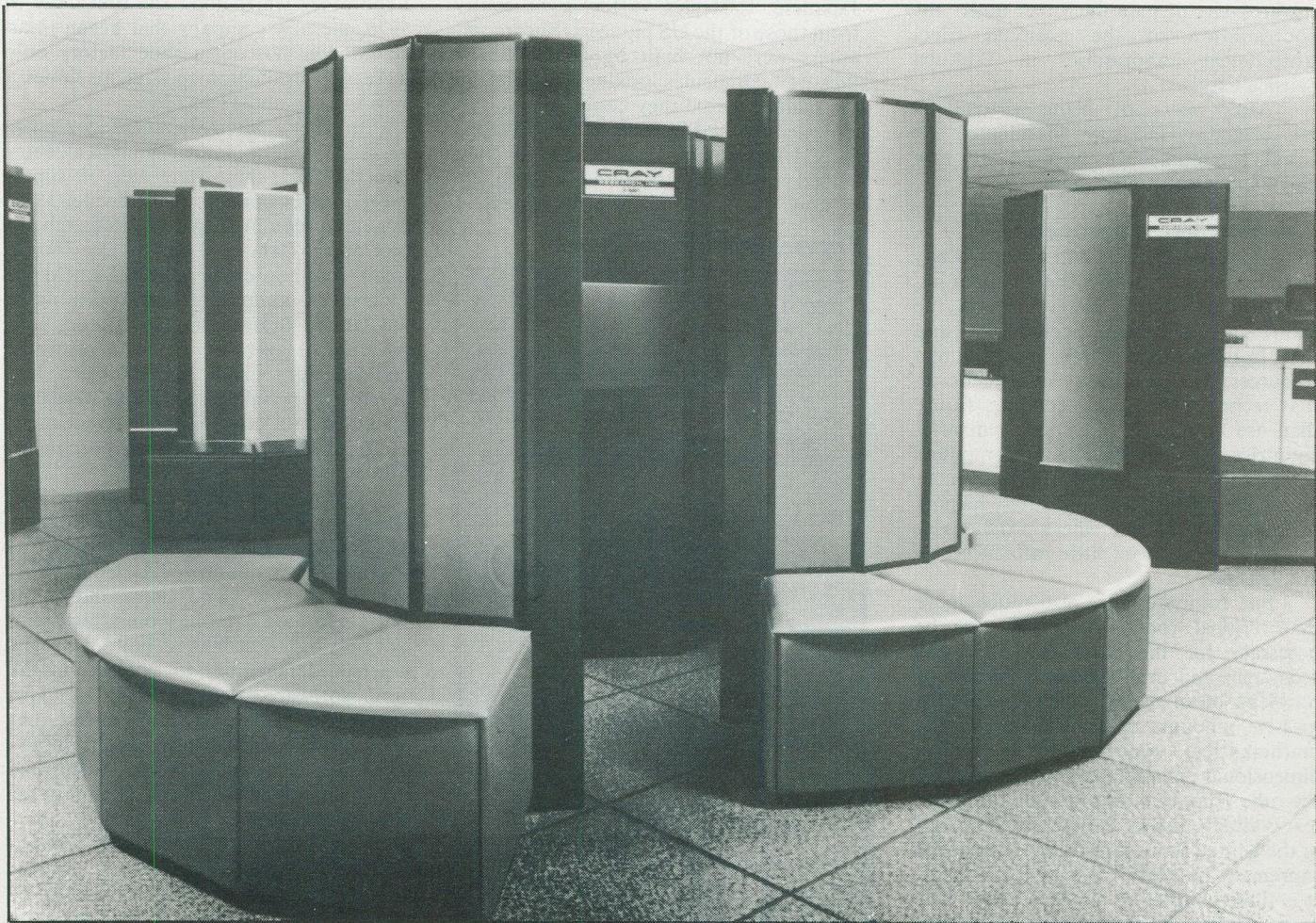
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# Supercomputers



The Cray X-MP series of supercomputers is among the most powerful available today. Photo courtesy of Cray Canada Inc.

WHEN the Japanese National Superspeed Computer Project got under way in January, 1982, few would have believed that just three years later it would be among the highest priority items on the US-Japan diplomatic agenda discussed by President Ronald Reagan and Prime Minister Yasuhiro Nakasone at their California summit.

At the time of their meeting in January, 1985, there were still scarcely more than 130 supercomputers in operation worldwide. Less than ten of these had been built by Japanese computer makers, and virtually all the rest were products of two American firms, Cray Research, Inc., and Control Data Corporation (CDC), a duopoly which had dominated world markets since the first ultra-highspeed number crunchers were built at the beginning of the 1970s. About 70% of the supercomputers then in operation were supplied by Cray Research.

## ***Ultra-highspeed computers can run at 400 million operations per second, and the small market is turning into a US/Japan sweepstakes.***

**By Gene Gregory**

The immediate problem which had been raised at earlier technical meetings between representatives of the two countries was access to Japanese markets, particularly universities. But in the background loomed a larger issue: the Japanese supercomputer project, not to

be confused with the Fifth Generation Computer Project intended to develop artificial intelligence, had set for its specific objectives the design of machines with a sustained execution rate of about 10 billion floating-point operations per second (gigaflops), or 100 times faster than American computers and ten times faster than the projected peak speed of the next generation of American machines.

### **Gigaflops**

The measure of computing speed, the Flop, refers to the binary version of representing scientific notation, in which a number is represented by a mantissa between 0.1 and 1 raised to an exact power of ten. A single Flop is arithmetic between two floating point operands to get a floating point result.

Speed in the 10-gigaflop range would be attained by using entirely new semiconductor devices, distributed parallel-

processing architecture, one billion bytes (a gigabyte) of memory and a memory bandwidth of 1.5 gigabytes per second. To meet these specifications, the Scientific Computer Research Association was established, combining six leading integrated circuit manufacturers under the coordination of the national Electrotechnical Laboratories in Tsukuba Science City.

After years of being virtually a one-company industry (dominated by Cray), the market was teeming with new players, projects and funding.

### The Japanese Approach

Japanese policymakers had unequivocally acted on the premise that the wealth of nations will be largely determined in the future by information technology, and that, in the absence of other than human resources, Japan is more dependent on that technology for its economic future than are most other advanced industrial countries. New generations of computers will therefore serve as the prime movers in an increasing range of industries, helping to improve efficiency where productivity has in the past been little influenced by automation.

But computers are also much more than critical tools; they will also be necessary for the management of the ecosystem and dealing with a myriad of problems of an ageing society, processing massive amounts of data to aid in the mathematical simulations of multi-dimensional physical phenomena. Here is the rub. What in a techno-economic context is vital to the security of Japan is seen in the United States as a threat to national supremacy in critical military technology. It is these conflicting interests, then, that have resulted in the current US-Japan confrontation. Japanese leaders have made clear their intentions to push forward the state of the art in supercomputing as well as Fifth Generation technology. The intention is not only to assure the competitive strength of Japanese industry and the wealth of the nation, but to contribute to the forward march of human progress.

However, the Japanese do not see their action as replacing US dominance in supercomputers, or as a threat to US security; given the high technological level of the Japanese electronics industry, the development of supercomputer production capability is seen rather as a natural course of open worldwide competition.

Japanese computer makers take seriously the expressed confidence of Cray executives that they will be able to match Japanese advances in supercomputers; the existing park of Cray supercomputers constitutes a formidable advantage in the competitive marketplace. There are also few illusions among Japanese leaders that

they will be able to penetrate the US government market, which has accounted for the bulk of supercomputer sales in the past.

### Strategies

Japanese strategies differ significantly from those of the US pioneers. Supercomputers were not built by IBM, but by "dwarf" companies looking for niches in the market where they could survive unmolested by the Big Blue. When Seymour Cray left Control Data, which he had helped create but later found to be unable or unwilling to sustain a commitment to

### Supercomputer Primer

**Supercomputer:** A computer which can process data at 20 megaflops or higher, but the fastest run at a sustained 400 megaflops and a peak of 1 gigaflop or higher. In future, supercomputers may use multidimensional arrays with many parallel operations being performed in a pipelined way. Over the last 30 years, computer speed has tended to double each year.

**Flops:** Floating point operations, a measure of computer speed. An operation is arithmetic performed on two floating point operands to get a floating point result. A megaflop is one million operations per second, and a gigaflop is one billion.

**Scalar Processing:** Operating on individual data elements with instructions that yield one result for each instruction. To operate on all elements of an array, for instance, the computer loops through the table of numbers, repeating the same instructions on each.

**Vector Processing:** Uses only one instruction to perform calculations on an array of data all in one operation. In supercomputers, the more data vectorized, the faster the operation. Vector processing is usually not available on mainframes.

**Array Processing:** Does not include scalar processing. Processors are usually peripheral devices, allowing mainframe users to run vectorized portions of programs.

**Parallel Processing:** Two or more processors performing simultaneous operations in a single task. Supercomputers may use vector processors in tandem with scalar processors, dividing each task for optimal use of the processors under central control; they may or not be pipelined.

**Pipelining:** Speeding computer operations by breaking down instructions into discrete steps for processing in an assembly-line system, with different steps in the execution of an instruction performed simultaneously. Vector registers, or high-speed memories, store instructions and feed them to the pipeline at a speed greater than calling instructions from the main memory.

supercomputers, he originally intended to build only one of his projected giant number crunchers a year for scientific purposes. He saw no great market for the machines, and even after favourable response from the Los Alamos National Laboratory which took the first Cray in 1976, the new company that began in an abandoned Wisconsin shoe factory only increased production to four machines a year in 1978 and 13 in 1984.

In 1982, when Japanese computer makers announced their imminent entry into the field, there were only 50 supercomputers in operation worldwide. 35 of these had been built by Cray Research, 14 by Control Data, and one by Denelcor, a small Colorado builder. Fully 38 of those superspeed computers were operating in the United States for Los Alamos and government agencies. It was a restricted market, for which the builders had developed special machines that were programmed in a completely different manner from standard general-purpose mainframes.

The new Japanese entrants, Fujitsu, Hitachi and NEC, are major highly-integrated companies producing a full line of computers from micros to mainframes, and are at the same time among the world's top ten computer manufacturers. Not only did these makers build their new superspeed machines to operate on the same software used by their standard mainframe models, but this meant that both Fujitsu and Hitachi would supply machines compatible with mainframe models built by IBM, which had not so far given any sign that it would add supercomputers to its product line. By producing IBM-compatible machines, Fujitsu and Hitachi offer the widest number of computer users at home and abroad the option of moving to more powerful machines without heavy expenditures for time and money for adapting special software.

### The Need

The global strategies of Japanese makers are based on other elements that have not weighed as heavily in plans and operations of Americans.

Firms in a widening range of high-technology industries are beginning to feel the need of ultrafast computer processing capabilities. Advanced graphics, computer-aided design, and distribution systems all require massive volumes of data, necessitating a processing improvement of two to three orders of magnitude during the current decade.

Supercomputer development is itself linked with the design of Very Large Scale Integrated circuits. They require devices with increasingly faster switching speeds, speeds which can be obtained only by developing entirely new technology.

By effectively responding to worldwide demand, Japanese makers see the possibility of cutting prices sharply; industrial users are likely to be more sensitive to lower prices.

### The Near Future

Supercomputers still occupy the niche that first generation computers held in the 1950s. Those earlier machines cost even more, allowing for inflation, and were so powerful that only a very special group of users could find applications for them. Yet in the US there were 13 computer manufacturers, with Remington Rand in the lead.

By comparison, the worldwide supercomputer park at the beginning of 1985 had reached 130, with only nine installations in Japan. By 1990, it is estimated that at least 100 will be operating in Japan, about the same number installed worldwide in 1984. World market estimates for 1990 vary widely from 400-500 machines to as many as 1000. Not surprisingly, the higher estimates of demand come from the Japanese, who are aiming at a broader range of users.

### The Machines

Fujitsu has concluded arrangements with the Amdahl Corporation, in which it holds a 49 percent equity, to market its supercomputers in North America and Europe. Initial benchmark tests in the US indicate that sustained throughput, consisting of both CPU and input/output speed, of the Amdahl 1100 and 1200 (identical to the Fujitsu VP-100 and VP-200) compares favourably with the competition. The 1100 sustains speeds of 175 megaflops and the 1200 300 megaflops. The 1100, in sustained throughput, is 1-2 times faster than the Cray X-MP uniprocessor and twice as fast as the two-pipeline Control Data Cyber 205, while the 1200 is about as fast as the Cray top-of-the-line X-MP two-way processor.

While some computer scientists discount the advantage of IBM compatibility, pointing out that if a customer is prepared to vectorize the scalar software code, the same results can be obtained on a Cray with an IBM up front, others assert that programs developed from the start for vector processing on supercomputers will run 20 to 30 times better than scalar converted to vector.

Hitachi has followed the Fujitsu strategy, supplying National Semiconductor's subsidiary, National Advanced Systems (NAS), on an OEM basis. In 1982, Hitachi introduced the two-model S-810 series, one with speeds up to 630 megaflops, and the other rated at 315 megaflops. In Europe, Hitachi-made supercomputers are being offered by NAS, Olivetti and BASF as vector pro-

cessing extensions of Hitachi-built mainframes.

NEC is following a different strategy. While its new SX series is compatible with NEC mainframes, neither are IBM-compatible. "IBM architecture is 20 years old," NEC manager Akihiro Iwayo points out. "To assure IBM-compatibility some performance compromises are necessary, which NEC chooses to avoid." As a result, NEC surpasses both Hitachi and Fujitsu, unveiling machines with operating speeds up to 1.3 gigaflops, or ten times the speed of the Cray 1S and twice as fast as the X-MP two-way processor, which has a peak 630 megaflop throughput.

### Architecture

Both NEC machines, the SX-1 and SX-2, feature logic LSI with a density of 1000 gates per chip and delay times of only 250 picoseconds. The SX system utilizes unique, high-density packaging with 36 LSI chips mounted on a substrate 10 cm square, achieving higher speeds due to shorter pathlength. Cooling, which is a critical factor in supercomputer design, is a direct liquid system within the LSI packages, but the cooling unit itself is air cooled and needs no special equipment. High speed main memory elements with a maximum data supply rate of 11 gigabytes per second is supplemented by an extended memory, offering significantly higher performance than obtainable with magnetic disks.

The architecture of the world's first giga-level machine is based on multiple parallel processing using four sets of four vector pipelines, enabling a maximum of 16 parallel vector operations to be performed simultaneously. Although the SX-2 is ten times faster than the Cray 1S, the price is substantially the same.

Both Fujitsu and NEC use an advanced type of Fortran 77, a compiler that can vectorize standard Fortran programs. Since 20 to 80 percent of Fortran programs can be vectorized, this approach in the main software issues will likely prove advantageous, since no special operator training would be required.

### The Future

The six Japanese semiconductor manufacturers have joined in the National Superspeed Computer Project. The goal is to produce a computer capable of sustained processing speeds of at least 10 gigaflops by 1989, one year before the completion of the separate Fifth Generation project.

The super-cooled Josephson junction, developed in 1983, is unlikely to be selected for supercomputers; despite its 7 picosecond delay, it requires special packaging and maintenance, and must be operated at a temperature near ab-

solute zero. Much more likely are gallium arsenide (GaAs) devices. Jointly developed by the project, it features high speed, low power consumption, and a large number of gates.

From 1985 to 1989 the focus will be on development of the actual supercomputer, stepping up work on distributed parallel processing architecture and related software. What will happen after 1990 is anyone's guess. Chances are good that by 1990 the three Japanese supercomputer makers will have as high as 50 percent of the world market, likely to be at \$2 billion a year and climbing. The US will not be standing still; Cray will remain a major power in the marketplace for the foreseeable future, and IBM may enter the market as it reaches volumes approaching \$2 billion. National projects in the UK, France and Germany will probably bring new actors onto the scene. It remains to be seen whether any future inventions will emerge to bring about a significant departure from the revolution.

*Dr. Gene Gregory is a professor of business at Sophia University in Tokyo.*

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**Direct reception of satellite relays means an enormous choice of video and audio programs; buy a complete system or build your own.**

**By Bill Markwick**

IF you'd like a wider choice of programs, or if you live in an area where cable is not available, you can receive the broadcasts from satellites that are used to relay network programming across the continent. You might expect the received signal to be feeble, and it is; you might also expect that some sophisticated hardware is required, and that's true as well. It's a bit more complicated and expensive than the average rooftop antenna, but the popularity of the systems is bringing prices down. You can also build your own; various companies supply kits and assistance.

#### **The Basic Idea**

Networks use satellites as relay links; if you have sensitive enough equipment, you can receive and decode the satellite signal. This obviously lets you in on a wealth of commercial-free programming.

The catch is that the signal is understandably small; the satellite may cover most of Canada using a transmitter with about ten watts of power. In addition, the signal is in the microwave region; satellites receive the ground signal at about 6 gigaHertz and rebroadcast it at about 4GHz (for audio fans who feel that the universe ends at 20kHz, a gig is 1000 megaHertz). There are some regional broadcasts from Canada's Anik that use the 14/12 gig band.

A whole lot of signal has to be collected with a parabolic reflector eight or ten feet in diameter. The signal is concentrated into a *feedhorn* which guides it into the low noise microwave amplifier, or LNA, which has about 40-50dB of gain. As you can imagine, a 4 gig signal isn't too easy to handle, particularly with long cable runs, so it's immediately sent to the *downconverter* to be changed to a band of frequencies surrounding 70MHz. This is sent by coaxial cable to the receiver unit indoors. The receiver extracts the particular station you want and sends the RF signal to your TV.

Nothing to it, except maybe cost and a big dish sitting in your backyard.

#### **The Options**

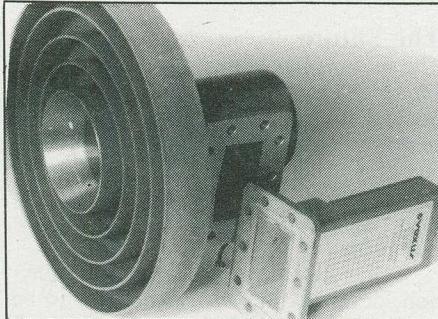
Just like buying a car. The jazzier

# **Satellite Rec**



receivers have microprocessors in them to eliminate knob-twiddling and simplify changing channels. There are remote control units to run the receivers; these are similar to the familiar cable-TV handheld remotes. Some receivers have stereo decoding for audio signals.

Because the satellite signal is so small, it's necessary to aim the dish accurately when changing to another satellite, and you may not fancy going out in the cold and wet to do this manually. There are



*Fig. 1 A feedhorn assembly which guides the signal into the Low Noise Amplifier shown with it; equipment courtesy of Vexus Telecommunications.*

lots of actuators available. It's a servomotor that rotates the dish on its mount under control of a microprocessor that sits near the receiver. Some just move the dish until your picture looks good, and the more expensive ones can be preprogrammed with satellite locations.

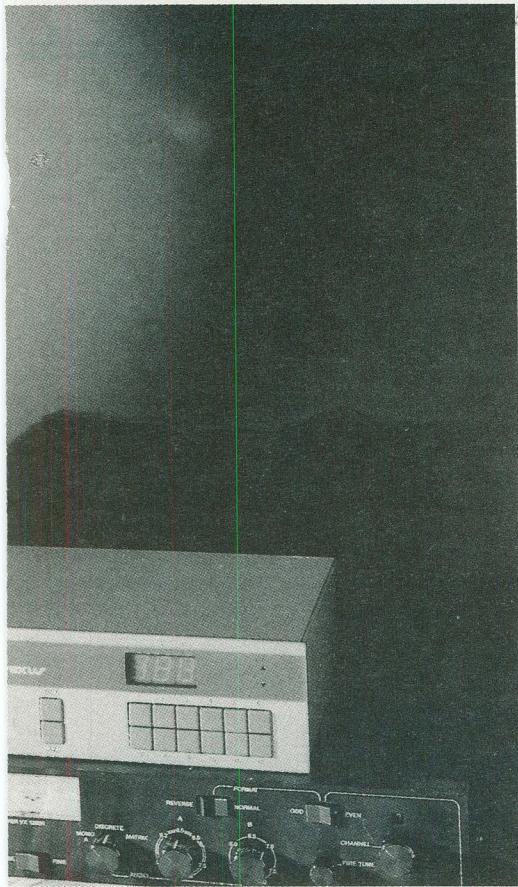
The number of bells and whistles naturally send the price up rapidly, so it all depends on how much you want done by gadgetry and how much you're willing to do by hand. One place where you probably shouldn't scrimp is in the dish and mount; a large dish is subject to immense wind loading, and even if it doesn't blow over, it may be easily knocked off optimum positioning.

There are lots of dealers around now who can advise you on complete systems that can be made to work right away. But for those of you with a sense of adventure, how about building your own? You do have a sense of adventure, don't you?

#### **An Adventure**

One of the most costly items in a satellite reception system is the dish. If you're reasonably good at basic carpentry and

# eption



can either do welding or get it done somewhere, Sothis Satellite Systems of New Brunswick will send you an instruction manual and blueprints for a dish that you can make yourself. It's rectangular rather than round ("spheribolic"), and is constructed largely from plywood ribs covered with mesh; the mount is made from angle iron. Its final size is 8 feet; its gain is said to be 36dB and its efficiency 68 percent. Studio-quality reception is claimed. In addition, the see-through mesh has a better look than a solid dish, and it's also more tolerant of wind loading.

We obtained a copy of the instructions and blueprints and found them to be well-written. There's a large blueprint for the wooden frame and another for the mount; both include comprehensive parts lists. Mind you, there's one spec I bet it can't live up to: it's claimed that the antenna can be assembled in four hours. Obviously the Sothis company has never seen me at work; I decided to renovate my house two years ago, and today it's still a patchwork of holes, plaster, drywall bits, and pink insulation. Now that I have the plans, maybe I'll have a go at building my

own dish; I'll let you know how it turned out by early 1996.

If you're itching to get at the power saw, the manual and drawings can be had by sending a money order or bank draft for \$24.95 to:

*Sothis Satellite Systems,  
Box 6637, Station A,  
Saint John, New Brunswick  
E2L 4S1*

## **Electronics**

Having assembled your dish, you may want to mop your brow and just head down to the satellite store for all the rest of the stuff. On the other hand, that thrill of accomplishment may make you want to go whole hog and take a stab at building the electronics, which will mean a Low Noise Amplifier and a receiver unit.

If this appeals to you, *Electronics Today* published a do-it-yourself project for the LNA, the downconverter and the receiver, written by Ron D.C. Coles and appearing in July and August, 1983 (back issues are available; see the Order Form in this issue).

This project is not for the faint of heart. The signal presented to the LNA by even a large dish is typically only 6dB above the thermal noise that exists in all circuits courtesy of Mother Nature. It's readily apparent that any amplification has to be done by gain stages which add an absolute minimum amount of their own noise. The usual choice is cascaded GaAsFETs mounted on a special microwave-grade PCB; the project features plans for this type of amplifier, which has a noise figure of 1.5dB. Noise performance for LNAs is usually given in degrees Kelvin; 1.5dB corresponds to 120 degrees. Better performance than this is possible, but the exotic techniques required will mean an exotic price tag.

If you're a bit nervous about building circuits that whiz along at 4GHz, and after all, the known universe *does* end at 20KHz, Ron received enough feedback from the article that he has now started a company to assist the constructor as well as supply complete systems. He can solve supply problems for you; some of the

high-frequency components aren't available at your local Radio Shack, to say the least. So, if you're game, contact:  
*Colesat Systems,  
RR 2,  
Tantallon,  
Nova Scotia B0J 3J0  
(902) 826-2875*

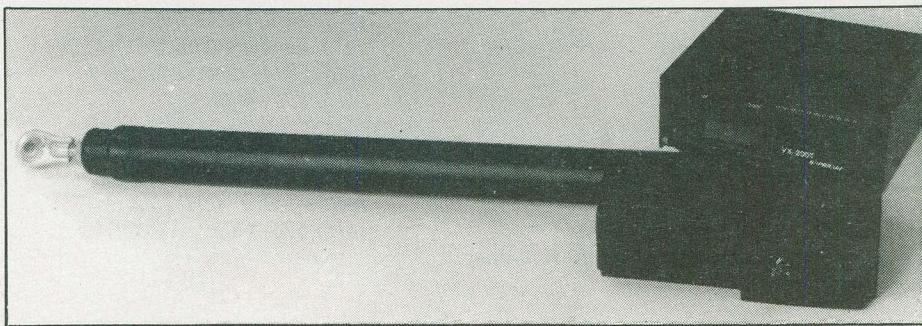
## **Other Features**

Radio stations use satellite services as well; there are stereo stations available on subcarriers from most satellite transmissions. Some specialize in particular types of music. Needless to say, you need a receiver with the necessary decoding option to hear the audio subcarrier.

A question that comes to mind: why should broadcasters let you have all this commercial-free TV? There has been some talk of scrambling the transmissions, but nothing firm has emerged so far. Some of the channels have at least a few commercials on them, so it isn't in their interest to scramble, and for the others, the number of people with satellite dishes may be too few to make it worthwhile. What they worry about is someone using a dish to obtain movies and so forth for commercial purposes, but this is illegal anyway, since the Canadian Radio-television and Communications Commission rules say that you can have a dish for your own use, but you can't distribute the signal to others without a licence, and they probably won't give you a licence.

The smaller dishes are also generating some interest; nobody wants a ten-foot monster if a three-footer will work. There's the vague idea around that dishes and electronics have improved to the point where the three- and four-footters are adequate replacements for the larger dishes, but this isn't 100 percent true. A smaller dish receives a smaller signal, period. There's a limit to how good you can make the electronics, since signal-to-noise is largely controlled by the laws of physics rather than engineering. However, if you live in an area that receives a strong signal, or you're tuning in to the newer more powerful satellites, or you don't mind doing without some of

*Fig. 2 An actuator and control panel for remote adjustment of the dish position.*



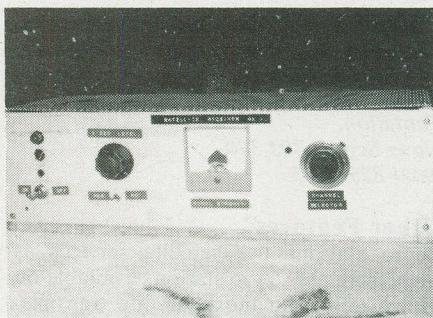


Fig. 3 The original receiver developed by R.D.C. Coles and described in one of our projects (see text).

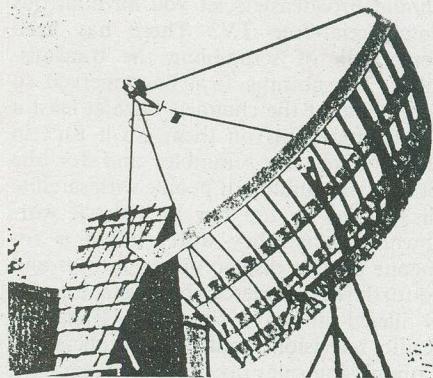


Fig. 4 A do-it-yourself dish antenna from Sothis Satellite plans.



Fig. 5 The angle iron mount from Sothis Satellite plans.

the weaker broadcasts, then the smaller dish is adequate. And, of course, a well-designed small dish beats a poorly manufactured large dish any time.

There are a number of publications around dealing with satellite reception. If you'd like a Canadian magazine, there's *SIGNAL*, from Fennwood Publishing

Ltd., Box 238, Station D, Scarborough, Ontario. It's published six times yearly and available on newsstands, and deals with general aspects of satellite reception. If you'd like a more technical guide, a book stuffed full of goodies is the *Satellite Experimenter's Guide*, published in 1984 by the American Radio Relay League and dealing with TV-broadcast, amateur radio, and weather satellites. If you can't find it in specialty stores, you can contact them at the ARRL, 225 Main St., Newington, CT 06111. It costs \$11 US.

### Sources

Here are some of the manufacturers and distributors who can supply you with systems, components, and advice. Our apologies to anyone who didn't get listed through oversight.

**Delta Satellite Systems,**  
40 Rivermede Rd. N., Unit 1,  
Concord, Ont. L4K 2H3  
(416) 736-0930

**Sigmacom Consumer Products,**  
111 Industrial Drive, Unit 6  
Whitby, Ontario,  
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**Pulsar Communications,**  
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Saskatoon, Saskatchewan  
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**Geocom Inc.,**  
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**Paco Electronics Ltd.,**  
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**Gensat Communications Corp.,**  
951 Alness St.,  
Downsview, Ont. M3J 2J1,  
(416) 736-4555

**MSE Systems Ltd.,**  
2015 West 12th Avenue,  
Vancouver, BC V6J 2G3  
(604) 736-1141

**CSE Satellite Equipment,**  
200 Hamford St.,  
Lachute, Que. J8H 3N8,  
(514) 562-3741

**Cale Communications,**  
693 Henderson Dr.,  
Regina, Sask. S4N 6A8,  
(306) 949-9181

**Evolution Technology,**  
1155 Appleby Line,  
Burlington, Ont. L7L 5H9  
(416) 335-4422

**Commander Satellite Systems,**  
309 Steeles Ave., E,  
Milton, Ont. L9T 1Y2,  
(416) 876-4707

**Columbia Satellite Systems,**  
2080 - 21 St. NE,  
Calgary, Alberta T2E 6S5  
(403) 250-5271

**Vexus Telecommunications,**  
2240 Argentia Rd.,  
Mississauga, Ont. L5N 2X6,  
(416) 821-8900

**Haussen Tech Inc.,**  
PO Box 2126,  
Moncton, NB E1C 8H7  
(506) 534-2530

**MISAT Corp. (Fanon),**  
20701 Langley Bypass, No. 4,  
Langley, BC,  
(604) 534-4104

**Petphil Electronics Inc.,**  
254 St-Anne St.,  
Senneville, Que. H9X 1N8  
(514) 457-1N8

**Communications Gigahertz Ltd.,**  
4649 Des Grandes Prairies,  
St-Leonard, Que. H1R 1A5,  
(514) 323-0030

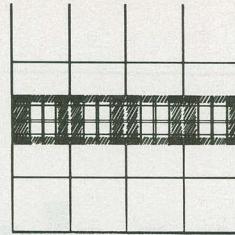


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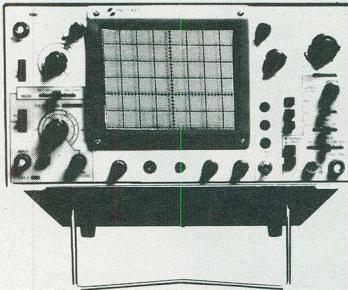


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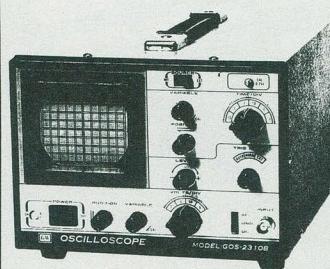


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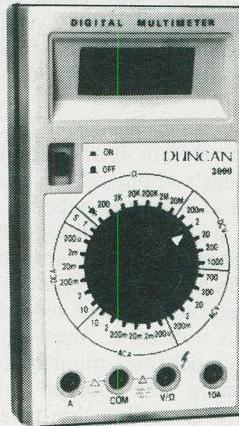


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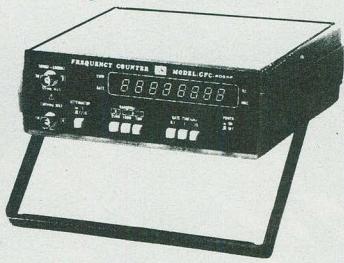


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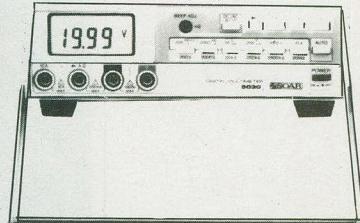


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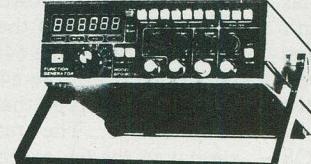
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6-digit display

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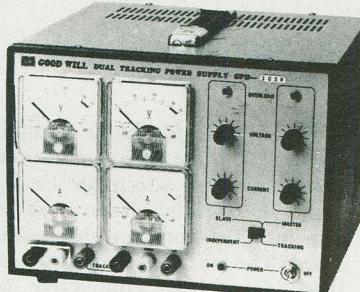
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# Commodore 16



**Commodore offers a smaller version of the popular C64 for two hundred dollars, featuring 16K and a powerful BASIC.**

**By Bill Markwick**

THE CRAZE for home computers has levelled off these days, and if you haven't bought one yet, you might be wondering about the various plastic boxes that you can just see through the settling dust cloud. The emerging standard seems to be IBM, or at least compatible MS-DOS machines, and Apple appears to be second favourite. Both of these machines and

their attendant clones are still a bit pricey, and the next one down the list would be the Commodore 64. These aren't particularly expensive, but if you're still afraid that you might not be able to find to get your money's worth out of a home computer, CBM now has the model 16. It's powerful enough to do a fair amount of programming, plus cheap enough that it can gather dust in the closet without causing twinges of regret. However, it's a lot more fun to use it:

#### Using It

If you're familiar with the C64, you'll notice right away that the 16 is in the same case, though its power supply is a tiny plugpack instead of the large box that sits in the middle of the 64's power cord. The connector is not the 64 DIN-type, either, but the sleeve type that you find on portable radios. The rear connectors are similar to the 64, except that there are no edge connectors for the cassette port and user port; the cassette port is now a tiny

circular 7-pin jack. The plastic connector remains, now labelled "memory expansion".

The right end looks about the same except for the smaller power connector and the addition of a Reset button; the two joystick ports are still there, now marked "Joy 1" and "Joy 2", which should puzzle archaeologists in the future who may wonder about the machine's intended function.

You can use either a TV set (RCA jack output, channels 3 or 4) or a monitor (DIN socket). That's about all the hardware you can get at from the outside.

On booting it up, you'll notice that the screen is 40 characters by 25 lines and that the characters are a bit different than the 64's; somewhat blockier, but very easy to read. The 64 is noticeably fuzzy when plugged into a monochrome monitor and requires eliminating the colour with POKE 53281, CRTL-2. You don't have to do this with the 16; the display is crisp and clear.

You'll also notice the message that only 12,227 bytes are available; some memory is lost to the 16's operating system. The 12k should be more than enough for most BASIC programming and for running small games; a memory expansion module plugs into the back if you'd prefer something of a safety margin.

### BASIC

The 16 boots with the announcement that it contains Version 3.5 of Commodore's BASIC. Just in case you're using it with a TV set that snips off a bit of the image, you can use ESC R to reduce the display to 38 columns. Arg — get a monitor.

The included manual gives only the barest of introductions to BASIC; it rattles off a few things like PRINT and SOUND and COLOUR in a half-dozen pages. However, CBM has more advanced manuals available; presumably the idea is that most users will run ROM packs and so forth, and the minority who want to create programs will have to buy the manual.

The first BASIC function in the manual, after the usual introduction, is SOUND. SOUND 1,266,60 is the syntax, and in order, selects one of three voices (the third is white noise), the next is the pitch, and finally the duration. The note

A (number 770) works out to 440.4Hz, which ought to be close enough to concert pitch to please anybody. The VOL command, from 0 to 8, sets the volume of the output. The output is, of course, a square wave, giving that familiar robotic drone. It's suitable for jazzing up your game programs.

The next tiny section deals with graphics, and these functions are certainly comprehensive. You can have the familiar block graphics marked on the front of CBM computer keys, or you can invent your own via DRAW, CIRCLE, BOX, etc.

There are 16 colours available and you can set the border, characters, and background. Again, you'll need the more advanced programming guide to take full advantage of these.

A memory expansion may well be necessary if you want to use the snappy graphics; they take about 10k of memory for themselves, leaving you with diddly for programming.

The BASIC ROM contains some upgrading compared to previous CBM versions. You'll find PRINT USING for formatting the display, AUTO for line numbering, and RENUMBER for getting rid of those odd numbering sequences that testify that you didn't program it right the

first time. There's also a DO loop; this gives you about the same function as the familiar FOR-NEXT, except that it loops until a specified value occurs somewhere instead of a fixed number of iterations.

There's also a monitor, a real live machine code monitor. Type MONITOR and you can assemble and disassemble code, load and save machine language programs, edit memory, move and compare memory, and check out what's happening in the various registers. The CPU is an 8501 which is similar to a 6502 which is similar to the 6510 in the C64.

### Peripherals

The first peripheral you'll want is probably some kind of storage system. Commodore offers the 1531 Datasette, the 1541 disk drive, and the 1551 disk drive. The cassette is a low-cost alternative to disks, but it's as slow as an arthritic turtle. The 1541 disk drive is only a little bit faster; you can expect one or two minutes to go by when you load a program. The manual lists a few other related CBM products: their line of printers, the 1700 series colour monitors, etc. They also mention the Commodore 16 Intermediate User's Manual and the Series 264 Programmer's Reference Guide, manuals well worth having.

## PRODUCT MART

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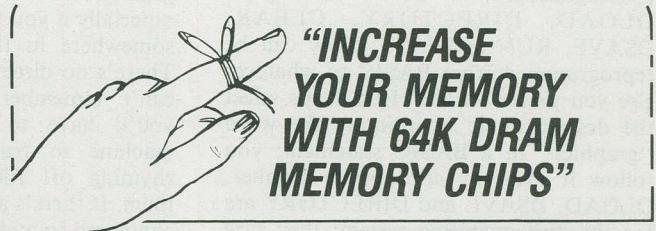
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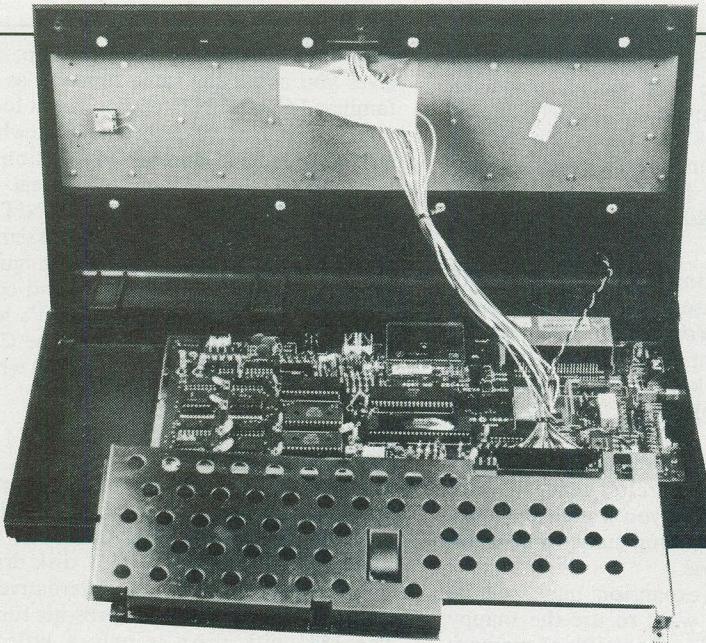
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*The interior of the Model 16 with the RF shield removed.*

### Other Features

There's a HELP key nested in with the function keys. When you encounter a syntax error, this key will write the offending line to the screen in flashing type. Not too useful, since the computer has already told you which line is wrong.

The other function keys are preprogrammed to hold GRAPHICS, DLOAD, DIRECTORY, CLEAR, DSAVE, RUN and LIST. They can be reprogrammed from BASIC to whatever else you prefer. GRAPHICS helps select the desired mode by printing the word "graphics" in a BASIC statement; you follow it with the appropriate number. DLOAD, DSAVE and DIRECTORY are for the disk operating system; they sure beat the previous Commodore system of typing in a string of symbols to list the directory or load a file.

Most of the other keyboard features of the 64 are on the model 16. Besides the large selection of graphics characters, there are the screen colour control keys, flashing and inverse video, plus Commodore's excellent cursoring and full-screen editing.

### Software

There are three possibilities here. The first

is to purchase and plug in a ROM pack; on powering up, it takes over the computer and runs whatever's contained in it. Simple enough. The second is the 1531 Datasette which plugs into the rear of the computer and doesn't require any external power. This is the lowest-cost, simplest way for the beginner to load and save programs, but it's frustratingly slow, especially if you have to try and find a file somewhere in the middle of the tape. There's no directory in one place; if you can't remember the filename exactly, you'll have to wait for the cassette machine to trundle through the tape rhyming off filenames as it comes to them. If there's any advantage to the 1531 compared to audio recorders, it's that the motor is controlled by the computer and about all you have to do is press Play or Rewind as instructed.

The 1541 disk drive is the next step up; it's much faster than a cassette tape, though it's still molasses compared to an Apple or IBM drive. It also costs more than the computer, and you can't graft on another manufacturer's drive because the Commodore operating system is unique.

### Compatibility

As mentioned, there's no problem with

CBM peripherals such as the disk drive, and the CBM works with either a TV or a monitor, monochrome or colour. Software is another thing. The tiny memory means a different layout from its predecessors, as well as the more obvious problem that large programs simply won't fit; the owner of a 16 will have to check carefully when offered software that isn't specifically designed for it. Incidentally, the general trend in memory devices is more for less, so it's a good question why CBM sees fit to put only 16k in the computer. Maybe they want to move more expansion packs.

### Generally

At two hundred dollars, or even less if discounted, the 16 is a good deal, with its comprehensive BASIC and compatibility with CBM hardware. Keep in mind that you'll need a proper manual and probably a memory expansion cartridge. It isn't a machine for the serious computer addict, but it looks ideal for the beginner.

### Quick Reference

Commodore 16

CPU: 8501

RAM: 16k

User RAM: 12k

Storage: optional disk, cassette

Video: 40 by 25

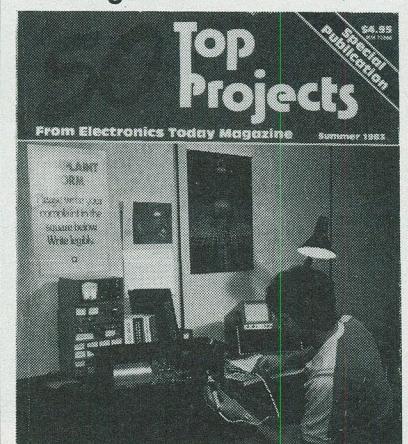
Audio: 3 voice music tones

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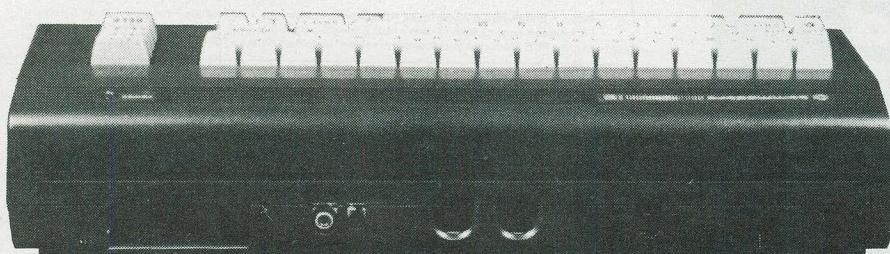
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*The rear of the 16 is similar to the 64, but lacks one user port.*

# Computing Today

## Designing Z80

### Projects, Part 1

**Create practical projects with the ever-popular, low-cost Z80 CPU.**

THIS SERIES takes a look at a practical approach to designing Z80 projects. Connecting ROM, RAM, and I/O ports will be discussed as well as reliable circuits to interface the Z80 CPU to the external world. First let's take a look at the Z80 CPU itself.

#### Z80 Inputs

The power inputs, GND and VCC, should be connected to a regulated 5V power supply such as the one shown in Fig. 2, utilizing the popular 7805 regulator. This power supply can be used to power TTL as well as other microprocessor chips. It is important that a 0.1uF bypass capacitor be connected across the power inputs of each VLSI chip and one for every 5 TTL chips, eliminating any sporadic failures caused by power spikes induced from the high-speed switching elements of the CPU. Also, if the power supply is mounted off the CPU board, a 10 uF electrolytic capacitor should be connected across the power inputs to the CPU board.

The master clock frequency, which can range from 1MHz to 4MHz, is connected to pin 6, the clock input. The common clock frequencies are: 2MHz for the Z80 and 4MHz for the Z80A CPUs. This clock frequency is used by the CPU as a timing reference when performing various operations. Fig. 3 shows a reliable clock circuit that will operate in the desired range without modification.

When the power is first applied, the CPU needs a special pulse to trigger it in

A11	1	40	-A10
A12	2	39	-A9
A13	3	38	-A8
A14	4	37	-A7
A15	5	36	-A6
CLK	6	35	-A5
D4	7	34	-A4
D3	8	33	-A3
D5	9	32	-A2
D6	10	31	-A1
VCC	11	30	-A0
D2	12	29	GND
D7	13	28	RFSH
D1	14	27	MI
D0	15	26	RESET
INT	16	25	BUSRQ
NMI	17	24	WAIT
HALT	18	23	BUSAK
MREQ	19	22	WR
IORQ	20	21	RD

Fig. 1 Z80 CPU pinout.

the right direction. This input is called the reset input. A suitable power-on reset circuit is shown in Fig. 4 with a manual reset button included. This reset circuit can be used to drive other LSI chips requiring a reset input.

The interrupt feature allows the CPU to finish the instruction that it's doing, and then accept an instruction from another peripheral while the first one waits. The NMI and INT inputs provide the CPU with two types of interrupts. The first is a non-maskable interrupt, which means that it cannot be internally disabled by the CPU itself, whereas the second can. In addition, the INT can operate in three different modes, allowing up to 128 devices to be connected to this input. Interrupts will be further discussed later on in the series, so for now these may be tied to the 5V line via a 1K pull-up resistor.

The WAIT input allows slow I/O and memory devices to be connected to the CPU. When this line is brought to the logic zero state, extra cycles called WAIT cycles are introduced to wait for the slower chips to input or output data. Since most modern chips can accommodate a Z80 CPU at 4MHz, this line can also be tied to the 5V rail with a 1K resistor.

The final CPU input is the BUSRQ. This line, when brought to logic zero, causes the CPU to shut itself off. The address bus, data bus, and control bus are placed in the high impedance state and the

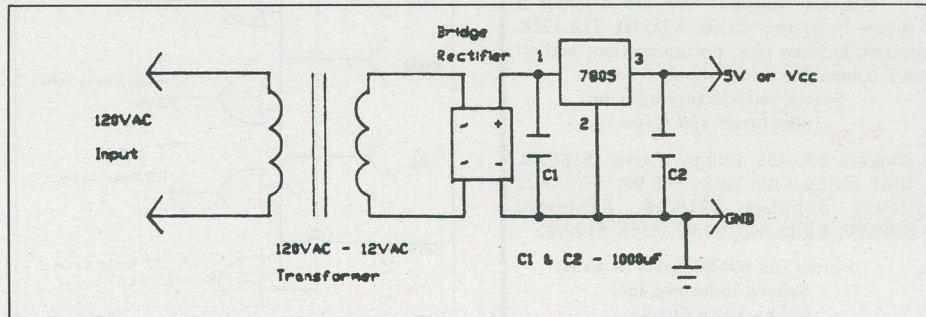
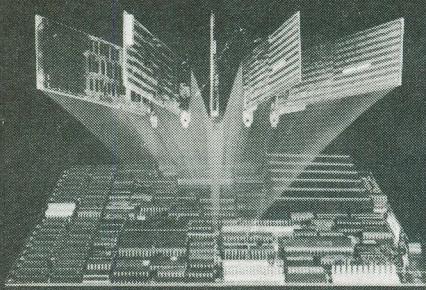


Fig. 2 5V regulated power supply.

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CPU then acknowledges this state by outputting a low on the BUSAK output. During this state other devices, such as CPUs or DMA (Direct Memory Access) controllers, can access the memory and I/O devices controlled by the Z80. In this state the CPU is completely idle and any interrupts incurred will be ignored.

at a logic level one until the appropriate cycle drives two of the outputs low. **Table 2** summarizes the four different machine cycles. The MREQ signal indicates a memory read/write cycle and the IORQ indicates an input/output cycle. Finally, the RD and WR signals indicate read and write operations respectively. Most Z80

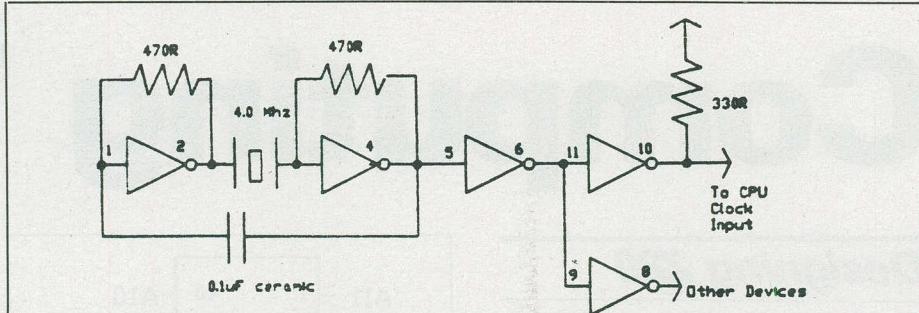


Fig. 3 Z80 CPU clock circuit using the 74L504 Hex inverter

### Z80 Output Pins

Generally, the HALT output is used to indicate to a peripheral device that the CPU is waiting for data and it should interrupt the CPU when it has the data ready. Apart from that, this output line is not too useful.

The RFSH output is used to indicate a memory refresh cycle which is used by dynamic RAM chips to refresh its data contents. During this cycle the lower seven bits of the address bus contain a refresh address used by the dynamic RAM. This cycle is provided to reduce the number of chips necessary to connect dynamic RAM to the Z80 CPU. In instances when no RAM is required, this line can be disregarded.

The M1 signal is a multi-purpose output line, which when used with MREQ and IORQ outputs, can indicate an instruction fetch, reset cycle, and interrupt cycle. **Table 1** illustrates the different combinations.

The next set of output pins is called the control bus, and consists of MREQ, IORQ, RD, and WR. All of these signals are active low; this means that they are all

peripheral chips use these four outputs and decode the proper signal internally. However, older 8080 peripheral chips need decoding to indicate what cycle is being performed. **Fig. 5** shows a typical decoding circuit used to indicate what cycle is being performed.

There are sixteen address lines labeled A0 through A15, giving the Z80 the capability to address 65536 different memory locations. When addressing an I/O port, only the lower 8 bits of the address bus is used, allowing the Z80 CPU to address up to 256 different I/O ports.

The final set of pins make up the data bus. These lines, labeled D0 through D7, are bidirectional, i.e., data can be input or output from the same lines. To determine the direction of the data, the RD or WR control lines are used.

### Buffering

Since there are thousands of transistors inside the Z80 CPU, not only is the size reduced, but also the drive capability of each transistor. Buffers are needed when the load placed on the CPU exceeds the maximum ratings. The maximum current which the CPU can handle for one output pin is 1.8mA in the logic zero state and 250mA in the logic one state. This means that each output is capable of driving one standard TTL load. **Table 3** shows a list of typical loads placed on the Z80 CPU. For small microprocessor projects (one ROM chip, one RAM chip, and a PIO) no buffers are needed. In fact, the buffers would draw more current from the CPU than the original chips.

**Fig. 6** shows how to connect a buffer to the data bus using a 74LS245 bidirectional buffer. The WR signal is connected to pin 1 on the 74LS245, controlling the direction of data flowing from the buffer. Pin 19 is connected to the BUSAK signal

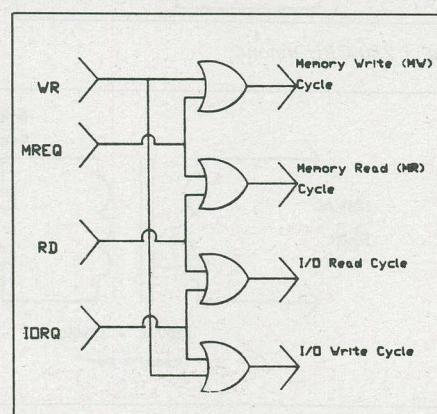


Fig. 5 Decoder for the control bus.

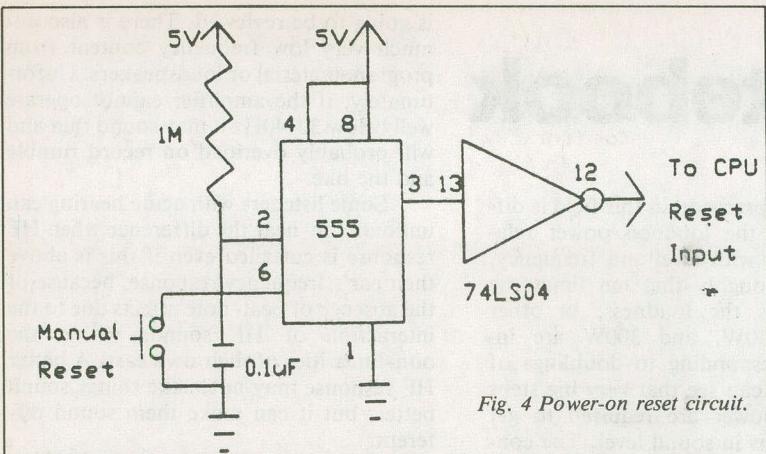


Fig. 4 Power-on reset circuit.

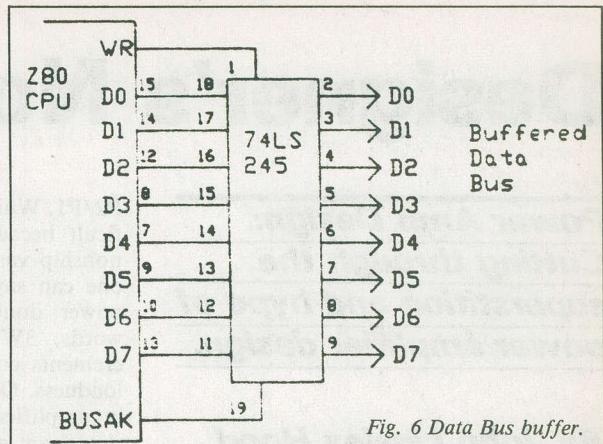


Fig. 6 Data Bus buffer.

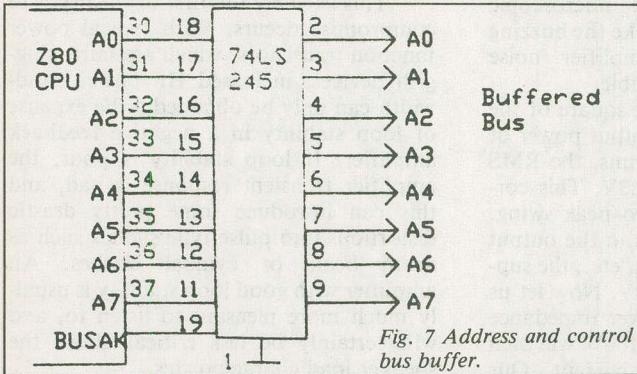
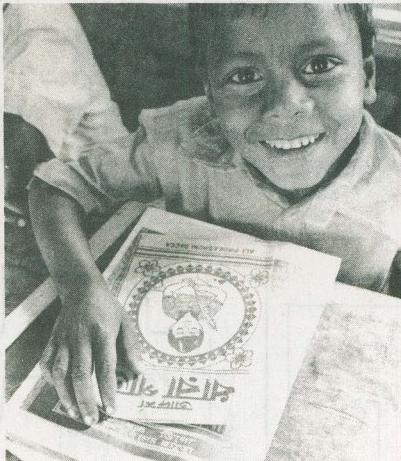


Fig. 7 Address and control bus buffer.

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coming from the CPU, insuring that the data bus buffer is shut off when the data bus on the CPU is turned off.

Fig. 7 illustrates how to connect buffers to the control and address buses. Again a 74LS245 buffer is used, but this time pin 1 on the buffer is tied to ground because direction on the address bus does not change. The 74LS245 is not the only buffer that can be used; however, this buffer is preferred because of the simple layout of the pin connections.

Pin Name	Description	Type	
GND, VCC	Power	Input	
CLK	Clock Frequency	Input	
Reset	Power-on Reset	Input	
NMI, INT	Interrupts	Input	
WAIT	Wait Cycle Request	Input	
BUSRQ	Bus Request	Input	
BUSAK	Bus Acknowledge	Output	
HALT	CPU Halts Executed	Output	
RFSH	Memory Refresh Cycle	Output	
M1	Machine Cycle 1	Output	
MREQ, IORQ	Control Bus	Output, tristate	
RD, WR	Address Bus	Output, tristate	
A0-A15	Data Bus	I/O, tristate	
D0-D7			
M1	MREQ	IORQ	Type of Cycle
0	1	0	Reset
0	0	1	Instruction Fetch
0	1	0	Interrupt Acknowledge

Table 1

MREQ	IORQ	RD	WR	Type of Cycle
0	1	0	1	Memory read
0	1	1	0	Memory write
1	0	0	1	I/O Read
1	0	1	0	I/O write

Table 2

	Logic "1"	Logic "0"
Z80 CPU Drive Current	250uA	1.8mA
Standard TTL (i.e. 7400)	40uA	1.6mA
Low power TTL (i.e. 74L500)	40uA	0.4mA
EPROM (i.e. 2716)	10uA	10uA
RAM (i.e. 2128)	10uA	10uA
Z80 PIO	10uA	10uA
Z80 CTC	10uA	10uA

Table 3.

Next month, we discuss ROM chips and how to connect them to the Z80 CPU. ■

# Designer's Notebook

## **Power Amp Design: Cutting through the superstition and hype of power amplifier design.**

**By John Linsley Hood**

A CONSIDERABLE amount of mystique surrounds the whole field of audio amplifier design, particularly power amplifiers, and a vast amount of time is spent in labs and listening rooms sorting the good from the not-so-good. How much of this mystique is justified is a speculative question.

My own feelings in this matter, particularly in respect to hifi devotees, are highly ambivalent. On one hand, I am convinced that much of the stock in trade of hifi journals, reviewers, and manufacturers who follow in the train of their approval, is built on the identification and exaggeration of differences which are, in reality, fairly small. On the other hand, I know that there are sound differences between differing designs. Contributing factors undoubtedly reside in a gray area of technology not yet clearly defined.

### **Output Power**

Because of the nature of the sensitivity of the human ear, doubling the power fed to a speaker does not make the resultant sound twice as loud. In reality, this is a logarithmic relationship, in which apparent increases in sound loudness are related to power by the equation  $W = k \log$

P2/P1. While precision in this field is difficult because the loudness/power relationship varies with level and frequency, one can say roughly that ten times the power doubles the loudness; in other words, 3W, 30W, and 300W are increments corresponding to doublings of loudness. One can see that very big steps in amplifier power are required to get significant gains in sound level. The converse is also true, that microscopic amounts of sound power, like the buzzing of a fly's wings or amplifier noise voltages, are still quite audible.

Power is related to the square of the voltage or current; if an output power of 100W is required into 8 ohms, the RMS output voltage must be 28.3V. This corresponds to an 80V peak-to-peak swing. Allowing for voltage drops in the output transistors, emitter resistors, etc., the supply should be closer to 100V. Now let us suppose that the loudspeaker impedance drops to 2 ohms at 15KHz. 100W will then correspond to 10A peak current. Our amplifier, to meet this spec, would require 28.3V RMS and 10A peak current capability. Fortunately, it usually doesn't require them simultaneously.

### **Power Bandwidth**

The audio spectrum is assumed to lie between 20Hz and 20KHz, but with average listening rooms, average listeners, and average program material, 45Hz to 15KHz is more realistic. Happily, there isn't much program energy above 10KHz, so we don't need to cater for maximum power in this region unless the equipment

is going to be reviewed. There is also not much very low frequency content from program material or loudspeakers. Unfortunately, if the amplifier cannot operate well below 30-40Hz it may sound thin and will probably overload on record rumble and the like.

Some listeners with acute hearing can undoubtedly hear the difference when HF response is curtailed even if this is above their ear's frequency response, because of the absence of beat-note effects due to the interaction of HF sounds within the non-linearities of their own ears. A better HF response may not make things sound better, but it can make them sound different.

This is where the first of the needs to compromise occurs. With typical power junction transistors, which are fairly sluggish devices, increased HF power bandwidth can only be obtained at the expense of loop stability in a negative feedback amplifier. If loop stability is poor, the amplifier transient response is bad, and this can introduce some pretty drastic distortions into pulse type signals such as drum beats or cymbal clashes. An amplifier with good loop stability is usually much more pleasant to listen to, and will certainly be less critical about the speaker load characteristics.

### **Feedback and Stability**

Negative feedback is the comparison of the input signal with the output and the generation of a corrective adjustment to the input signal to make sure input and output are closely identical. It is the major tool of circuit designers, but there are snags.

We need to make sure that our use of feedback does not make the whole system unstable, and this is particularly the case for amplifiers which have to drive speakers, since these are notoriously com-

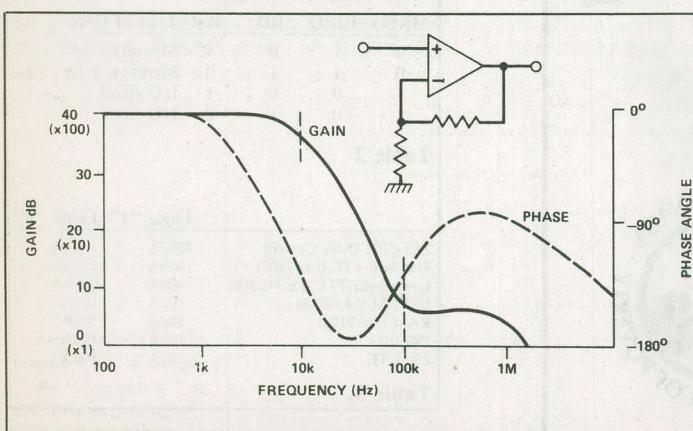


Fig. 1 Gain/phase or Bode diagram for conditionally stable feed-back amplifier (a change of load might make this amplifier oscillate at 30-50 kHz).

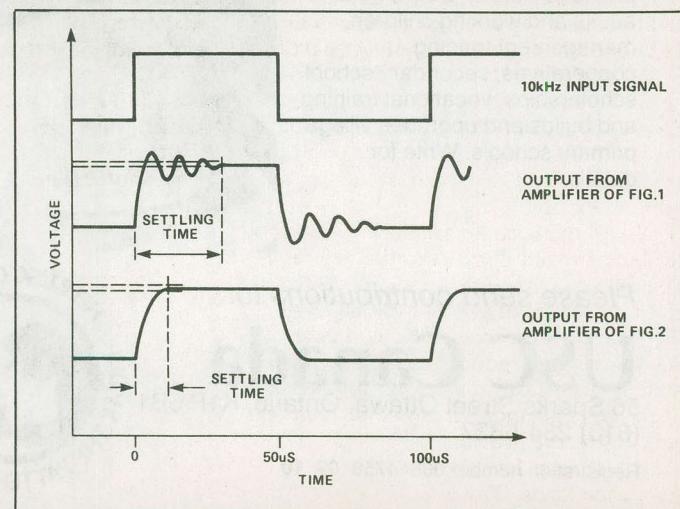


Fig. 2 Influence of gain and phase margins of NFB amplifiers and settling time.

plex in their impedance and delayed response characteristics. It is also essential to remember that a feedback path is just what its name suggests: a means by which signal components can be fed back from the speaker to the input. Since speakers can generate signals of their own because of internal cabinet echoes and inadequately damped reflections, we have to watch this point.

With regard to loop stability, this field was investigated by Bode and Nyquist many years ago in respect to closed-loop servomechanisms. I find the Bode diagram of Fig. 1 the easiest to follow and explain. In this, the gain and phase shift are shown as a function of frequency. If the amplifier has a gain of 1 or more at a frequency where the feedback is in phase with the input (feedback shift of 180 deg.) it will oscillate. The reason for this is simple: the feedback path is providing an input signal of the right size and phase to generate the actual output without the need for any other signal at all (the circuit starts by amplifying its own inherent noise). If the gain is more than unity, the output will continue to increase until some other effect such as clipping reduces it to unity.

It isn't sufficient merely to ensure that the amplifier doesn't oscillate on load. There must be an adequate margin of gain or phase at the unity gain point to make sure that the amplifier is not triggered into misbehaviour during transients in the input signal. In particular, the settling time, or time required following an input voltage excursion for the circuit to settle to the new value, depends solely on the system's speed and stability margin, as shown in Fig. 2. I would very much like reviewers of audio amps to measure this value for a step input with a real live speaker load, since this is one of the areas where the pursuit of very low THD figures at the top end of the audio spectrum can lead to circuit design characteristics which are bad for the transient handling qualities of the amplifier and make it fussy about the speaker with which it is used. It seems pointless to try to reduce .1 percent distortion at 20KHz to .01 percent if the price you pay is 20 to 50 percent distortion on transient signals.

In a typical audio amplifier, the major factor which dominates the gain and HF phase shift characteristics is the relative slowness of the output transistors. The faster the response of these, the easier it is to design a good, stable amp. The catch is that fast junction output transistors are also more fragile and require more restrictive protection circuitry. This makes the amp less good at driving low impedance loads. The answer, and a virtually complete one too, is to use power MOSFETs. Some of the recent ones have almost instantaneous response and are

more linear than either tubes or junction transistors. They too have drawbacks, of which the main one is that they are particular about the phase characteristics of their loads, but there's a simple design answer to this. It is possible to design MOSFET amplifiers that are ten times better than their forerunners.

### Distortion

Not all distortions are equal in their unpleasing effects, and the characteristic of distortion can be greatly influenced by the relative phases of the signal components. This effect has relevance to the behaviour of multi-driver speakers, which can jumble up the phases of a signal and

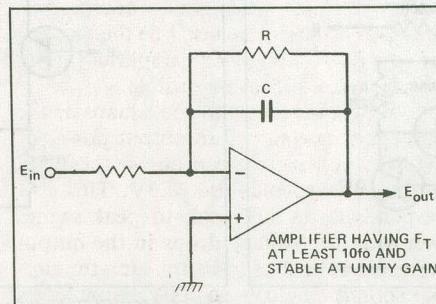


Fig. 3 Feedback amplifier having good gain and phase margins.

thereby alter the nature of the amplifier nasties. Our efforts in the design exercise should be aimed at removing components of distortion which can be so transformed.

Obviously it is helpful if we can keep the distortion of the amplifier as low as possible before feedback is applied, in that low feedback leads to better loop stability. A useful design yardstick, cribbed from servo theory, is to determine the time constants of the bits of the circuit which lead to HF rolloff, and then make one of these ten times the size of the rest. This isn't as arbitrary as it seems; if we wish to end up with an ideal Bode plot, we must remember that the phase shift due to an RC element begins one decade below the -3dB rolloff point. In this way, the system will behave as though it had only a single phase-shifting component.

Another useful design approach is to

limit the system to no more than two stages within the loop, adding more loops if necessary. This isn't always possible with power amps, so other approaches, such as the phase-lead generating step network, may be necessary. One can always put in an internal loop operating only at HF where transistor trouble is likely to arise, and include only two gain stages.

The aim of the designer should be to produce an amplifier in which the harmonic distortion is as low as possible, and the gain bandwidth is as high as possible, before the application of feedback. The feedback should be used mainly to control the gain and output impedance characteristics rather than as a way of lowering distortion.

As far as the feedback path is concerned, it is best not to use a parallel capacitor across the feedback resistor as this can make the amplifier sound less good on some speaker units. It is also helpful if a small resistor in the range of 0.15 to 0.33 ohms is added in series with the output to act as one element of an attenuator. Against the much lower output impedance of the amplifier, this will assist in attenuating signals originating from the speaker itself. (Editor's note: this goes against the standard dogma of having the lowest possible output impedance to raise the damping factor. Comments from designers are invited.)

A final thought with regard to amplifier sound. The human ear is not a particularly good judge of distortion in that a number of randomly chosen people, all reasonably aware of sound quality, either preferred the addition of 0.3 percent third harmonic distortion, or had no specific preferences. Similar work has been done by other investigators. This tends to cast doubt on the value of such judgements, where the listener may actually prefer inferior equipment because it adds a wanted colouration to the sound.

### A Practical Design

My preference for power MOSFETs as the output devices is definite, but not just simple source followers where they require an output inductor in the speaker lead to

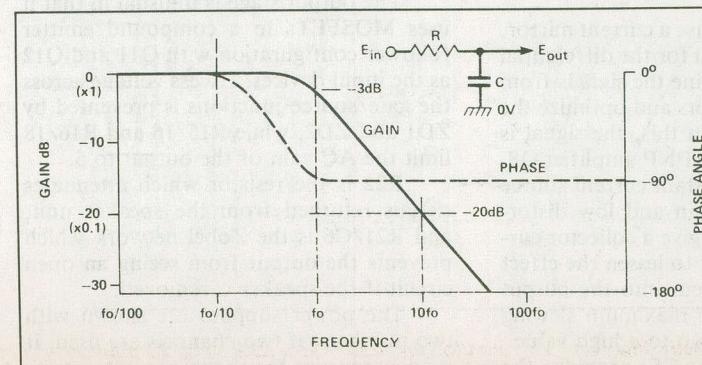


Fig. 4 Gain/phase diagram for a simple RC attenuator; the characteristics of the amplifier in Fig. 2 would be similar to this.

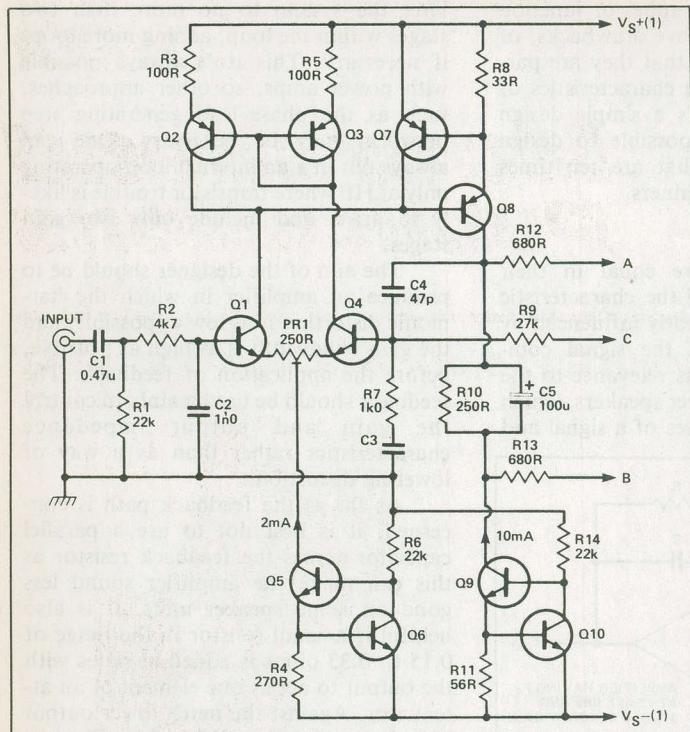


Fig. 5 Power amp. driver stage.

prevent VHF oscillation due to the device's own small lead inductance. If the power MOSFET is used in combination with a bipolar small-signal transistor as shown, this small inelegance can be avoided without degradation of the fast response of the MOSFET. The only necessary device protection can then be an output fuse or a current-limited DC supply.

A typical small-signal voltage amplifier to drive this output stage is shown. Because small-signal transistors are cheap I have been lavish in using them to confer some practical benefit to the circuit. R2 and C2 roll off the response at 33KHz to prevent very fast transients from affecting the circuit. R1 defines the zero-volts DC level. Q5 and Q6 are a compound constant-current source, setting 1mA each through Q1 and Q4, and minimizing noise pickup through the negative supply. PR1 allows the output DC voltage (offset voltage) to be set to precisely zero.

I have chosen to use a current mirror, Q2 and Q3, as the load for the differential amp in order to combine the signals from the two input transistors and optimize the input stage gain. From this, the signal is fed to the second stage PNP amplifier Q8, again loaded by a constant current source to ensure high AC gain and low distortion. R11 is chosen to give a collector current of 10mA in order to lessen the effect of variable drive current into the output stage and to push the maximum slewing rate possible with C4 up to a high value. R10, bypassed by C5, provides the

2.5V bias to set the output stage at 100mA quiescent current.

Under overload conditions, Q9 and 10 limit the current which can be drawn from the -Ve line to 10mA. Q7 and R8 provide a similar protective function for Q8.

R9, R7 and C3 provide the negative feedback path from the output stage to stabilize the AC gain to 27 which allows maximum output (in this case, 50W) from 0 VU or .77V input. C4 provides HF stabilization by means of an internal HF feedback loop enclosing the two stages Q1/4 and Q8, which gives a good well-damped transient response, especially with reactive loads. Eight ohm speakers most definitely do not behave like resistances.

R12 and R13 serve the useful function of preventing temporary latch-up if the amplifier is driven into clipping; in their absence clicks and bangs are prolonged and sound louder.

The output stage is unusual in that it uses MOSFETs in a compound emitter follower configuration with Q11 and Q12 as the input devices. Excess voltage across the gate/source junctions is prevented by ZD1 and ZD2, while R15/16 and R16/18 limit the AC gain of the output to 5.

R22 is the resistor which attenuates signals returned from the speaker unit, and R21/C6 is the Zobel network which prevents the output from seeing an open circuit if the speaker is removed.

The power supplies are shown with two numbers; if two channels are used, it is advantageous to use two separate power

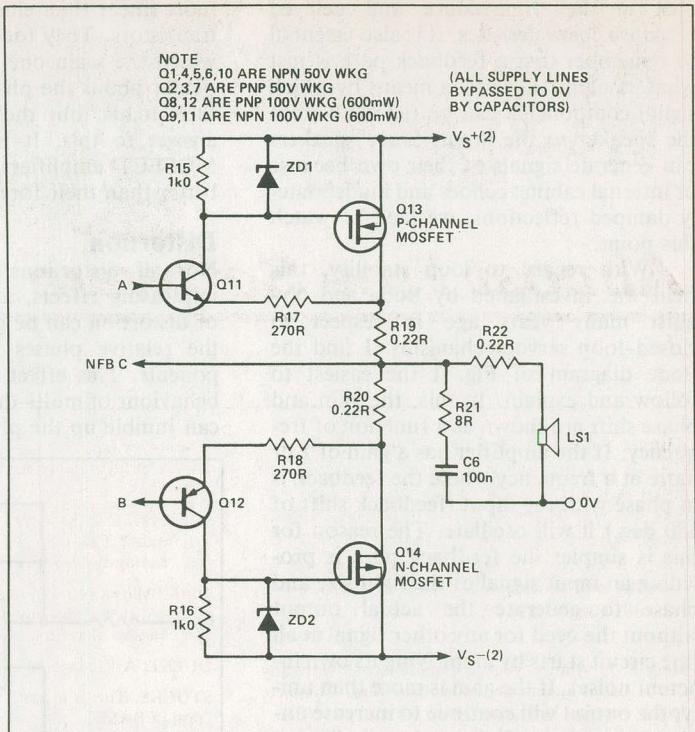
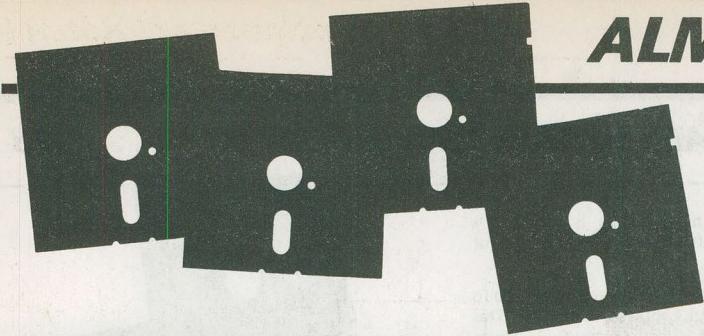


Fig. 6 MOSFET output stage.

supplies to prevent interaction, particularly to prevent power supply nasties from intruding into the sensitive input stages. You could always run the inputs from a regulator, since it draws only 12mA; you can then use a single large power supply with protective fuses in the speaker lines.

This article is meant as a design exercise; I am not going to fill in all the small details of power supplies, PCB, and so on. Designers may wish to do a bit of "fine-tuning" to make sure it as good as possible in all the various conflicting requirements of the system. ■



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## Almost Free Software

### Almost Free Software (CP/M) . . . . . #1

**Almost Free Software #1, #2 and #3 are for CP/M and are available in a variety of formats: Apple // + CP/M, 8 inch SSSD\*, Access Matrix, Morrow Micro Decision, Superbrain, Xerox/Cromemco\*, Epson QX-10VD, Sanyo MBC 1000, Nelma Persona, Kaypro II, Osborne and double densities, Televideo, DEC VT-180, Casio FP-1000, Zorba.**

**Modem 7.** Allows you to communicate with any CP/M based system and download files. Complete details were in Computing Now! November 1983.

**PACMAN.** You can actually play PACMAN without graphics, and it works pretty fast.

**FORTH.** A complete up-to-date version of FIG FORTH, complete with its own internal DOS.

**DUU.** The ultimate disk utility allowing you to recover accidentally erased disk files, fix garbled files, rebuild and modify your system. A real gem.

**D.** A sorted directory program that tells you how big your files are and how much space is left on the disk.

**USQ/SQ.** Lets you compress and uncompress files. You can pack about 40% more stuff on a disk with this system.

**Finance.** A fairly sophisticated financial package written in easily understandable, modifiable Microsoft BASIC.

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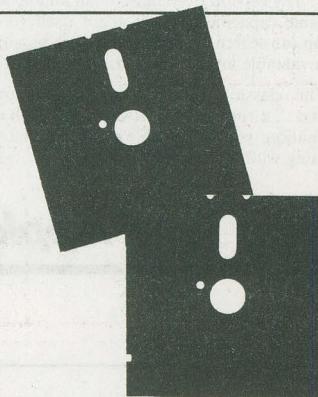
**DISK.** Allows you to move whole masses of files from disk to disk without having to do every one by hand, you can also view and erase files with little typing.

**QUEST.** A "Dungeons and Dragons" type game.

**STOCKS.** This is a complete stock management program in BASIC.

**SEE.** Also known as TYPE17, will TYPE any file, squeezed for not allowing you to keep documents in compressed form while still being able to read them.

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### Almost Free Software (CP/M) . . . . . #2

**BISHOW** The ultimate file typer, BISHOW version 3.1 will type squeezed or unsqueezed files and allow you to type files which are in libraries (see LU, below). However, it also pages in both directions, so if you miss something, you can back up and see it again.

**LU** Every CP/M file takes up unnecessary overhead. If you want to store lots of ata in a small space, you'll want LU, the library utility. It permits any number of individual files to be stored in one big file and cracked apart again.

**MORTGAGE** This is a very fancy mortgage amortization program which will produce a variety of amortization tables.

**NSBASIC** Large disk BASIC packages, such as MBASIC, are great . . . and very expensive. This one, however, is free . . . and every bit as powerful as many commercial programs. It's compatible with North Star BASIC, so you'll have no problem finding a manual for it.

**RACQUEL** Everyone should have one printer picture in their disk collection.

**Z80ASM** This is a complete assembler package which uses true Zilog Z80 mnemonics. It has a rich vocabulary of pseudo-ops and will allow you to use the full power of your Z80 based machine . . . much of which can't be handled by ASM or MAC.

**VFILE** Easily the ultimate disk utility, VFILE shows you a full screen presentation of what's on your disk and allows you to mass move and delete files using a two dimensional cursor. It has heaps of features, a built-in help file and works extremely fast.

**ROMAN** This is a silly little program which figures out Roman numerals for you. However, silly programs are so much fun . . .

**CATCHUM** If you like the fast pace and incredible realism of Pacman, you'll go quietly insane over Catchum . . . which plays basically the same game using ASCII characters. Watch little "C"'s gobble periods while you try to avoid the delay "A's" . . . it's a scream.

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## Almost Free Software (CP/M) . . . . #3

**OIL.** This is an interesting simulation of the workings of the oil industry. It can be approached as either a game or a fairly sophisticated model.

**CHESS.** This program really does play a mean game of chess. It has an on-screen display of the board, a choice of colours and selectable levels of look ahead.

**DEBUG.** The DDT debugger is good but this offers heaps of facilities that DDT can't and does symbolic debugging... it's almost like being able to step, trace and disassemble through your source listing.

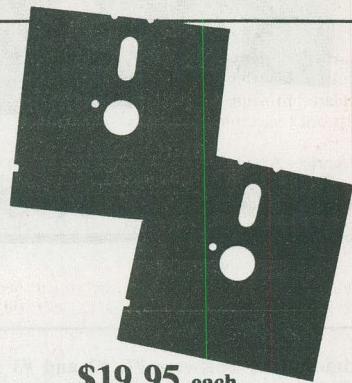
**DU87.** The older DUU program does have some limitations. The version overcomes them all and adds some valuable capacities. It will adapt itself to any system. You can search map and dump disk sectors or files. It's invaluable in recovering damaged files too.

**ELIZA.** This classic program is a micro computer head shrinker... it runs under MBASIC, and with very little imagination, you will be able to believe that you are conversing with a real psychiatrist.

**LADDER.** This is... this program is weird. It's Donkey Kong in ASCII. It's fast, bizarre and good for hours of eye strain.

**QUIKEY.** Programmable function keys allow you to hit one key to issue a multicharacter command. This tiny utility allows you to define as many functions as you want using infrequently used control codes and to change them at any time... even from within another program.

**RESOURCE.** While a debugger will allow you to disassemble small bits of code easily enough, only a true text based disassembler can take a COM file and make source out of it again. This is one of the best ones available.



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## Almost Free Apple DOS Software

## Almost Free Apple DOS Software . . . #1

While CP/M is a wonderful thing in its own right, the Apple computer can also, and usually does, operate under DOS. For this reason, there's a multitude of programs available for it. Below, we offer a mini-multitude of our own.

The following programs will operate on any Apple //+, //e, //c, or true compatible operating under DOS 3.3. Apple users operating only under ProDOS may have to make alterations to some programs.

**Picture Coder:** All Apple HiRes pictures take up 36 sectors in their binary form. This program creates a textfile of a program in memory, squeezing out the zero bytes, that can later be EXECed into memory. The textfile often takes up less room on the disk.

**DNA Tutorial:** Operating under Integer BASIC, this program might appeal to 'clone' owners. In actuality, though, it's an interactive low-res graphics tutorial of DNA in its inherent forms. And you thought your Apple was only good for games...

**Toad:** Speaking of games, this program is an Applesoft BASIC implementation of 'Frogger' that can be controlled with either a joystick or the keyboard. The user's high scores are saved to disk.

**Function Plotter:** A fairly extensive Applesoft BASIC program that takes any inputted function and plots it on the HiRes Screen.

**Data Disk Formatter:** Apple DOS disks need not be bootable to be useful. This binary program formats a disk without setting DOS on the tracks, conserving useful disk space.

**BASIC Trace:** A program for the advanced Applesoft programmer, this file, when EXECed, displays the hexadecimal locations of each Applesoft line number of a program in memory.

**Gemini Utility:** A word processor pre-boot for Gemini printer users, this BASIC program initialises the printer's font or pitch before you boot your word processor.

**Payments:** This BASIC program allows you to keep track of payments and credits to and from up to 100 accounts on a single disk. A sample account is included.

**Databox:** A small but useful database program in Applesoft BASIC. Sample files are included to get you started.

**Nullspace Invaders:** A quick BASIC HiRes game testing coordination and judgement as you manipulate a monolith through mysterious gates.

**Fine Print:** The majority of this software has been obtained from on-line public access sources, and is therefore believed to be in the public domain. Any remaining programs were written in-house. The prices of the disks defer the cost of collecting the programs, debugging them, reproducing and mailing them, plus the cost of the media they're supplied on. The software itself is offered without charge.

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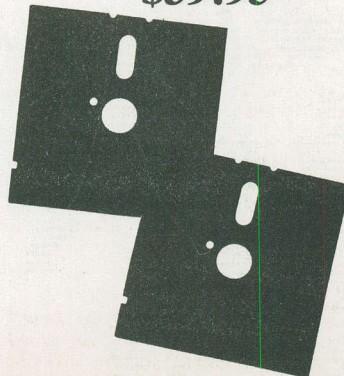
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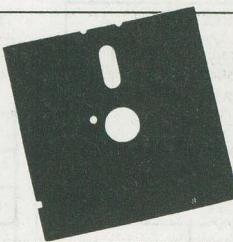


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Electronics Today June 1985

## Almost Free Apple DOS Software . . . #2



**Amort:** A monthly amortization program that calculates monthly payments to an inputted figure, calculates principle, interest on every balance, and prints out the resulting chart.

**Voiceprint:** An unusual program that uses the HiRes screen to sample sounds inputted through the cassette jacks at the back of your Apple. Sampling rate and other variables can be controlled, and two sounds may be compared side-by-side.

**Calc NOW!**: Written in BASIC, this spreadsheet program is somewhat slower than VisiCalc, but still offers the power you expect from a spreadsheet. With sample files.

**Cavern Crusader:** A mix of BASIC and binary programming, winning this HiRes game is difficult, to say the least. For every wave of aliens shot in the cavern, there's always a meaner bunch in the wings.

**Newcout:** With source file. This binary program replaces the I/O hooks in the Apple with its own so you can operate your Apple through the HiRes screen. Comes with a character set.

**Charset Editor:** A utility to help you create your own character sets to use with Newcout.

**Calendar:** A BASIC utility useful for finding a particular day of any inputted month and year, or for printing out any given year.

**LCLODR:** With source. This binary utility BLOADS any given file into the 16K language card space at \$D000. The source is useful in showing how to use DOS commands through assembly language.

**Cristo Rey:** An animated HiRes BASIC program showing Cristo Rey by moonlight. For apartment-bound romantics.

**ATOT:** That's an acronym for 'Applesoft to Text'. EXEC this textfile to produce a textfile of your program.

**Applesoft Deflator:** This program takes a textfile made by ATOT and squeezes it, replacing PRINT statements with '?' and removing unnecessary spaces from the listing.

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## Almost Free PC Software

## Almost Free PC Software . . . #1

Our Almost Free Software disks, volumes one through three, for systems running CP/M have been so thunderingly popular that we have assembled a volume for IBM PC users. The considerably greater power of a sixteen bit system, coupled with its larger capacity disk drives, have enabled us to offer a collection of programs that will knock the socks off virtually any sentient life form booting the disk. Be warned... wear sandals when you unwrap this thing.

This software will run superbly on genuine IBM PC's and compatible systems.

**PC-WRITE** While not quite Wordstar for nothing, this package comes extremely close to equalling the power of commercial word processors costing five or six bills. It has full screen editing, cursor movement with the cursor mover keypad, help screens and all the features of the expensive trolls.

**SOLFE** This is a small BASIC program that plays baroque music. While it has little practical use, it's just a kick to tinkle with. It's also a fabulous tutorial on how to use BASIC's sound statements.

**PC-TALK** Telecommunications packages for the IBM PC are typically intricate, powerful and huge. This one is no exception. It has menus for everything and allows full control of all its parameters, even the really silly ones. It does file transfers in both ASCII dump and MODEM7/XMODEM protocols and comes with... get this... 119424 bytes of documentation.

**SD** This sorted directory program produces displays which are a lot more readable than those spewed out by typing DIR. It's essential to the continued maintenance of civilization as we know it.

**FORTH** This is a small FORTH in Microsoft BASIC. It's good if you want to get used to the ideas and concepts of FORTH... you can build on the primitives integral with the language.

**LIFE** This is an implementation of the classic ecology game written in 8088 assembler. While you may grow tired of watching the cells chewing on each other, in time the source will provide you with a powerful example of how to write code.

**MAGDALEN** This is another BASIC music program. We couldn't decide which of the two we've included here was the best trip, so we wound up putting them both on the disk. Ah... the joys of double sided drives.

**CASHACC** This is a fairly sophisticated cash acquisition and limited accounting package written in BASIC. It isn't exactly BPI, but it's a lot less expensive and suitable for use in most small business applications.

**DATAFILE** This is a simple data base manager written in... yes, trusty Microsoft BASIC.

**UNWS** Wordstar has this unusual propensity for setting the high order bits on some of the characters in the files it creates. Looks pretty weird when you try to do something other than Wordstar the file, doesn't it... Here's a utility to strip the bits and "unWordstar" the text. The assembler source for this one is provided.

**HOST2** This is a package including the BASIC source and a DOC file to allow users with SmartModems to access their PC's remotely. It's a hacker's delight.

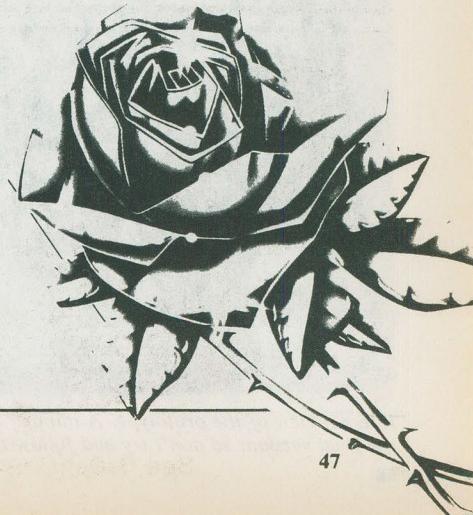
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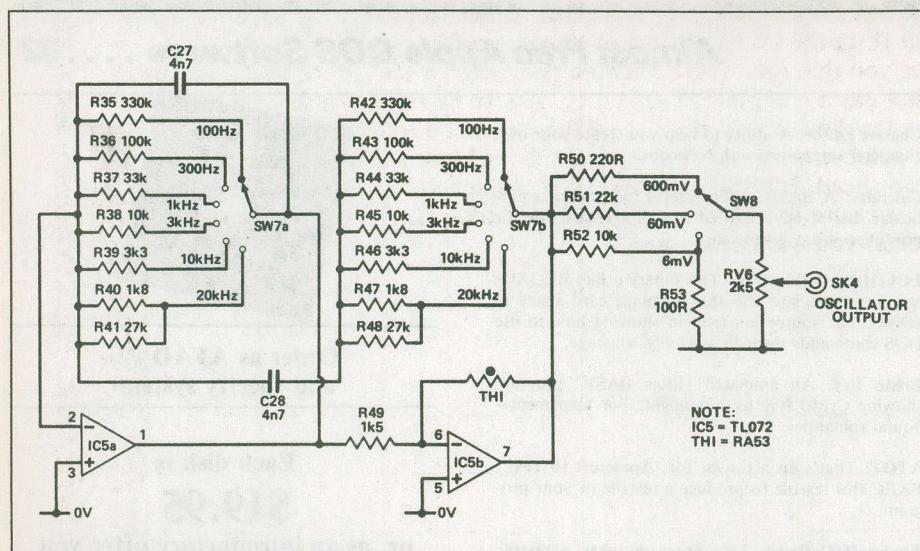


Fig. 5 The final circuit diagram of the spot frequency oscillator.

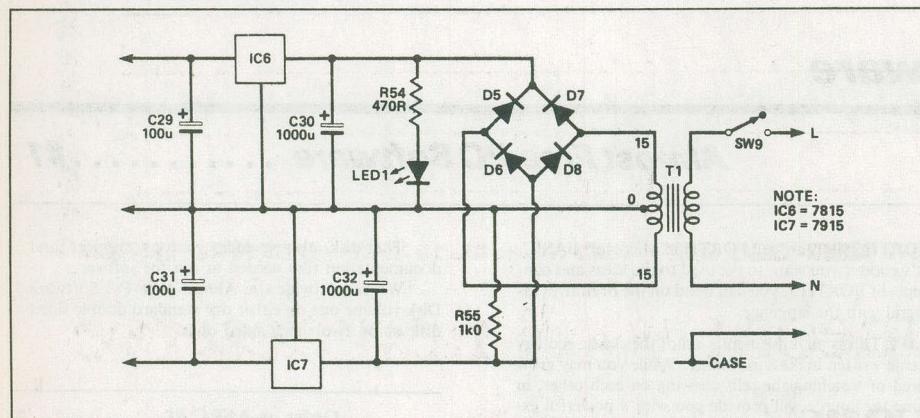
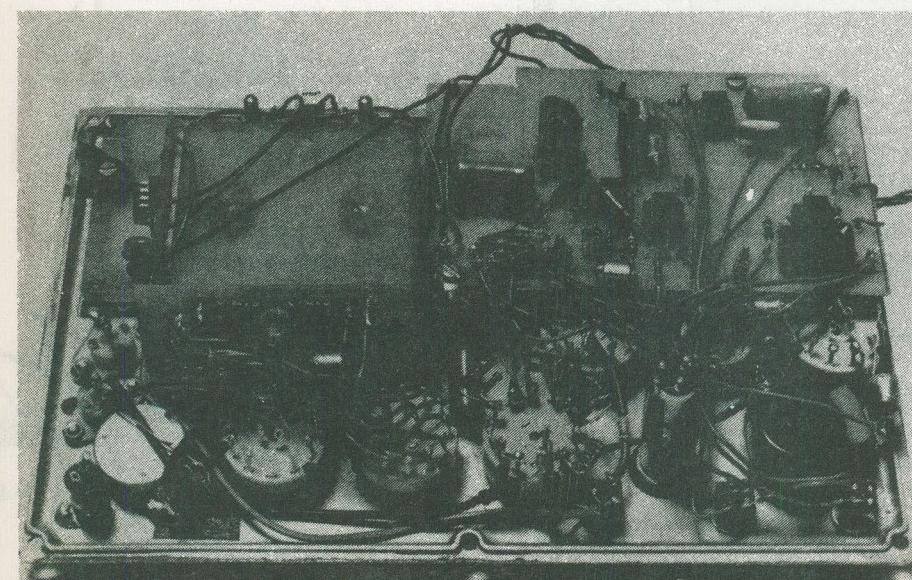


Fig. 6 The circuit diagram of the stabilized power supply.



Internal view of the prototype. A number of modifications have been incorporated in the final version, so don't try and follow this wiring too closely!

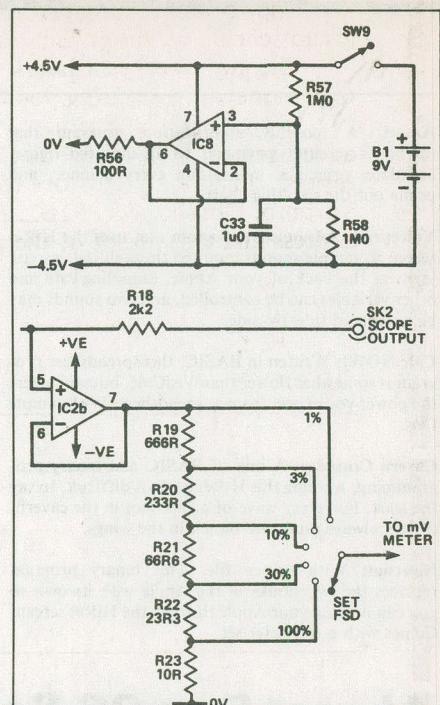


Fig. 7 A dual supply from a battery (top) and changes in the circuit to suit this method.

However, if the supply voltage chosen is the plus and minus 4V5, a problem would arise because the notch amplifier circuit would overload at the 3V RMS required from ICs 2 and 3 for FSD on the measuring instrument. It is therefore necessary to downgrade this a bit by cutting out R19 (2k33) so that SW2b is that shown in Fig. 7. This gives a minimum sensitivity of 1 percent. This requires only a 1V swing from the notch amplifier.

### The Wien Bridge

It is not a difficult matter to generate quite a good notch in a frequency response and tune it to the test waveform, and there are several circuit choices for doing this. Of these, the two most convenient are the RC Twin-T and the Wien bridge.

The interesting thing about the Wien bridge is that it has zero phase shift and an attenuation of just three at one specific frequency. If one makes the resistors adjustable, this frequency can be altered. If the capacitors are not quite the same, the attenuation will not be exactly three, but this can be adjusted by altering the values of the resistors.

In the circuit I have chosen, the amplifier is used to simply invert the phase of the signal and amplify it by 2x. This utilizes the feature of the Wien bridge that the impedance of one leg is twice that of the other when the phase shift produced by each leg is equal. So, if the amplifier applies a signal to the upper half which is exactly twice that of the lower, the output will come to a null at some frequency

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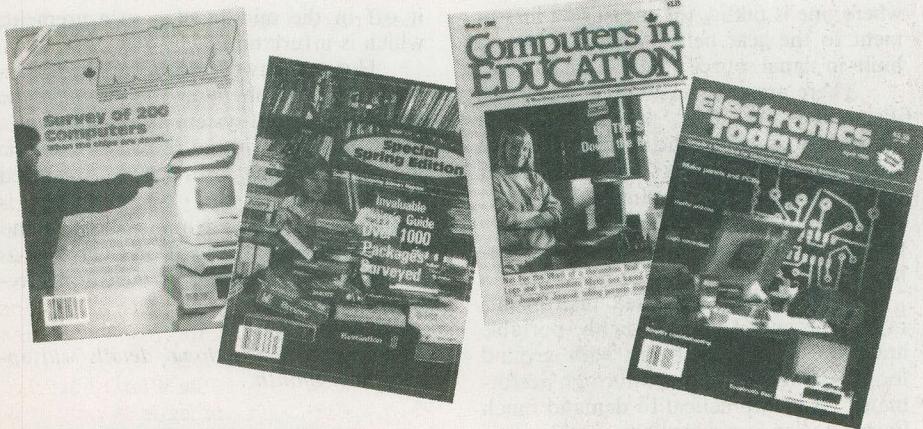
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dependent on the values chosen. This amplifier method eliminates the problem of requiring a very well-balanced differential amplifier.

If we just want to remove the input signal frequency without attenuating the harmonics, the skirts of the notch must be much steeper than those of an ordinary Wien bridge. However, we can sharpen the notch by applying some negative feedback around the bridge.

To tune the notch frequency so that it exactly coincides with the input frequency, we need to be able to adjust either the Cs or the Rs in the network. Since the operating frequency is given by the equation

$$f_o = \frac{1}{2\pi\sqrt{C_1 C_2 R_3 R_4}}$$

the values for C are too large to allow the use of a variable ganged capacitor unless a very high impedance circuit is used. In fact, if the Rs were 10k each, the capacitors would have to be 16nF for a 1KHz notch frequency, and lower frequencies would require proportionately larger values of capacitors.

It is possible to make such a system with an air-spaced twin-gang capacitor, but the necessary high values of R make the whole unit very sensitive to hum pickup. Overall, I think it is better to use variable resistors, which are easier to get and a lot more compact.

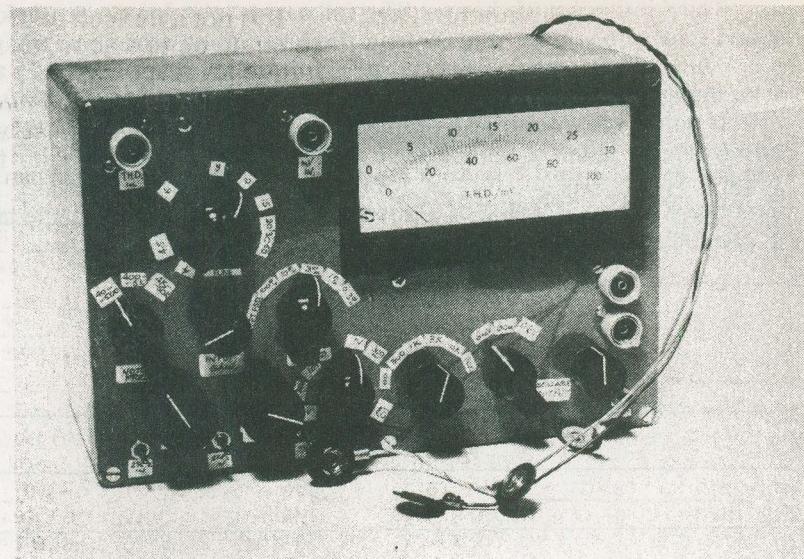
The necessary slow-motion adjustment can be obtained by the use of two resistors in series, one ten times the value of the other, when the high value resistors (as ganged pairs adjusted together) can be used as the coarse adjustment and the lower ones for fine trimming. This principle could be extended, of course, to employ three such resistors in series to allow a very fine adjustment indeed.

Since the resistor which adjusts the gain of the Wien amplifier is a single potentiometer, a ten-turn type can be used in this position to adjust the gain of this leg so that a complete notch is obtained with no residues of the input frequency remaining.

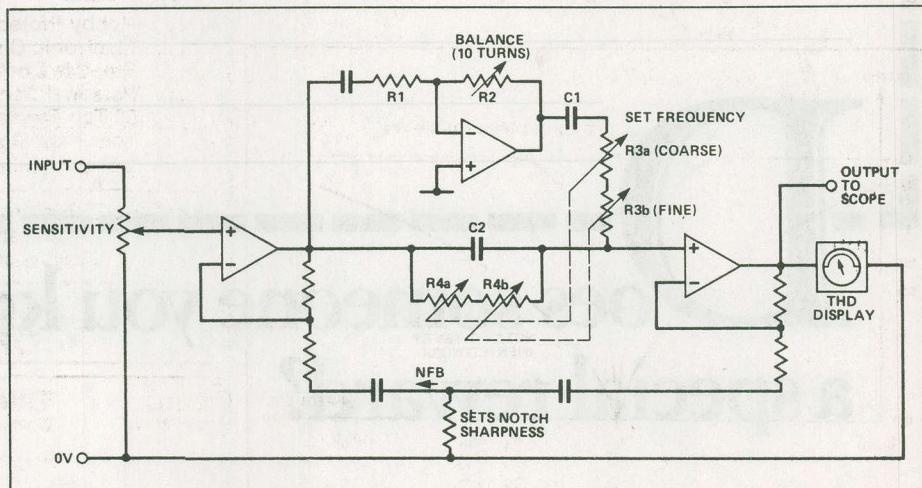
The final part of the system will be a wide bandwidth millivoltmeter to display the value of the distortion and noise residues remaining when the input sinewave is removed.

Since we live in the real world and there will inevitably be some hum pickup somewhere in the system we are testing, it is useful to incorporate a 60 Hz filter which can be switched in. Also, while we are doing that, we might as well include some HF filtering options so that we don't measure the THD over too wide a frequency window, with its associated noise components.

Finally, it is very helpful in tests



*The prototype, looking much the way most prototypes do at this stage in their development!*



*Fig. 8 Wien network with negative feedback to produce a sharper notch.*

where one is taking the measuring instrument to the gear being tested, to have a built-in signal source of adequate quality.

There are two versions of the above instrument in this project; one a laboratory standard instrument operated from the AC line, and a somewhat simpler version operated from a single 9V battery, which will be somewhat easier to make, if the demands made upon it are less stringent.

I like battery operated instruments myself because they are highly portable and don't cause problems with ground loops. However, if one wants high performance, it is impractical to demand much lower power consumption at the same time. If one then accepts a higher battery drain, say, 10-25mA, it is expensive if one forgets to switch off the instrument after use, while any auto-off circuit may switch

it off in the middle of a measurement, which is infuriating.

Hence the two versions of the instrument. I have deliberately tried to make the battery-operated system as economical in use as possible without resorting to exotic ICs, and in both cases I have organized things so that the millivoltmeter is available as a separate input so that it and the oscillator can be used on their own as a means, for example, of measuring frequency response.

*PCBs and constructional details will appear next month.*

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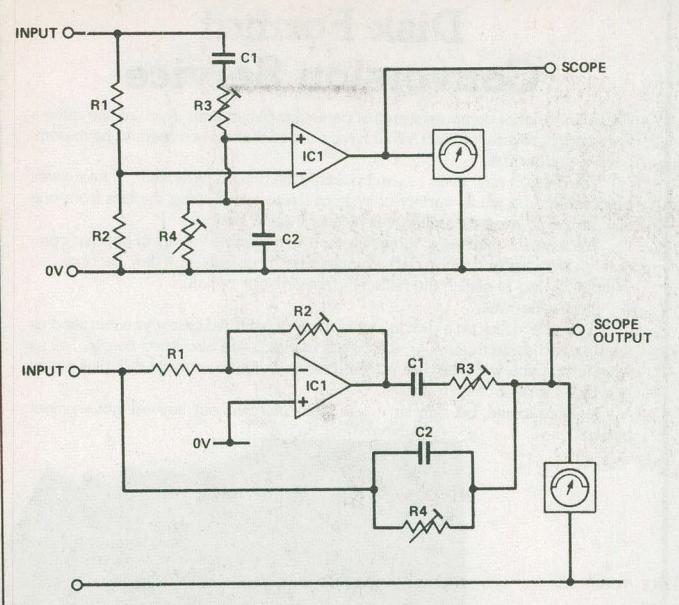


Fig. 9a and b — Two possible arrangements of the Wien network.

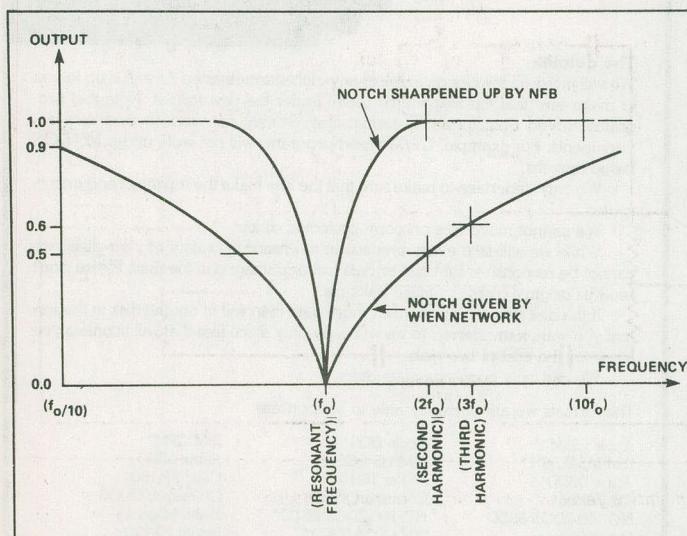


Fig. 10 The notch produced using the arrangement of Fig. 9b.

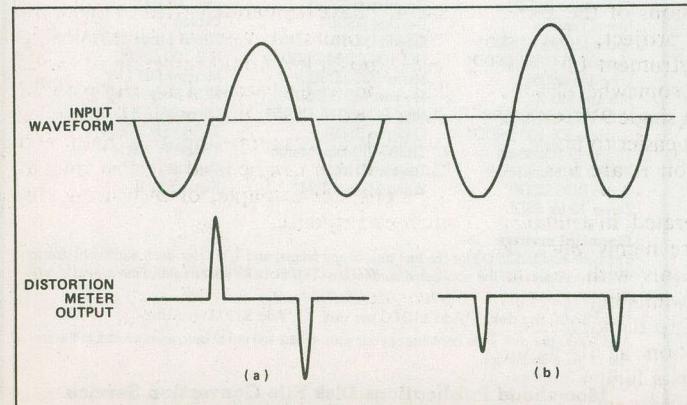
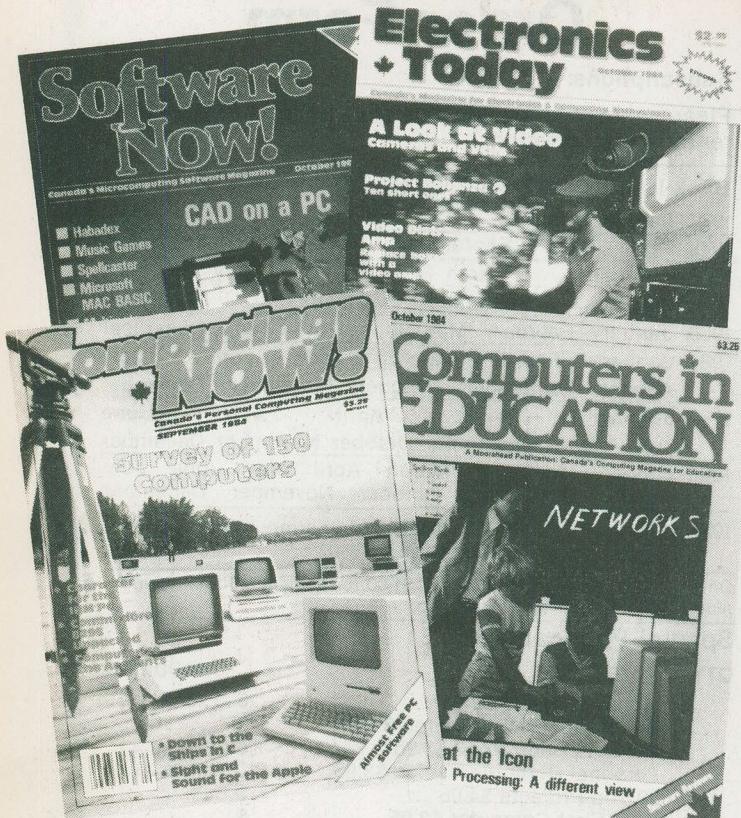


Fig. 11 Examples of the waveforms obtained by using a notch filter on distorted sine-waves.

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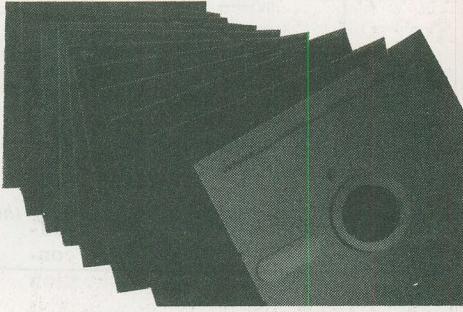
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# Anti Phaser

**The Anti Phaser is a phaser with a difference: the notch filters sweep in opposite directions. Suitable for any amplified instrument.**

By D. Bradshaw

DESIGNING a notch filter is not difficult, and there are several standard configurations to choose from. The situation is rather more difficult where a tunable notch filter is required, and much more difficult in an application such as this where the filter must be voltage controlled so that it can be swept by a low frequency oscillator. The main difficulty is that notch filters tend to require that several filter resistances or capacitances are varied in value and remain accurately matched in order to give usable results over a wide frequency range.

There are two conventional solutions to the problem. The most common one is to use a series of voltage controlled phase shifters, and to then mix the shifted and unshifted signals. At frequencies where the signals are out of phase they tend to cancel each other out and produce the required notches of high attenuation in the frequency response. Two phase shifters per notch are required, and matching of the two voltage controlled resistors (usually JFETs) is unnecessary. The other method is essentially the same but uses a different system to obtain the anti-phase signals. Rather than phase shifters an analogue delay line is utilised.

These days there are practical alternatives to the conventional systems, and it is one of these that is used in this unit. It is based on four operational transconductance amplifiers, two being required for each notch filter. Fig. 1 shows the block diagram for the Anti-phaser.

Transconductance amplifiers can easily be used to act as bandpass, low-pass, or high pass filters, and it is the bandpass configuration which is used here. This is, of course, the exact opposite of what is required here, but a bandpass

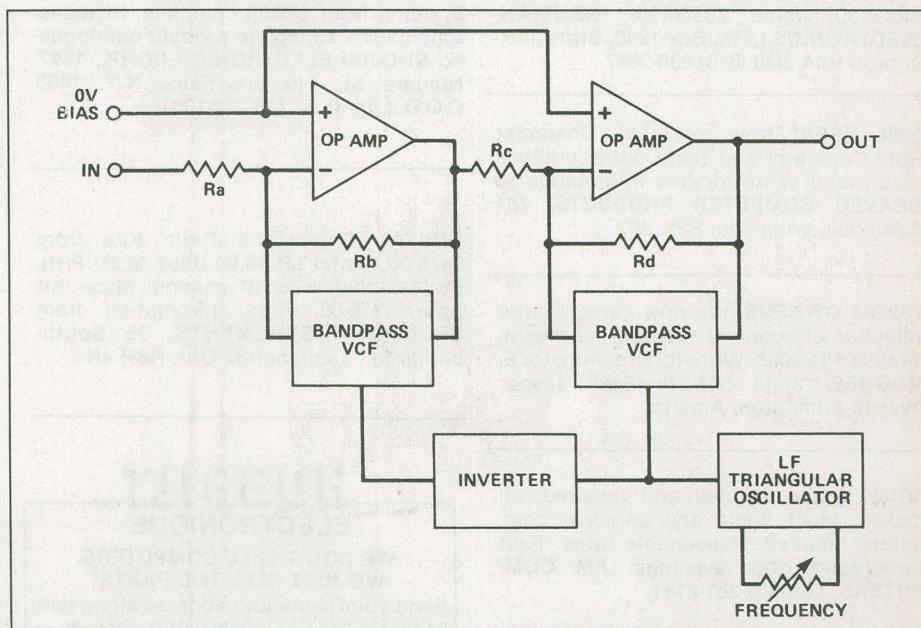


Fig. 1 The block diagram of the Anti-Phaser. This is for one notch filter. Two are used.

response can be converted to a notch type simply by using the filter in the feedback path of an inverting mode operational amplifier circuit. Fig. 2 shows the basic inverting mode circuit, and the two resistors control the voltage gain of the circuit. The voltage gain of the operational amplifier itself is extremely high, being typically about 100,000 times at low frequencies. What it is actually amplifying is the voltage difference across its two inputs, with the output going positive when the non-inverting (+) input is at the higher voltage, or negative when it is at a lower voltage than the inverting (-) input. Due to the very high voltage gain of the device only a very small voltage difference of

typically under 1 millivolt is needed in order to send the output fully positive or negative.

In the circuit of Fig. 2 the negative feedback from the output to the inverting input results in the inverting input being maintained at the same potential as the non-inverting input. If an input signal was to take the inverting input slightly positive, this would unbalance the inputs and send the output negative. The coupling through R2 would result in the inverting input being taken negative, counteracting the positive input signal and maintaining the balance. Just how negative the output has to go for a given input voltage depends on the values of R1 and R2. If we

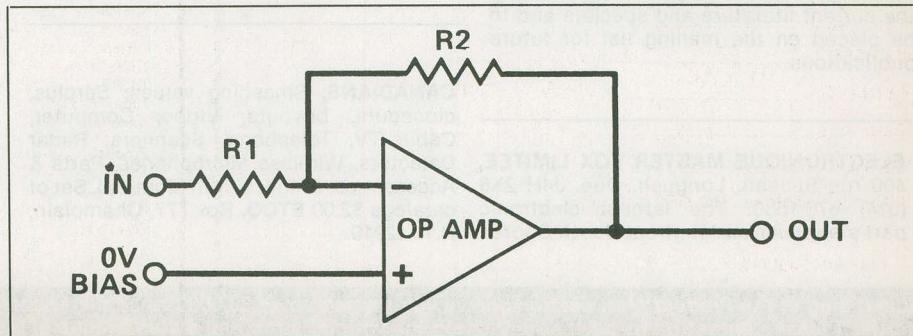


Fig. 2 The basic circuit which inverts the bandpass response into a notch type response.

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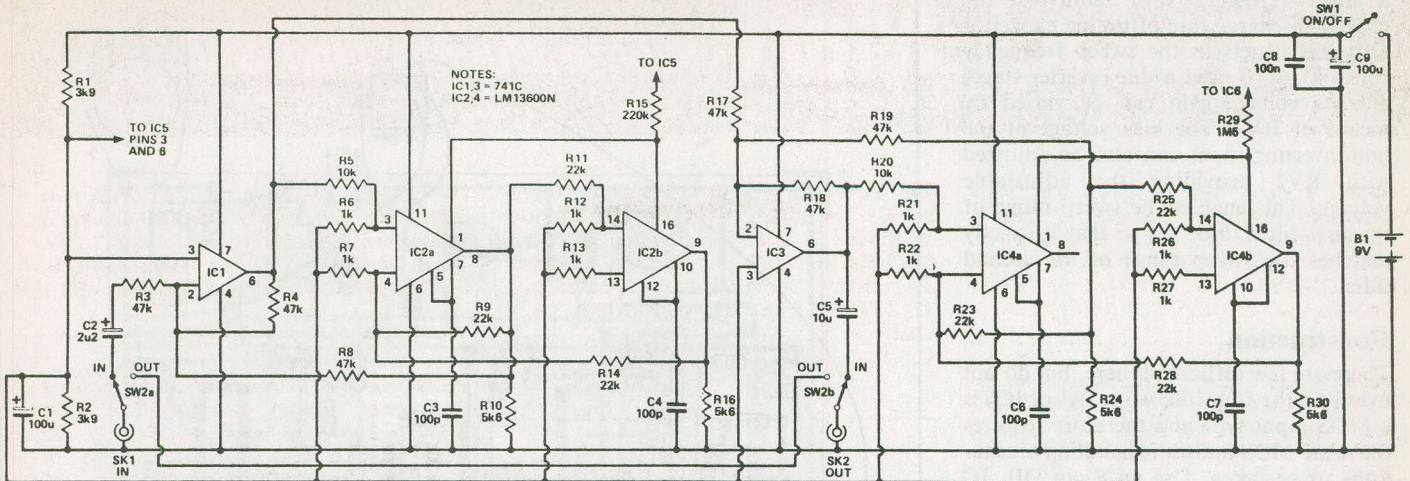
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assume that they have the same value then any increase in voltage at the input will produce and identical but opposite change in the output voltage. If R2 is made higher in value than R1 it becomes necessary for the output voltage to change by a larger amount in order to maintain the input voltage balance by what is really just a simple potential divider action.

What is of greater importance is this application, if R2 is made lower in value than R1, the output has to change by a smaller amount than any change at the input in order to maintain the balance. The circuit can therefore provide both voltage gain and attenuation, depending on the feedback resistor values. The voltage gain/attenuation of the circuit is simply equal to the value of R2 divided by the value of R1. For instance, if R1 and R2 had values of (say) 100k and 1k respectively, this would give a gain of 1/100, of some 40dB of attenuation if you prefer.

Returning to the block diagram of Fig. 1, each bandpass filter is connected in the feedback path of an inverting mode operational amplifier circuit. In effect the bandpass filters replace R2 of Fig. 2. At pass frequencies the filter provides a low resistance, and consequently a very low level of voltage gain. Outside the passband it provides a very high feedback resistance and therefore a very high voltage gain. In this application we only require a low level of gain within the filter's passband, and about unity gain rather than high gain at other frequencies. R<sub>b</sub> and R<sub>d</sub> are therefore used to limit the feedback resistance to a suitable level so that the response of the circuit as a whole is tamed to an acceptable degree.

The sweeping of the filters is accomplished using a low frequency oscillator having a triangular output waveform. The sweep frequency is continuously variable from about 0.1Hz to 10Hz. One of the notch filters is driven

direct from output of the oscillator, but the other is driven by way of an inverter stage which provides the anti-phase operation of the second filter.

### Circuit Operation

Fig. 3 shows the circuit diagram of the signal processing stages while the oscillator/inverter circuit diagram appears in Fig. 4.

Taking Fig. 3 first, R1, and R2 and C1 provide a centre tap on the supply rail, effectively giving dual 4V5 supplies from the single 9 volt input. The circuit is in fact powered from a 9 volt battery and the current consumption is only about 7 milliamps.

The two notch filters are essentially the same, one utilizing IC1 and IC2 while the other is built around IC3 and IC4. We will consequently only consider the operation of the first filter. The two transconductance amplifiers of IC2 are con-

nected in a standard transconductance amplifier state variable filter configuration. Lowpass filtering is produced at the output of IC2b, and the bandpass filtering needed here is obtained at the output of IC2a. This bandpass filtering is connected in parallel with feedback resistor R4, which is the equivalent of R<sub>b</sub> in Fig. 1. The two transconductance amplifiers are used here as voltage controlled resistors which act as lowpass filters in conjunction with filter capacitors C3 and C4. Feedback through R9 and R14 is used to convert the response at the output of IC2a to the required bandpass type.

SW2 provides a means of bypassing the unit and switching out the effect. In practice this is a heavy duty push button switch so that it can be operated by foot.

Turning to Fig. 4 now, IC5 is used as a conventional triangular/square-wave oscillator with IC5a acting as the Miller Integrator and IC5b being used as the

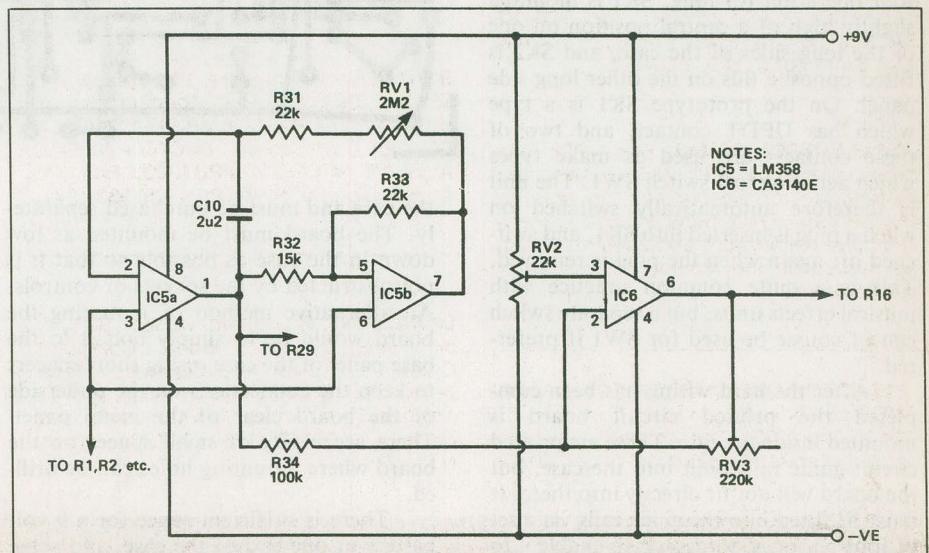


Fig. 4 Part of the circuit, showing the operation of IC5, the L.M. 358.

Schmitt Trigger. RV1 controls the charge/discharge rate of timing capacitor C10 and it acts as the sweep frequency control. IC6 is used as the inverter stage, and its voltage gain can be varied by means of RV3. The bias voltage at the non-inverting input can also be adjusted with RV2 providing the adjustable voltage. This enables the sweep range of the first filter to be set so that it exactly matches the sweep range of the second filter.

### Construction

There are few difficulties here, but do not overlook the four link wires. Also, IC6 is a MOS input type and therefore requires the usual MOS antistatic handling precautions to be taken. Use an 8 pin DIL IC socket for this device, do not fit it into place until the unit is otherwise finished, and handle it as little as possible.

C10 must be good quality non-polarised component (ie not an electrolytic or a tantalum type), and the printed circuit board is designed to take a printed circuit type having 15mm lead spacing. However, it should be possible to fit other types onto the board without too much difficulty, but the component used in the C10 position must be physically quite small.

When all the link wires and components have been fitted onto the board connected the battery clip and fit pins at the points where connections to the other off-board components will be made.

### Case

SW2 and RV1 are mounted on the top of the case (which in this case is the panel opposite the removable one). SW2 must be a heavy duty component since an ordinary push button type is unlikely to stand up to foot operation for long, SK1 is mounted slightly high of a central position on one of the long sides of the case, and SK2 is fitted opposite this on the other long side panel. On the prototype SK1 is a type which has DPDT contact, and two of these contacts are used as make types which act as on/off switch SW1. The unit is therefore automatically switched on when a plug is inserted into SK1, and switched off again when the plug is removed. This is a quite common practice with musical effects units, but a separate switch can of course be used for SW1 if preferred.

After the hard wiring has been completed the printed circuit board is mounted inside the case. There are printed circuit guide rails built into the case, but the board will not fit directly into these. It must be fitted into the guide rails via a set of four plastic adaptors which enable it to be mounted at right angles to the rails. These adaptors are not normally supplied with

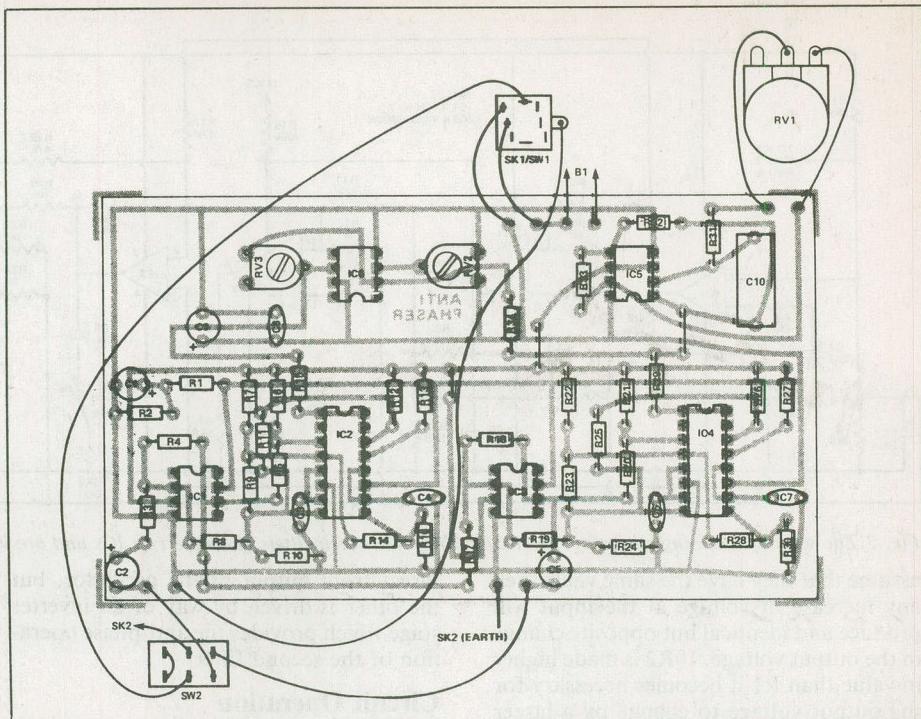
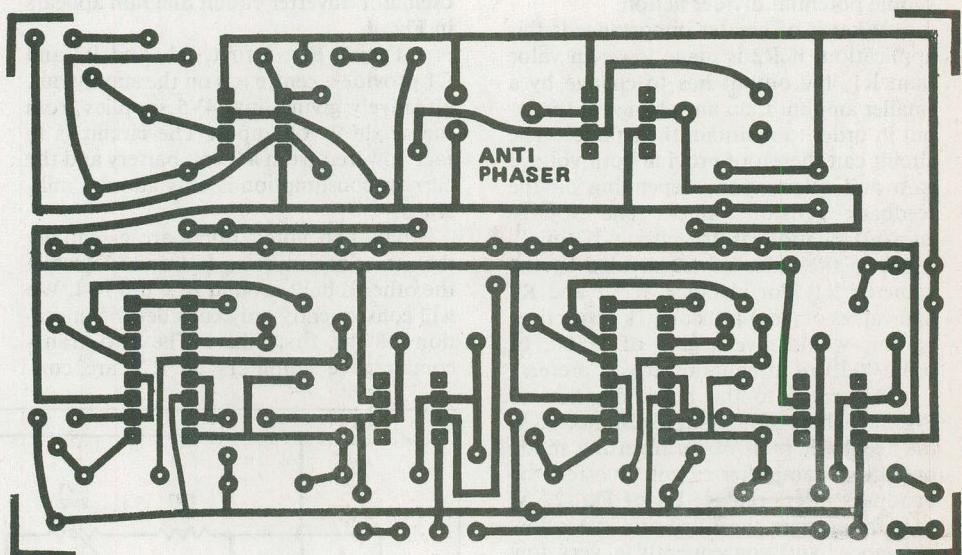


Fig. 5 The PCB layout. Assembly is straightforward, but see the note in the text about C10.



the case and must be purchased separately. The board must be mounted as low down in the case as possible so that it is not obstructed by the sockets or controls. An alternative method of mounting the board would be to simply bolt it to the base panel of the case, using short spacers to keep the connections on the underside of the board clear of the metal panel. There are plenty of small spaces on the board where mounting holes can be drilled.

There is sufficient space for a 9 volt battery at one end of the case, and some foam material can be used to trap the battery firmly in place there. Four small

cabinet feet are fixed to the base panel of the case to prevent the unit from slipping when SW2 is operated.

### In use

The electric guitar or other instrument connects to SK1 by way of the usual screened jack lead, and as explained previously, the unit is automatically switched on and off as the plug is inserted into and withdrawn from SK1. The output signal is taken from SK2, and again, a normal screened jack lead is used to take the output to the amplifier (or whatever).

RV2 and RV3 should be set at a roughly mid setting initially, and the unit

## PARTS LIST

### Resistors

(All 1/4W 5% carbon)

R1, 2	3k9
R3, 4, 8, 17, 18, 19	47k
R5, 20	10k
R6, 7, 12, 13, 21, 22, 26, 27	1k
R9, 11, 14, 23, 25, 28, 31, 33	22k
R10, 16, 24, 30	5k6
R15	220k
R29	1M5
R32	15k
R34	100k

### Potentiometers

RV1	2M2 linear
RV2	22k 0.1W horizontal preset
RV3	220k 0.1W horizontal preset

### Capacitors

C1, 9	100uF 10V radial elect
C2	.2u2 63V radial elect
C3, 4, 6, 7	100pF ceramic plate
C5	10uF 25V radial elect

C8	100nf ceramic
C10	.2u2 polyester or carbonate

### Semiconductors

IC1, 3	741C op amp
IC2, 4	LM13600N or LM13700N dual transconductance amp
IC5	LM358 dual op amp

IC6	CA3140E MOS op amp
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### Miscellaneous

SK1	Standard jack with DPDT contacts
SK2	Standard jack
SW1	Part of SK1
SW2	Heavy duty DPDT push button switch

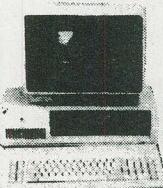
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should then provide the anti-phase phasing effect. However, the two notches will almost certainly cover different frequency spans and will not exactly complement one another. By adjusting RV2 and RV3, using a process of trial and error, it should be possible to set the range of the first filter so that it matches that of the second. This adjustment is probably most easily accomplished with RV1 adjusted to give a middle modulation frequency of about 1Hz. It is also easier if the input signal contains a wide range of frequencies (a low frequency squarewave or a noise signal for example) since the phasing effect is then most apparent. Of course, you can use any settings for these two presets that given an effect you like, and you do not have to adjust them for matched sweeps.

The sweep range of the first filter should be satisfactory, but if necessary the range of frequencies covered can be moved up by making R29 lower in value or shifted down by using a higher value resistor here.

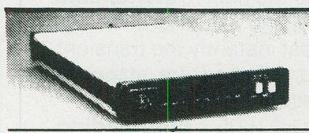
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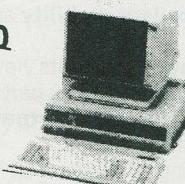


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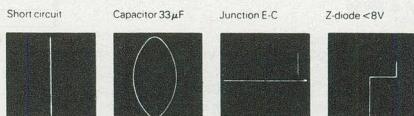
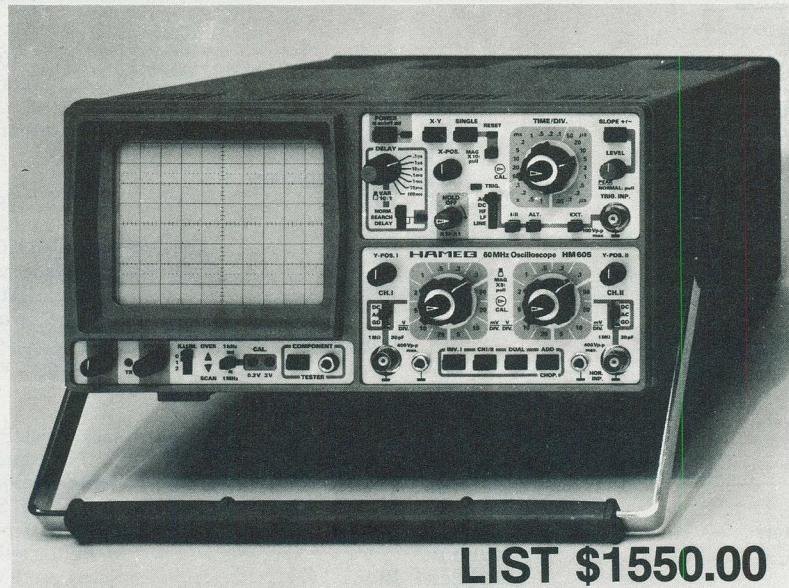
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## Crescendo Alarm

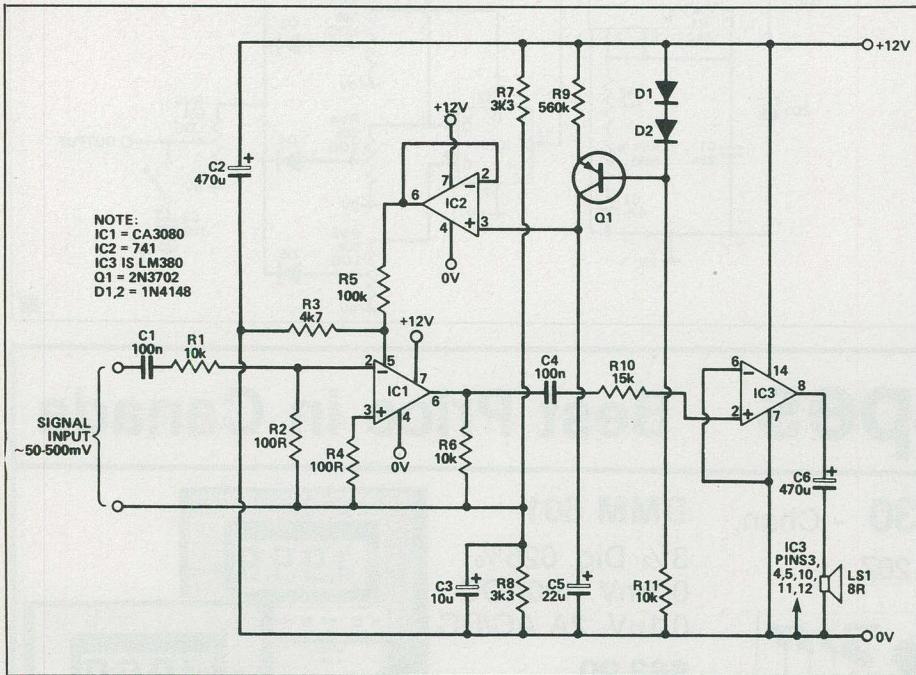
By A.N. Collinson

This circuit is designed for the benefit of those who find the start of the day a little too alarming. It can be coupled to an alarm clock or almost any other timing mechanism, and produces a output which builds up from nothing to full volume. The sleeper is thus awakened by the very minimum volume necessary.

The input can be an oscillator or almost any other audio source, such as music from a clock radio. R1 and R2 provide attenuation and the signal is then fed to IC1, a transconductance amplifier whose gain is controlled by the current entering pin 5. Q1, D1, D2 and R11 pro-

vide a constant current of about 1uA which is used to charge capacitor C5. The constant current ensures that the voltage across C5 rises linearly, full charge being reached after about 3 or 4 minutes. This voltage is passed to IC1 via IC2; R3 and R5 are included to compensate for IC2's offset. Some experimenting may be necessary with R5 if the offset causes an output when you don't want one.

The output of IC1 is coupled via R10 and C4 to the audio amplifier IC3 and then to the loudspeaker. If a power amplifier output is not required, IC3 can be deleted and the output taken from C4. Constant current source Q1 can be any small-signal PNP general purpose transistor.



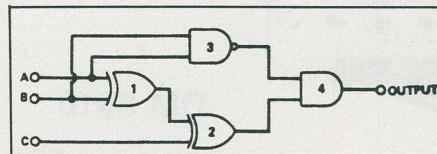
## Expanding Ex-Or Gates

By L. Robertson

Exclusive-OR gates are only obtainable in 2-input packages, and simply cascading two gates does not give the correct truth table. Any application, therefore, which requires an Ex-OR gate with three or more inputs is going to require some tricky logic combinations.

In the first circuit, inputs A and B are fed into gate 1 and the output of the gate is combined with input C at gate 2. This arrangement satisfies every part of the truth table except  $A = B = C = 1$ , where the output from gate 2 will be 1 instead of 0. To overcome this problem, inputs A and B are also fed to gate 3 so that when both are high the consequent high output from

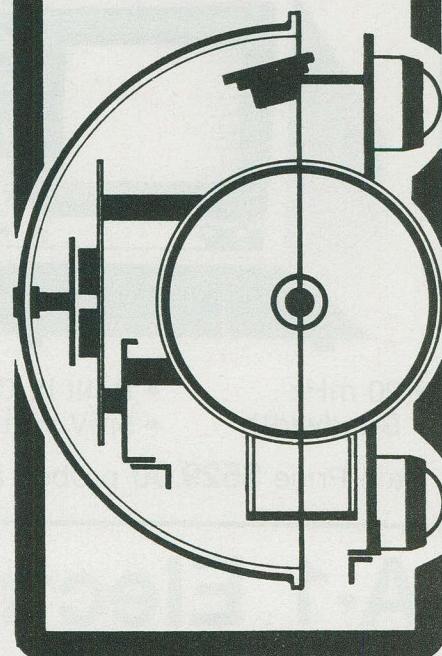
that gate will disable gate 4 and so produce a final output of 0.



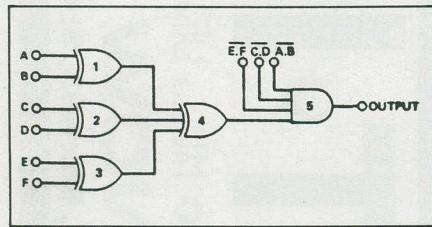
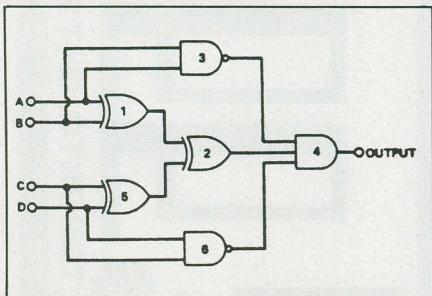
If a four-input arrangement is required, the expansion can be achieved by treating inputs C and D in the same way as inputs A and B in the first circuit. Thus, in the second circuit gate 5 performs a similar function to gate 1 and gate 6 behaves in the same way as gate 3.

The final permutation is a six-input gate, shown in simplified form in the third circuit diagram. The three-input Ex-OR gate shown as gate 4 is made up as shown

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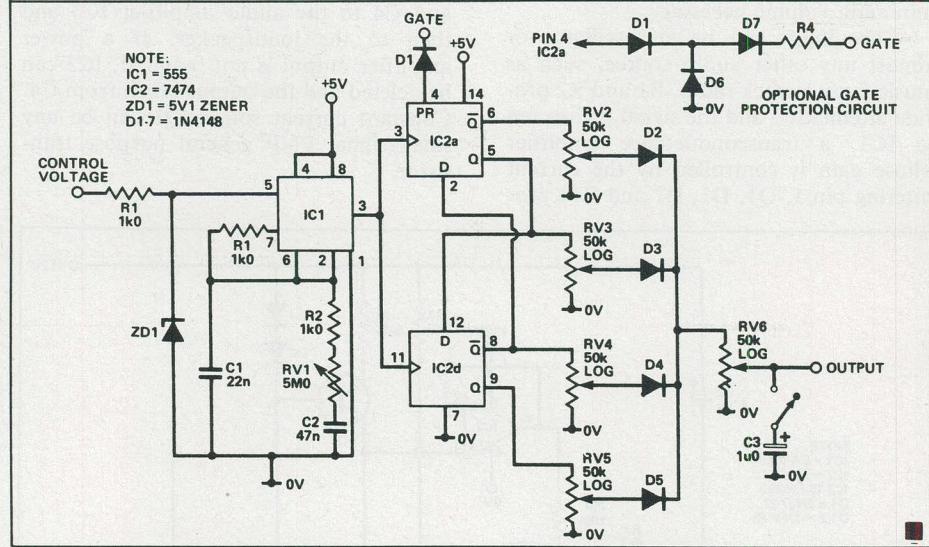
in the first diagram above and the pairs of inputs AB, CD, EF are combined in three NAND gates and fed to three of the inputs of the final AND gate.

## Walking Ring Sequencer

By S.P. Giles

This circuit is useful for generating voltages for note sequences, gating, controlling filters, etc. A 555 timer is used as a simple voltage controlled clock with the 5 meg pot determining sequence speed along with the voltage at the control

voltage input. This input will vary the speed with a positive voltage applied. This function is non-linear, has a 3:1 range and increases speed as the voltage decreases. The range is 2Hz to 7KHz; the outputs overlap so the final output is apparent as four steps. Negative inputs should not be applied without the optional gate protection circuit. The 1uF cap and switch provide a slide function.

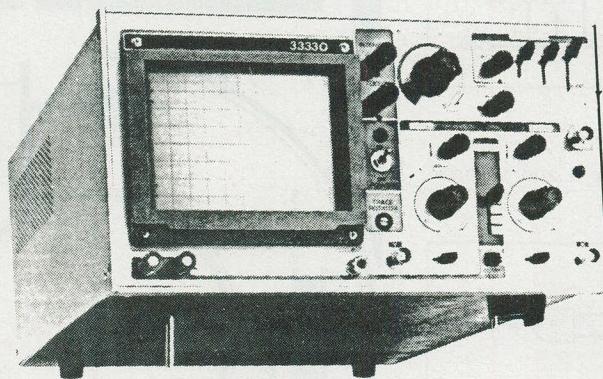


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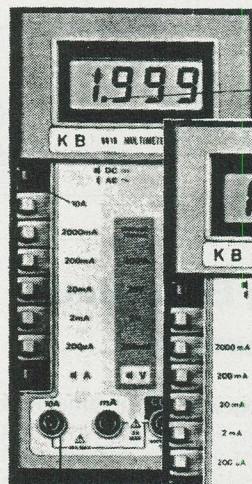
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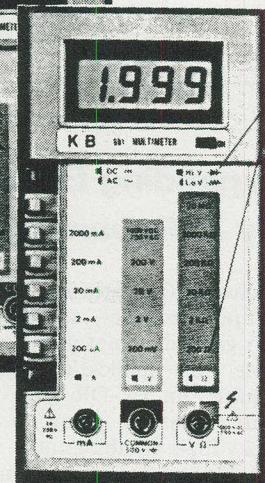
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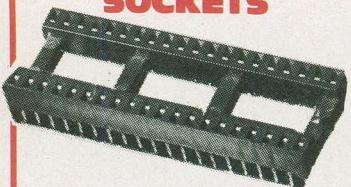
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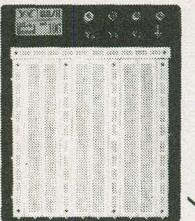
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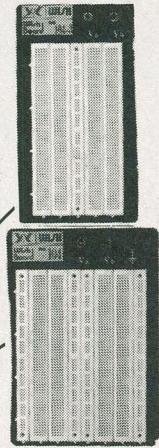
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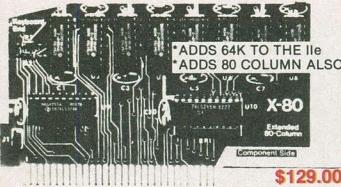


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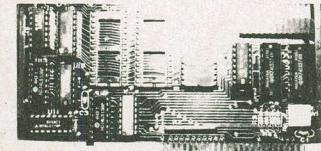


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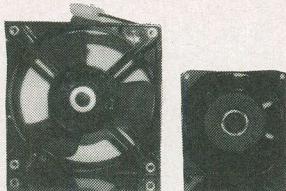
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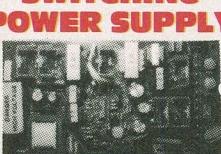
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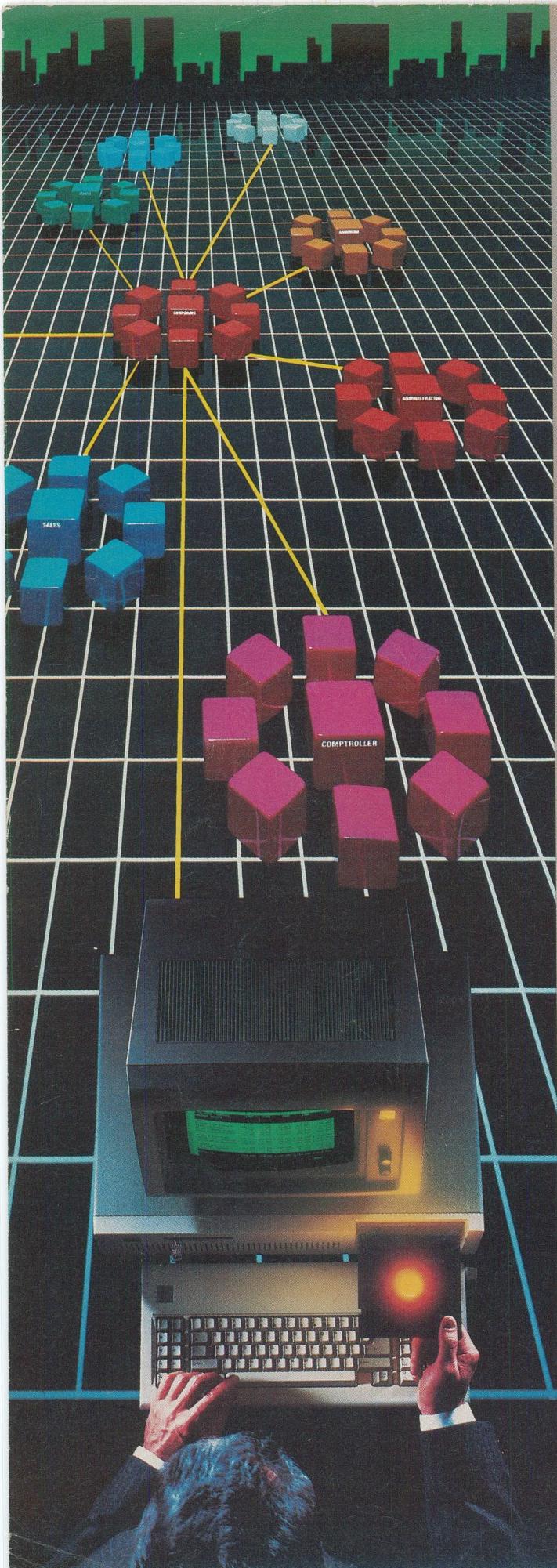


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